

N-Channel Trench Power MOSFET

General Description

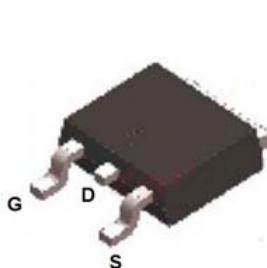
The HM1607D is N-channel MOS Field Effect Transistor designed for high current switching applications. Rugged E_{AS} capability and ultra low R_{DS(ON)} is suitable for PWM.

Features

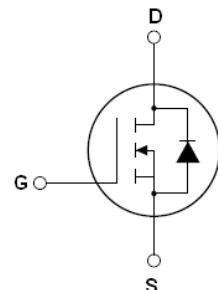
- V_{DS}=75V; I_D=150A@ V_{GS}=10V;
R_{DS(ON)}<6.3mΩ @ V_{GS}=10V
- Ultra Low On-Resistance
- High UIS and UIS 100% Test

Application

- Hard Switched and High Frequency Circuits
- Uninterruptible Power Supply



TO-263-2L Top View



Schematic Diagram

V_{DS} = 75V

I_D = 150A

R_{DS(ON)} = 5.0mΩ

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
HM1607D	HM1607D	TO-263-2L	-	-	

Table 1. Absolute Maximum Ratings (TA=25°C)

Symbol	Parameter	Value	Unit
V _{DS}	Drain-Source Voltage (V _{GS} =0V)	75	V
V _{GS}	Gate-Source Voltage (V _{DS} =0V)	±25	V
I _D (DC)	Drain Current (DC) at T _c =25°C	150	A
I _D (DC)	Drain Current (DC) at T _c =100°C	115	A
I _{DM} (pulse)	Drain Current-Continuous@ Current-Pulsed (Note 1)	600	A
P _D	Maximum Power Dissipation(T _c =25°C)	380	W
	Derating Factor	2.5	W/°C
E _{AS}	Single Pulse Avalanche Energy (Note 2)	1000	mJ
T _J , T _{STG}	Operating Junction and Storage Temperature Range	-55 To 175	°C

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature

2.E_{AS} condition:T_J=25°C,V_{DD}=50V,V_G=10V, R_G=25 Ω

Table 2. Thermal Characteristic

Symbol	Parameter	Value	Unit
R _{JC}	Thermal Resistance,Junction-to-Case	0.4	°C/W

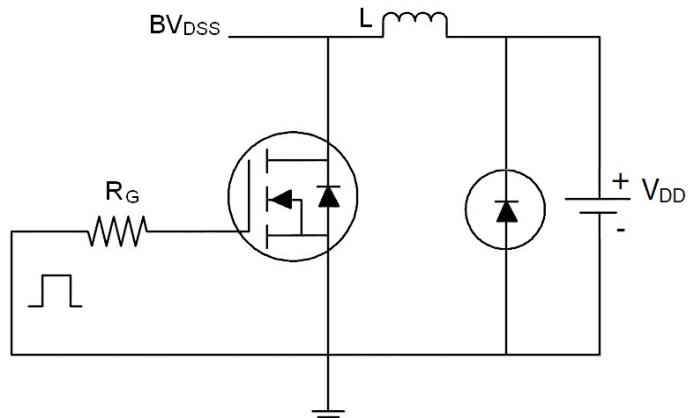
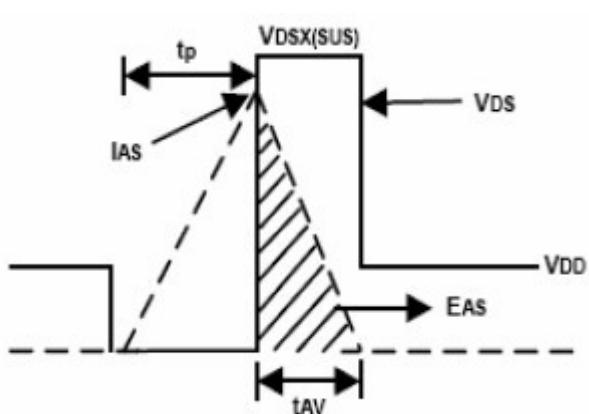
Table 3. Electrical Characteristics (TA=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =250μA	75			V
I _{DSS}	Zero Gate Voltage Drain Current(Tc=25°C)	V _{DS} =100V, V _{GS} =0V			1	μA
I _{DSS}	Zero Gate Voltage Drain Current(Tc=125°C)	V _{DS} =100V, V _{GS} =0V			1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	2		4	V
R _{DSON}	Drain-Source On-State Resistance	V _{GS} =10V, I _D =40A		5.0	6.3	mΩ
Dynamic Characteristics						
g _{FS}	Forward Transconductance	V _{DS} =50V, I _D =40A	30			S
C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0V f=1.0MHz		8184		PF
C _{oss}	Output Capacitance			955		PF
C _{rss}	Reverse Transfer Capacitance			463		PF
Q _g	Total Gate Charge	V _{DS} =44V, I _D =40A V _{GS} =10V		158		nC
Q _{gs}	Gate-Source Charge			29		nC
Q _{gd}	Gate-Drain Charge			42		nC
Switching Times						
t _{d(on)}	Turn-on Delay Time	V _{DD} =65V, I _D =40A, R _L =15Ω V _{GS} =10V, R _G =2.5Ω		25		nS
t _r	Turn-on Rise Time			29		nS
t _{d(off)}	Turn-Off Delay Time			53		nS
t _f	Turn-Off Fall Time			63		nS
Source-Drain Diode Characteristics						
I _{SD}	Source-Drain Current(Body Diode)			150		A
I _{SDM}	Pulsed Source-Drain Current(Body Diode)			600		A
V _{SD}	Forward On Voltage ^(Note 1)	T _J =25°C, I _{SD} =40A, V _{GS} =0V		0.8	1	V
t _{rr}	Reverse Recovery Time ^(Note 1)	T _J =25°C, I _F =40A di/dt=100A/μs		95		nS
Q _{rr}	Reverse Recovery Charge ^(Note 1)			189		nC
t _{on}	Forward Turn-on Time	Intrinsic turn-on time is negligible(turn-on is dominated by L _S +L _D)				

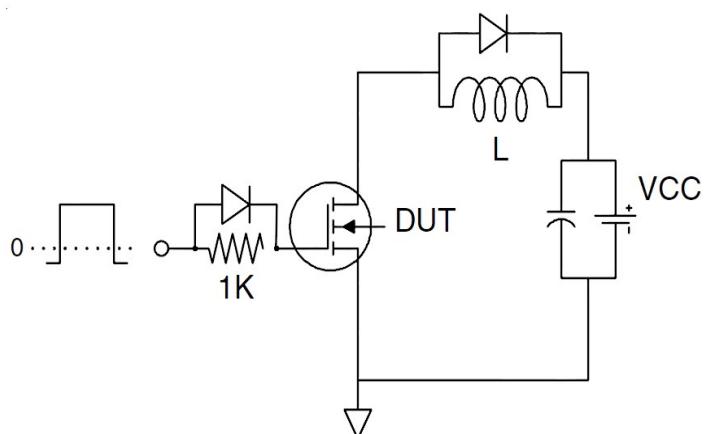
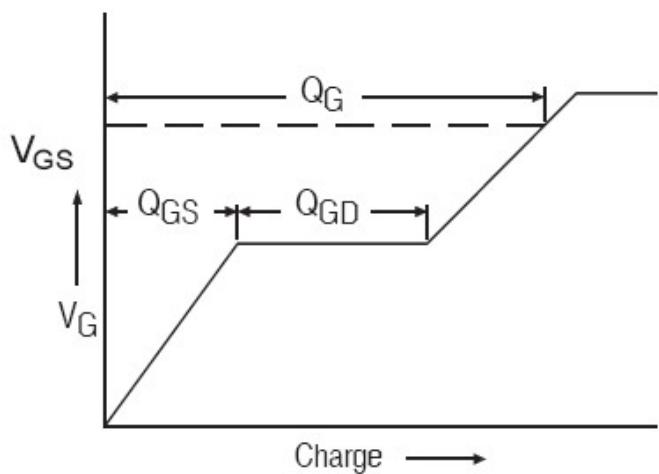
Notes 1.Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 1.5%, R_G=25Ω, Starting T_J=25 °C

Test Circuit

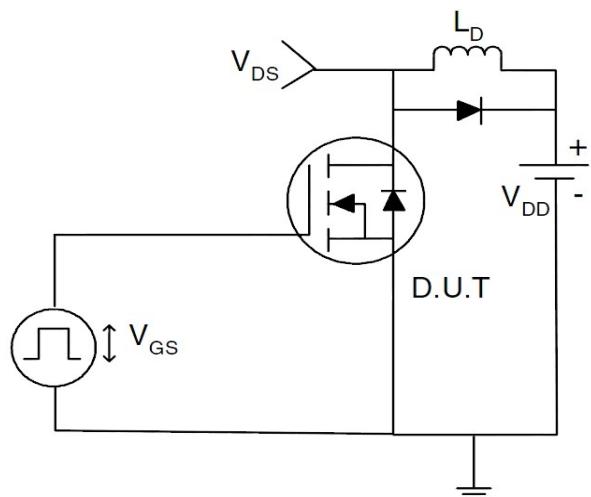
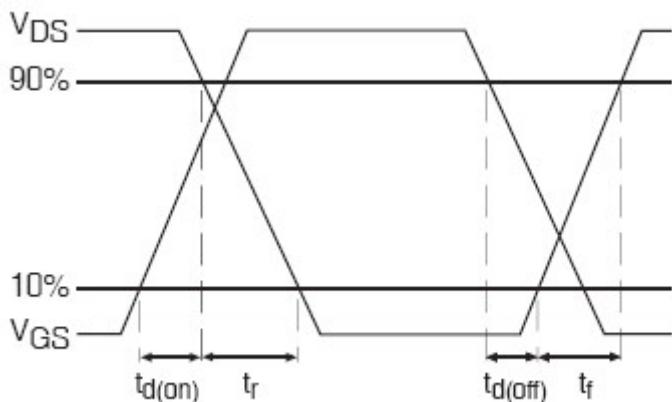
1) E_{AS} Test Circuits



2) Gate Charge Test Circuit:



3) Switch Time Test Circuit:



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (Curves)

Figure1. Output Characteristics

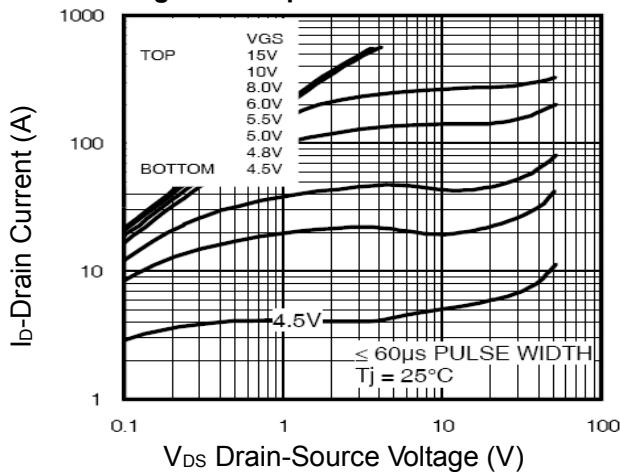


Figure2. Transfer Characteristics

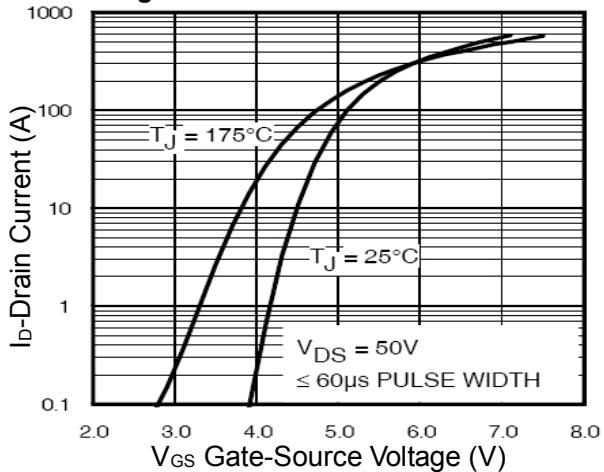


Figure3. $R_{DS(ON)}$ - Drain Current

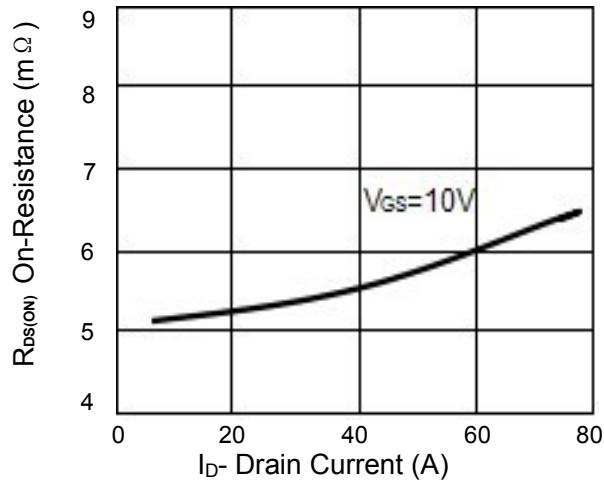


Figure4. $R_{DS(ON)}$ - Junction Temperature

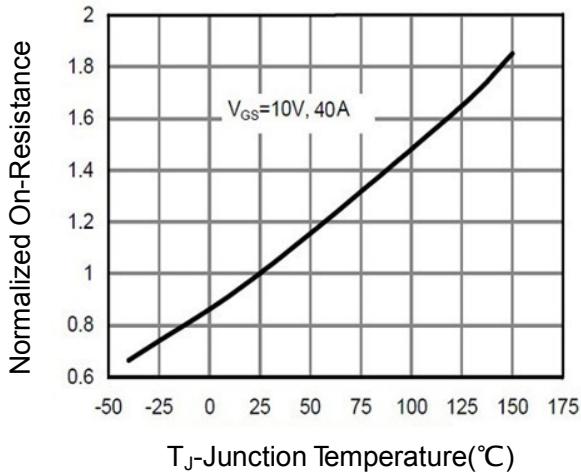


Figure5. Gate Charge

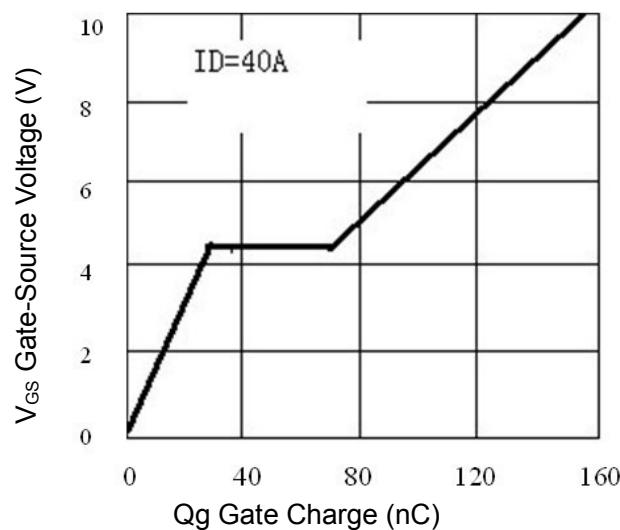


Figure6. Source- Drain Diode Forward

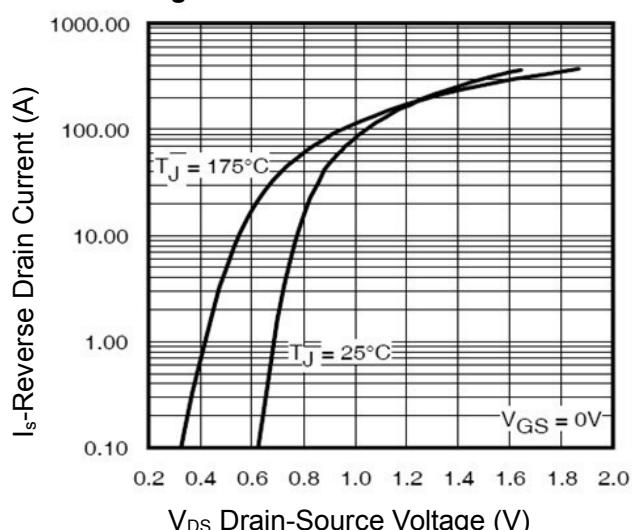


Figure7. Capacitance vs V_{DS}

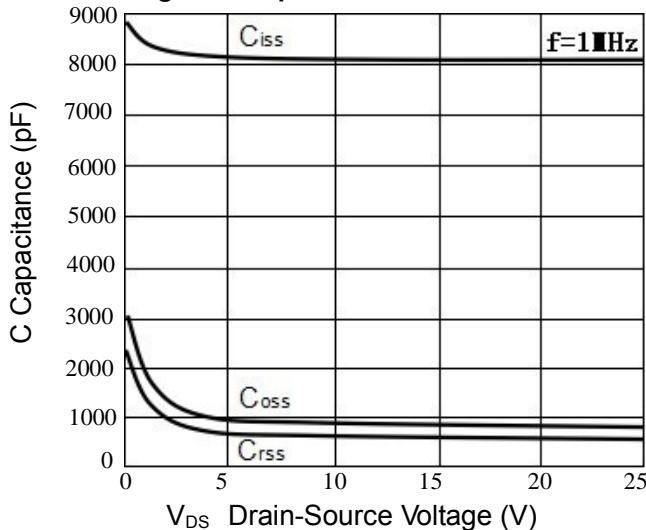


Figure8. Safe Operation Area

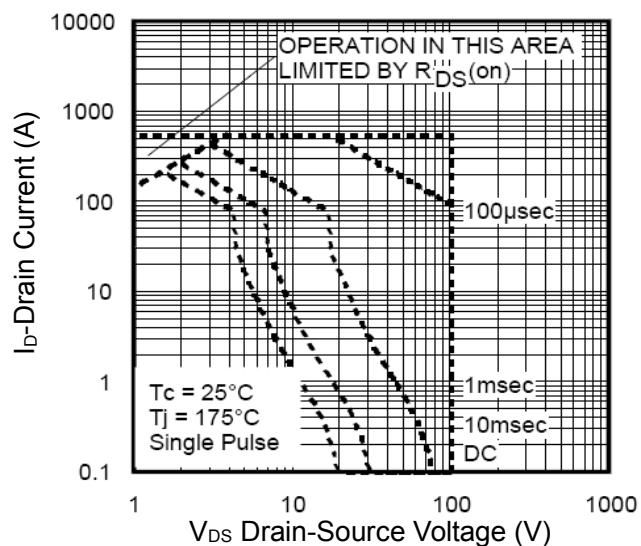


Figure9. BV_{DSS} vs Junction Temperature

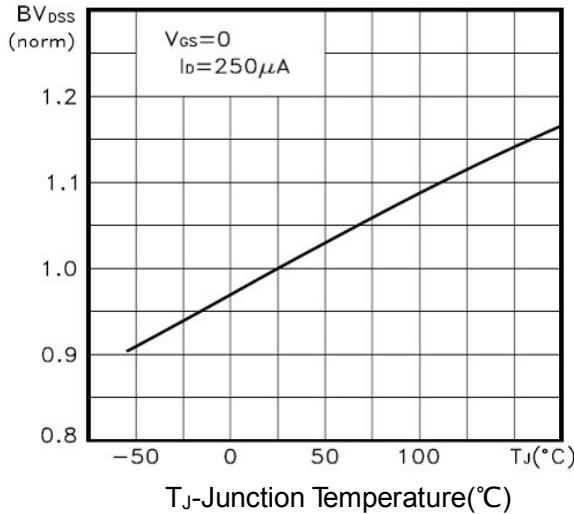


Figure10. V_{GS(th)} vs Junction Temperature

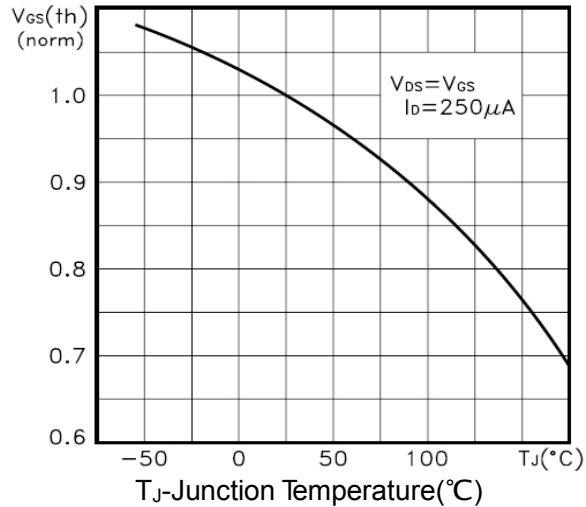
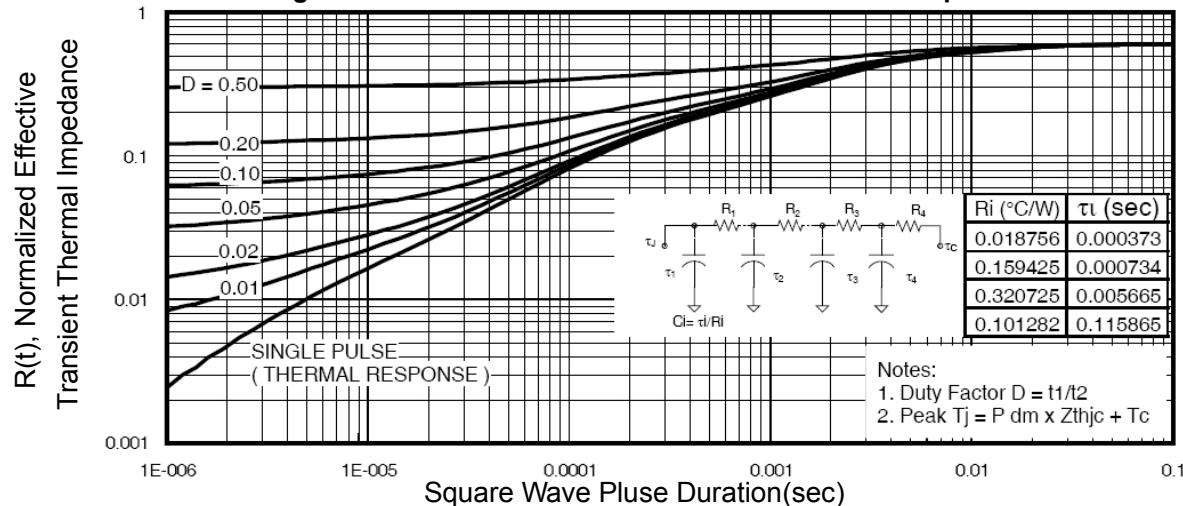
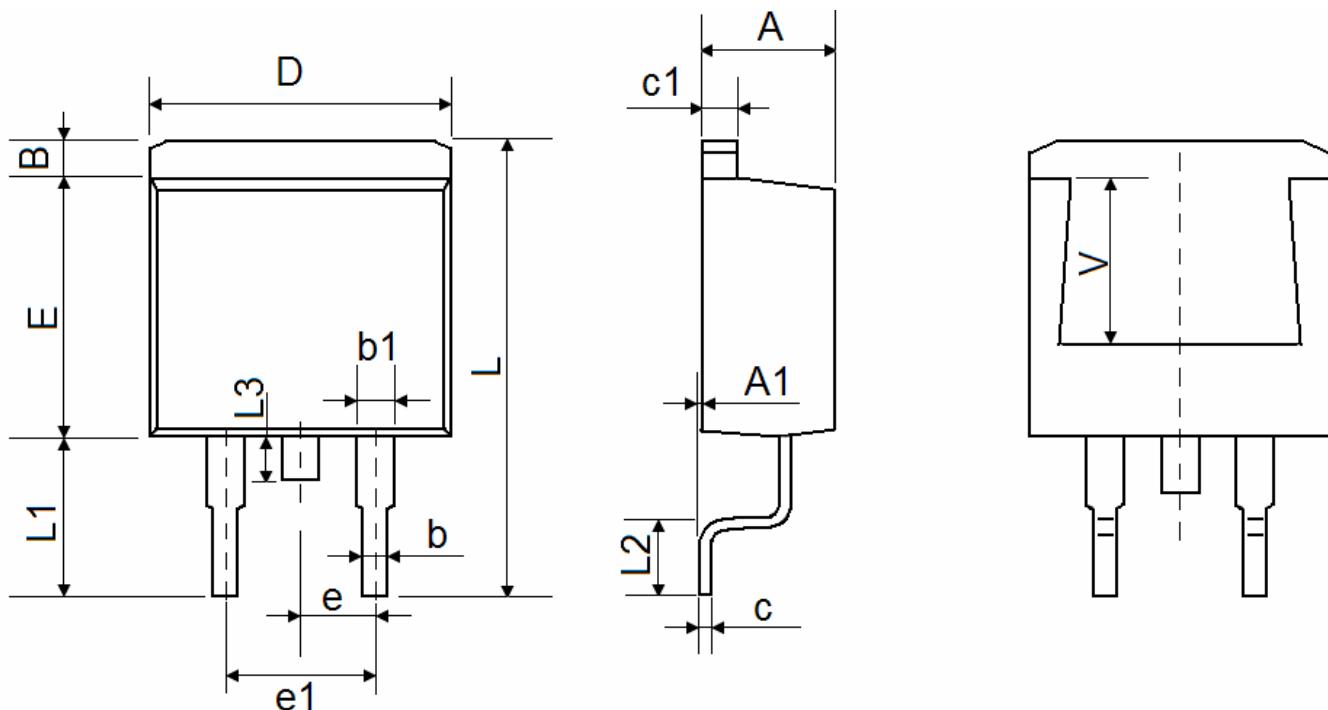


Figure11. Normalized Maximum Transient Thermal Impedance



TO-263-2L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.470	4.670	0.176	0.184
A1	0.000	0.150	0.000	0.006
B	1.170	1.370	0.046	0.054
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.310	0.530	0.012	0.021
c1	1.170	1.370	0.046	0.054
D	10.010	10.310	0.394	0.406
E	8.500	8.900	0.335	0.350
e	2.540 TYP.		0.100 TYP.	
e1	4.980	5.180	0.196	0.204
L	15.050	15.450	0.593	0.608
L1	5.080	5.480	0.200	0.216
L2	2.340	2.740	0.092	0.108
L3	1.300	1.700	0.051	0.067
V	5.600 REF		0.220 REF	