

## P-Channel Enhancement Mode Power MOSFET

### DESCRIPTION

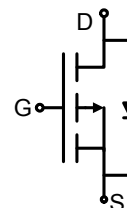
The HM2341 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications.

### GENERAL FEATURES

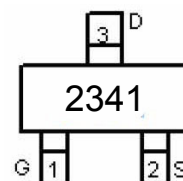
- $V_{DS} = -30V, I_D = -4.2A$   
 $R_{DS(ON)} < 130m\Omega @ V_{GS} = -2.5V$   
 $R_{DS(ON)} < 75m\Omega @ V_{GS} = -4.5V$   
 $R_{DS(ON)} < 65m\Omega @ V_{GS} = -10V$
- High Power and current handling capability
- Lead free product is acquired
- Surface Mount Package

### Application

- PWM applications
- Load switch
- Power management



Schematic diagram



Marking and pin Assignment



SOT-23-3L top view

### Package Marking And Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
2341	HM2341	SOT-23-3L	Ø180mm	8 mm	3000 units

### Absolute Maximum Ratings (TA=25°C unless otherwise noted)

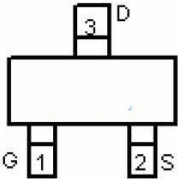
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	±12	V
Drain Current-Continuous	$I_D$	-4.2	A
Drain Current-Pulsed (Note 1)	$I_{DM}$	-30	A
Maximum Power Dissipation	$P_D$	1.2	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	°C

### Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	104	°C/W
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### Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=-250\mu A$	-30		-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-24V, V_{GS}=0V$	-	-	-1	μA



Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±12V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =-250μA	-0.7	-1	-1.3	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-4.2A	-	50	55	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-4A	-	64	72	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-1A		95	120	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =-5V,I <sub>D</sub> =-4.2A	-	10	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-15V,V <sub>GS</sub> =0V, F=1.0MHz	-	950	-	PF
Output Capacitance	C <sub>oss</sub>		-	115	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	75	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =-15V,I <sub>D</sub> =-3.2A V <sub>GS</sub> =-10V,R <sub>GEN</sub> =6Ω	-	7	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	3	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	30	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	12	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-15V,I <sub>D</sub> =-4A,V <sub>GS</sub> =-4.5V	-	9.5	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	2	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	3	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =-1A	-	-	-1.2	V

## Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production

## TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

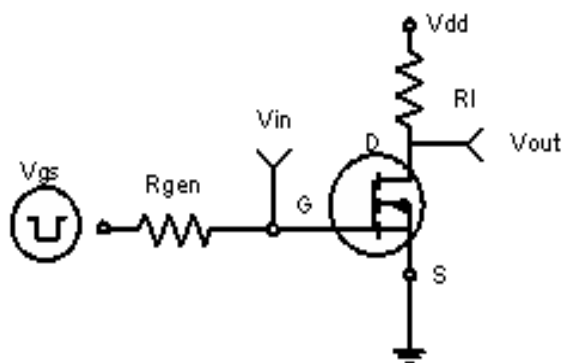


Figure 1: Switching Test Circuit

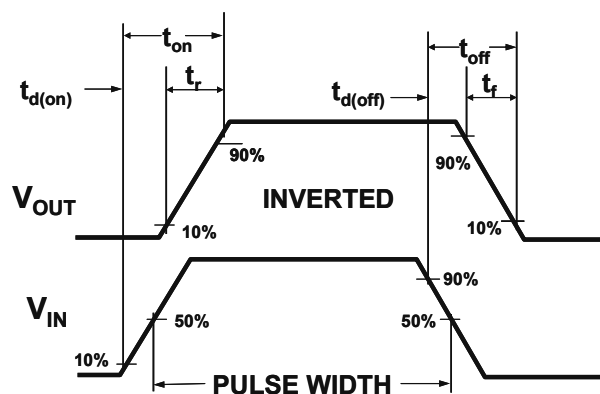


Figure 2: Switching Waveforms

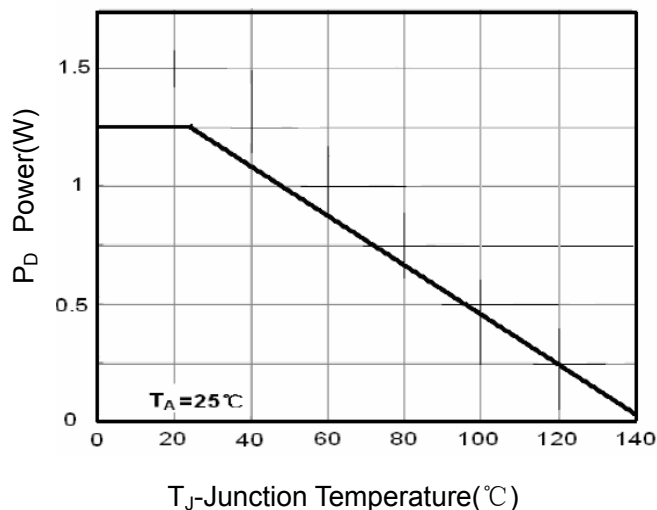


Figure 3 Power Dissipation

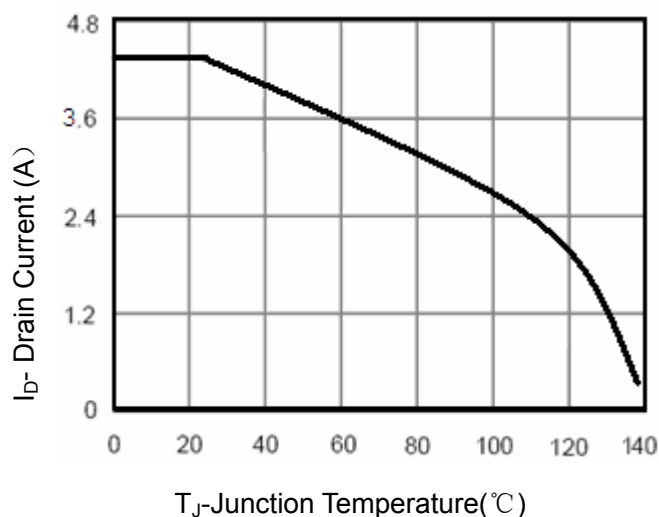


Figure 4 Drain Current

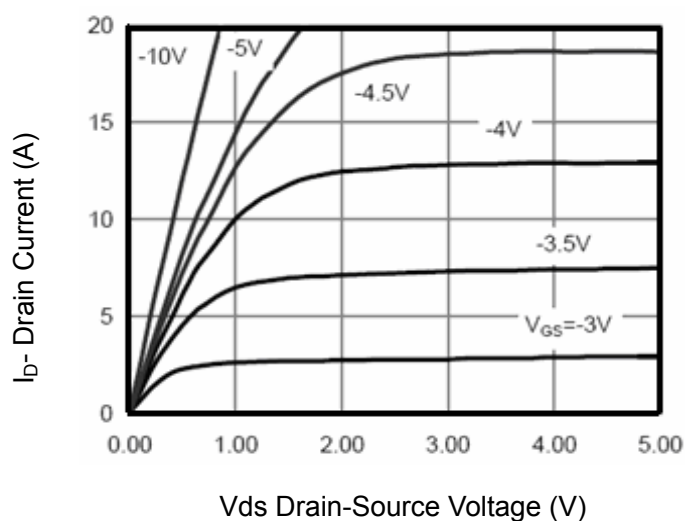


Figure 5 Output CHARACTERISTICS

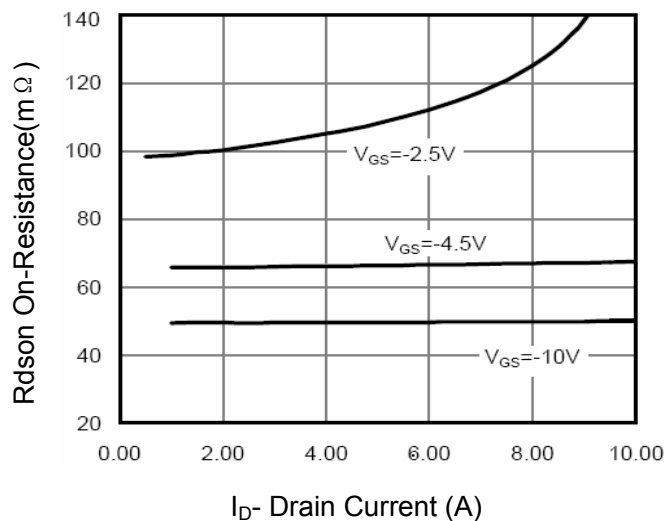
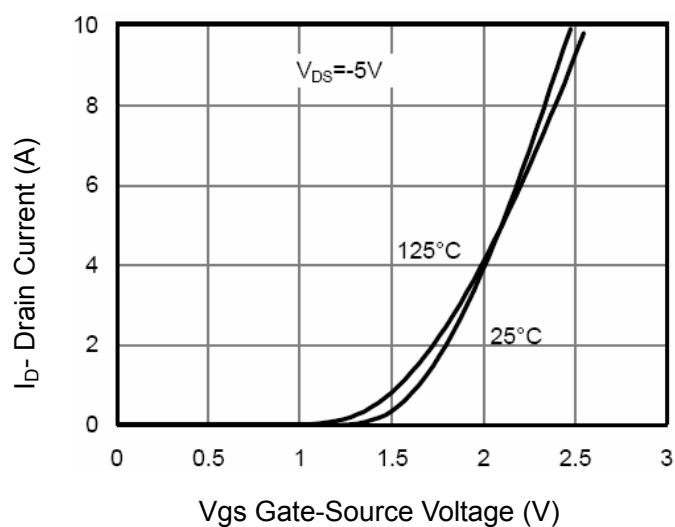
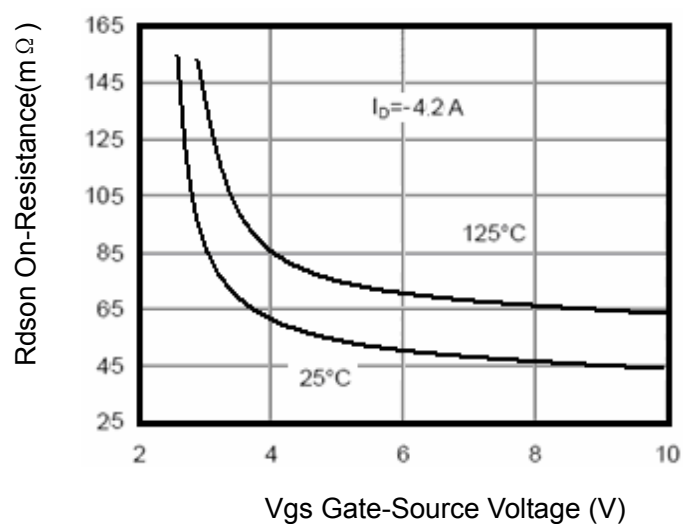


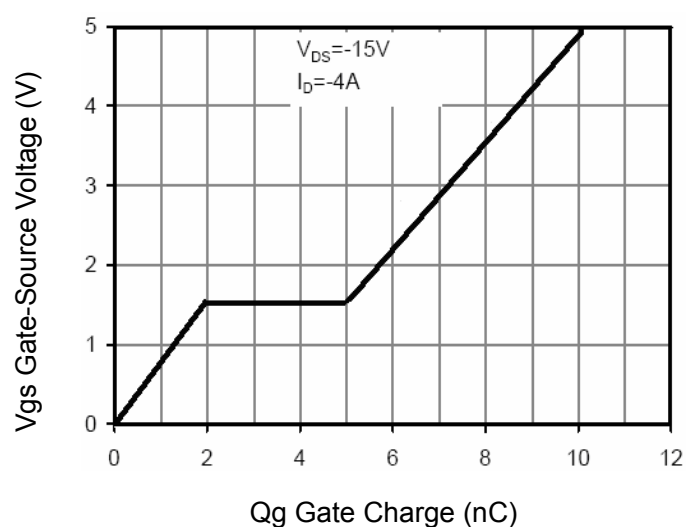
Figure 6 Drain-Source On-Resistance



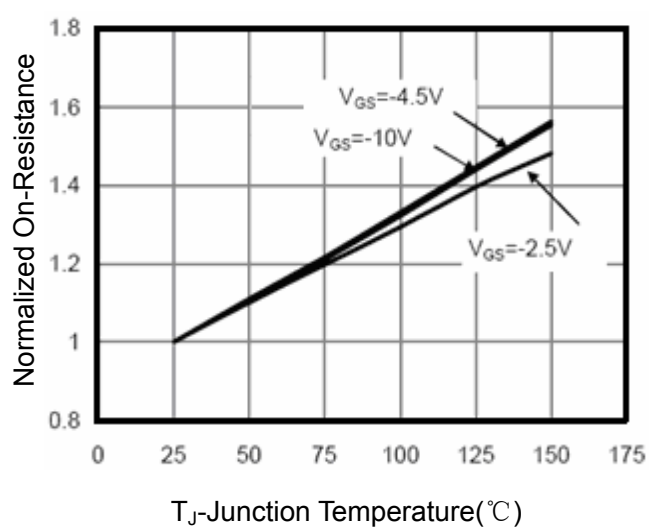
**Figure 7 Transfer Characteristics**



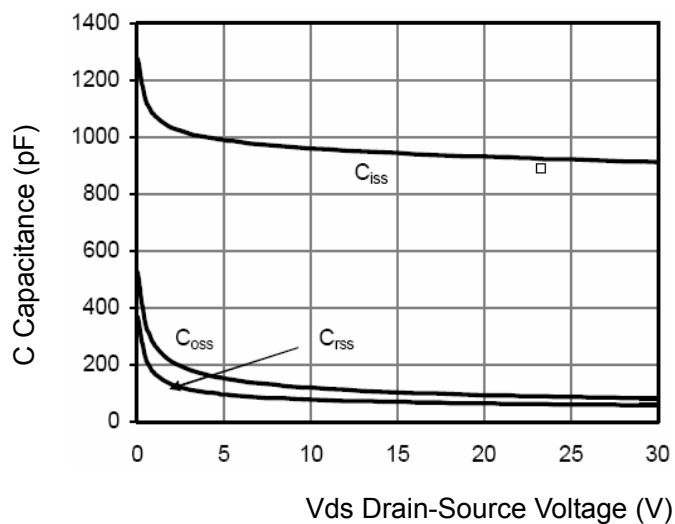
**Figure 9 Rdson vs Vgs**



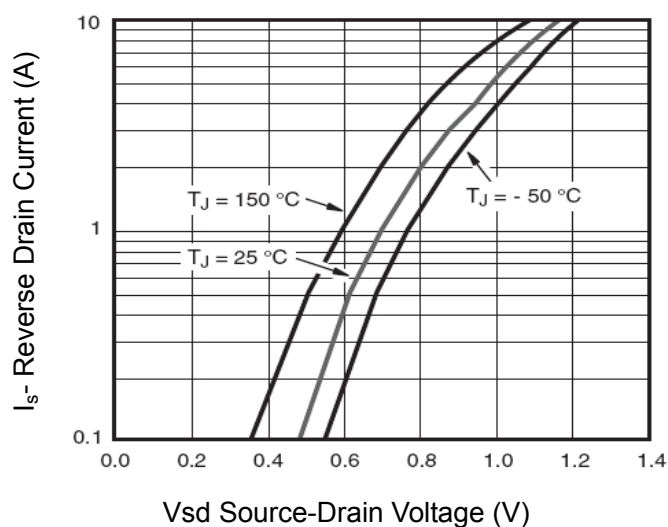
**Figure 11 Gate Charge**



**Figure 8 Drain-Source On-Resistance**



**Figure 10 Capacitance vs Vds**



**Figure 12 Source- Drain Diode Forward**

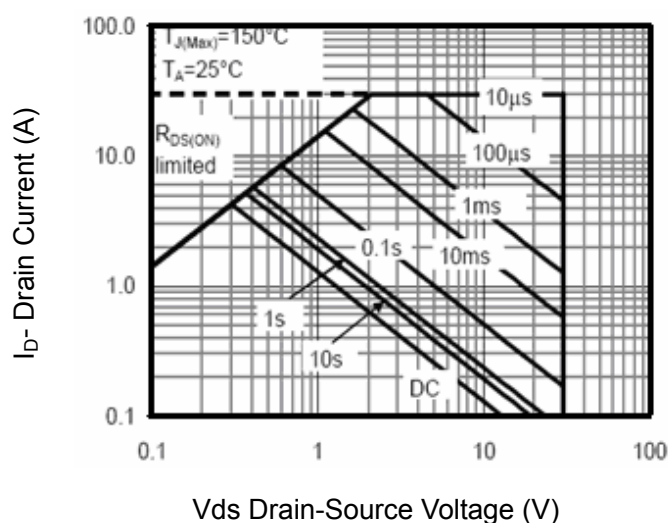


Figure 13 Safe Operation Area

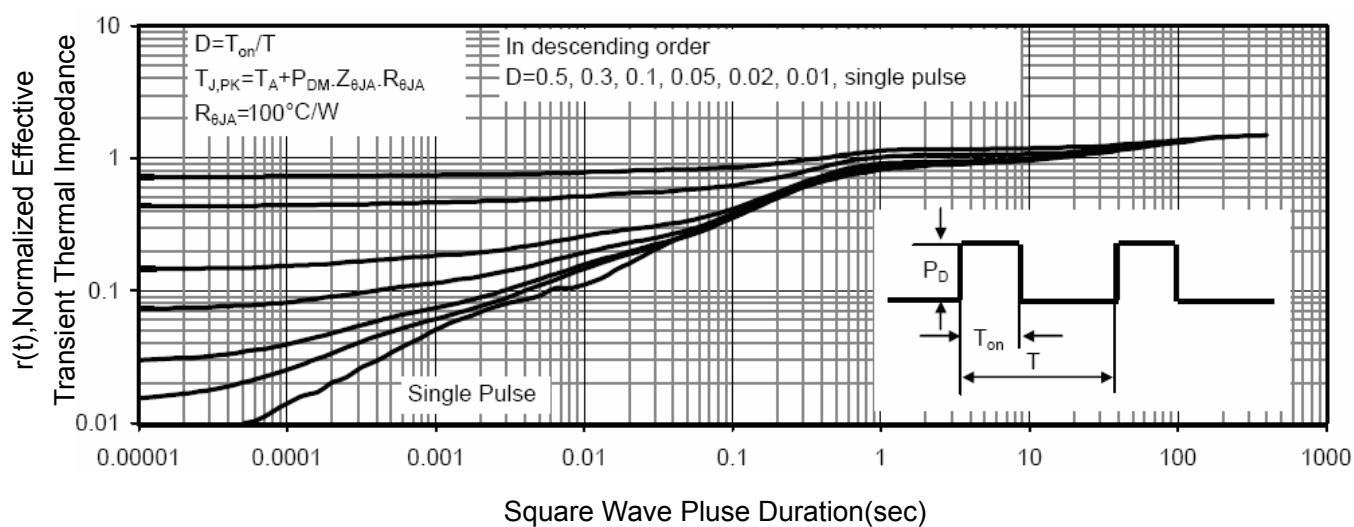
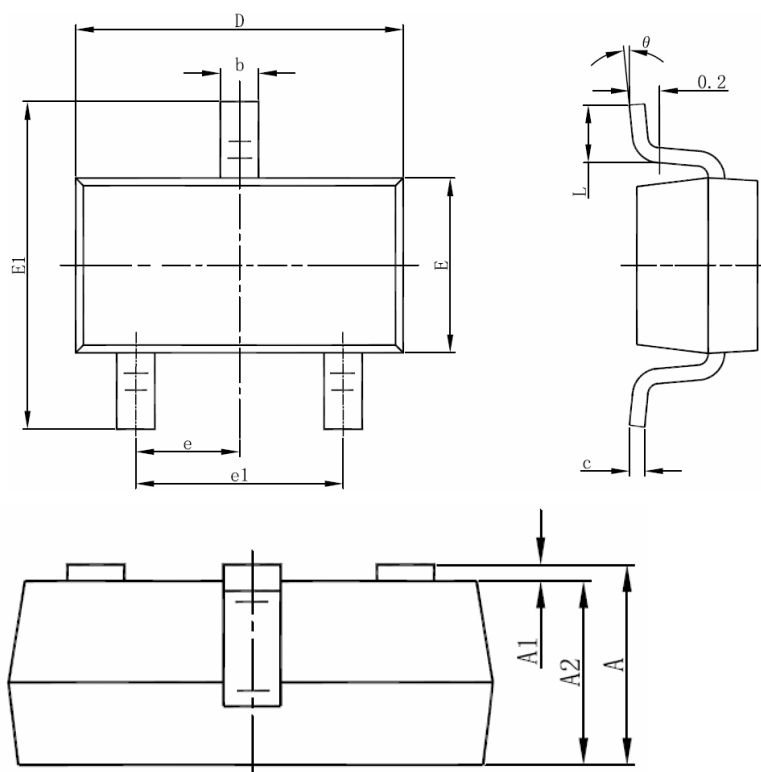


Figure 14 Normalized Maximum Transient Thermal Impedance

## SOT-23-3L PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

### NOTES

1. All dimensions are in millimeters.
2. Tolerance  $\pm 0.10\text{mm}$  (4 mil) unless otherwise specified
3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
4. Dimension L is measured in gauge plane.
5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

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