

## N-Channel Enhancement Mode Power MOSFET

# **Description**

The HM4412 uses advanced trench technology to provide excellent  $R_{DS(ON)}$  and low gate charge. This device is suitable for use as a load switch and PWM applications.

#### **Genera Features**

•  $V_{DS} = 30V, I_D = 7.0A$ 

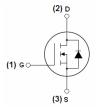
 $R_{DS(ON)}$  < 31m $\Omega$  @  $V_{GS}$ =10V

 $R_{DS(ON)}$  < 43m $\Omega$  @  $V_{GS}$ =4.5V

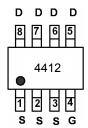
- High Power and current handing capability
- Lead free product is acquired
- Surface mount package

# **Application**

- Load switch
- ●PWM application



#### Schematic diagram



#### Marking and pin Assignment



SOP-8 top view

# **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
4412	HM4412	SOP8	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (T<sub>A</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	30	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Drain Current-Continuous	I <sub>D</sub>	7.0	А
Drain Current-Pulsed (Note 1)	I <sub>DM</sub>	28	Α
Maximum Power Dissipation	P <sub>D</sub>	2.5	W
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 150	°C

### **Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	89	°C/W
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## Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	30	33	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V	-	-	1	μA

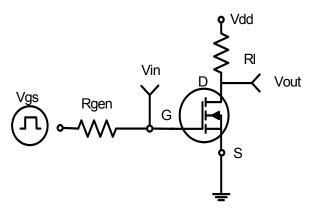


Parameter	Symbol	Condition	Min	Тур	Max	Unit
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS},I_{D}=250\mu A$	1.0	1.9	3.0	V
Drain-Source On-State Resistance	D.	V <sub>GS</sub> =10V, I <sub>D</sub> =5A	-	25.5	31	mΩ
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A	-	34	43	mΩ
Forward Transconductance	<b>g</b> FS	$V_{DS}$ =5 $V$ , $I_{D}$ =5 $A$	-	15	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	\/ -45\/\/ -0\/	-	255	-	PF
Output Capacitance	Coss	$V_{DS}$ =15 $V$ , $V_{GS}$ =0 $V$ , F=1.0MHz	-	45	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.UIVITIZ	-	35	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	4.5	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =15V, $R_L$ =3 $\Omega$	-	2.5	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10 $V$ , $R_{GEN}$ =3 $\Omega$	-	14.5	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	3.5	-	nS
Total Gate Charge	Qg	\/ 45\/  54	-	5.2	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}=15V,I_{D}=5A,$	-	0.85	-	nC
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> =10V	-	1.3	-	nC
Drain-Source Diode Characteristics	, ,		•			•
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =5A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	5	Α

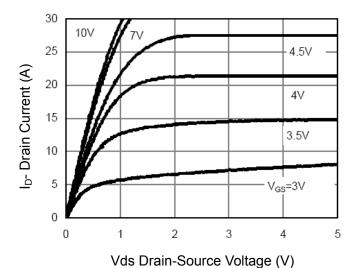
## Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production

# **Typical Electrical and Thermal Characteristics**



**Figure 1:Switching Test Circuit** 



**Figure 3 Output Characteristics** 

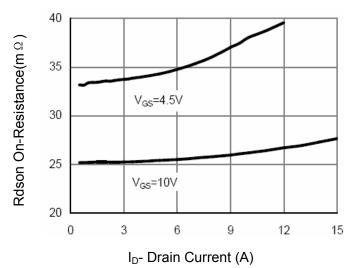


Figure 5 Drain-Source On-Resistance

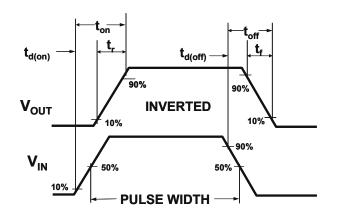
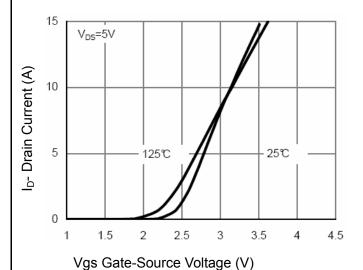


Figure 2:Switching Waveforms



**Figure 4 Transfer Characteristics** 

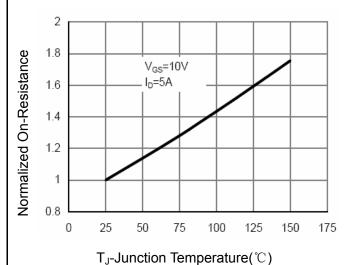
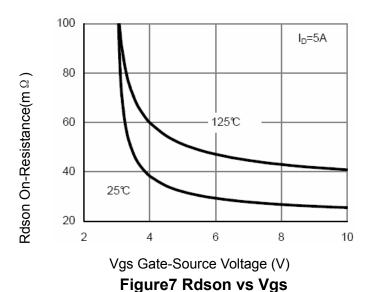
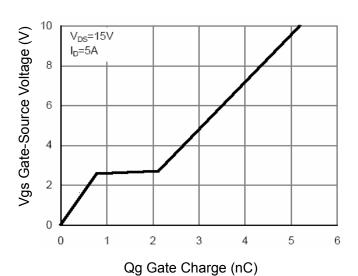


Figure 6 Drain-Source On-Resistance





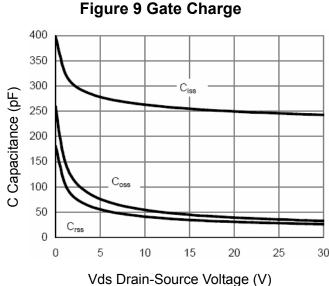


Figure 11 Capacitance vs Vds

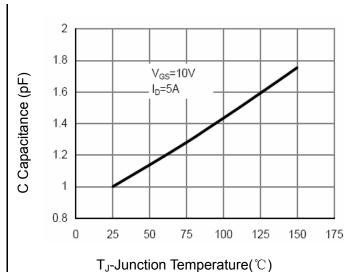


Figure 8 Drain-Source On-Resistance

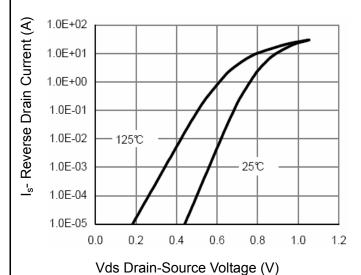
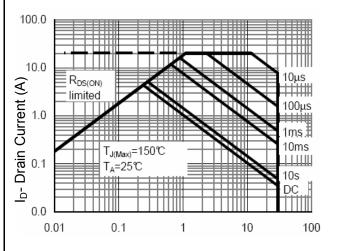
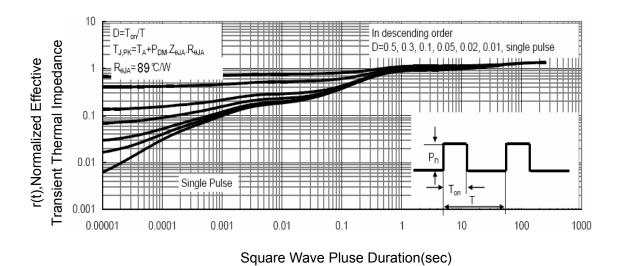


Figure 10 Source- Drain Diode Forward



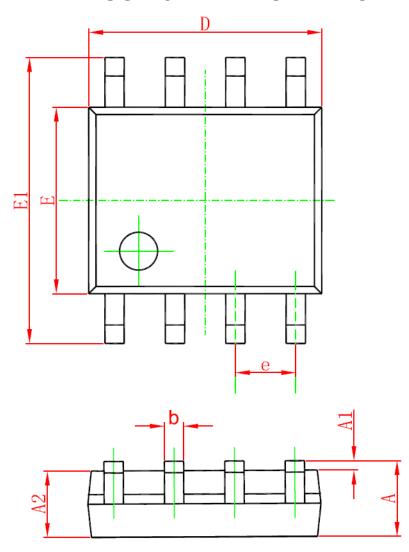
Vds Drain-Source Voltage (V)

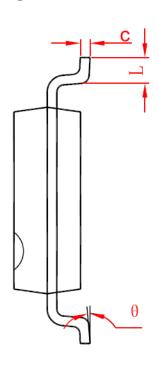
Figure 12 Safe Operation Area



**Figure 13 Normalized Maximum Transient Thermal Impedance** 

# **SOP-8 PACKAGE IN FORMATION**





Combal	Dimensions Ir	n Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
A	1. 350	1. 750	0. 053	0. 069	
A1	0. 100	0. 250	0.004	0. 010	
A2	1. 350	1. 550	0.053	0. 061	
b	0. 330	0. 510	0. 013	0. 020	
С	0. 170	0. 250	0.006	0. 010	
D	4. 700	5. 100	0. 185	0. 200	
Е	3. 800	4. 000	0. 150	0. 157	
E1	5. 800	6. 200	0. 228	0. 244	
е	1. 270 (BSC)		0. 050 (BSC)		
L	0. 400	1. 270	0. 016	0. 050	
θ	0°	8°	0°	8°	

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