

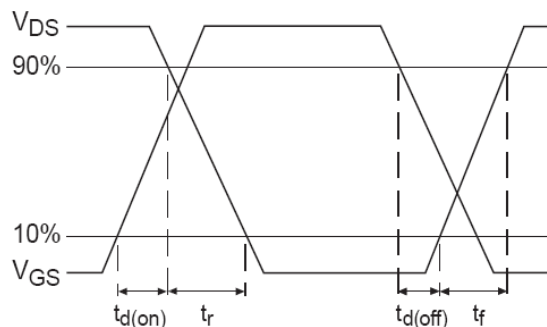
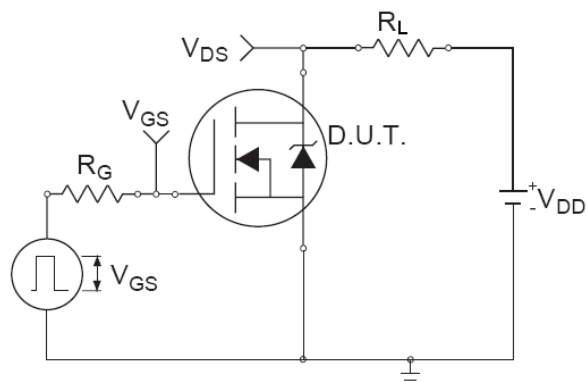
Features

- V_{DS}=100V/V_{GS}=±20V/I_D=3.5A
- R_{DS(ON)}=105mΩ(Max.)@V_{GS}=10V
- R_{DS(ON)}=175mΩ(Max.)@V_{GS}=4.5V
- ESD protect
- Reliable and Rugged
- High Density Cell Design For Ultra Low On-Resistance

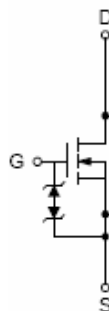
Applications

- Synchronous Rectification
- Power Management in Inverter System

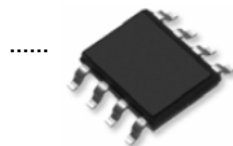
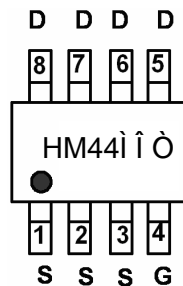
Switching Time Test Circuit and Waveforms



Pin Description



Marking and pin Assignment



SOP-8 top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
HM4486	HM4486E	SOP-8	-	-	-

Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

Symbol	Parameter	Typical	Unit
V _{DSS}	Drain-Source Voltage	100	V
V _{GSS}	Gate –Source Voltage	±20	V
I _D ¹	Continuous Drain Current	T _C =70°C 2.8 3.5	A A
I _{DM} ¹	300us Pulsed Drain Current Tested	T _C =25°C 14	A
I _S ¹	Diode Continuous Forward Current	3	A
E _{AS} ²	Avalanche Energy, Single Plused(L=0.3mH)	30	mJ
T _J	Operating Junction Temperature	150	°C
T _{STG}	Storage Temperature Range	-55 ~ 150	°C

Note: 1: Surface Mounted on 1in² pad area, t ≤ 10sec..

2: UIS tested and pluse width limited by maximum junction temperature 150°C (initial temperature T_J=25°C).

Electrical Characteristics (T_A=25°C unless otherwise noted)

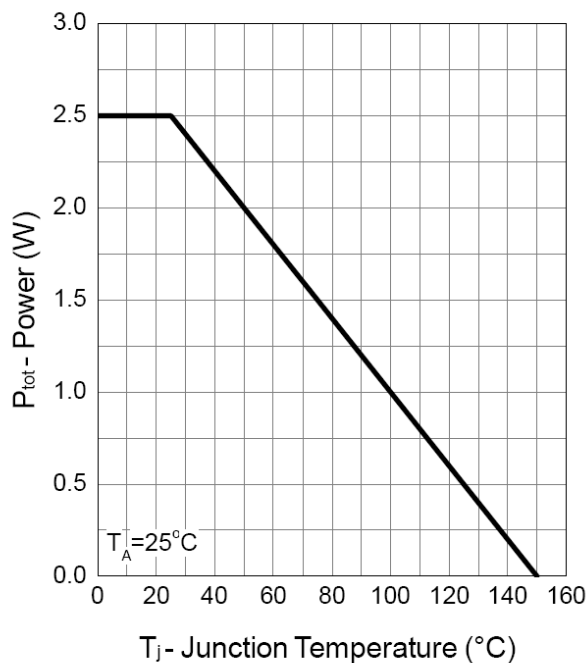
Symbol	Parameter	Test Conditions	Min.	Typ	Max.	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	100			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-80V, V _{GS} =0V T _J =85°C			1 30	uA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250uA	1.5	2	2.5	V
I _{GSS}	Gate Leakage Current	V _{GS} =±16V, V _{DS} =0V			±10	nA
R _{DS(on)} ¹	Drain-Source On-Resistance	V _{GS} =10V, I _D =3.5A V _{GS} =4.5V, I _D =2A		85 135	105 175	mΩ
Diode Characteristics						
V _{SD} ¹	Diode Forward Voltage	I _{SD} =3A, V _{GS} =0V	0.6	0.8	1.1	V
t _{rr}	Reverse Recovery Time	I _{SD} =3.5A,		44		ns
Q _{rr}	Reverse Recovery Charge	dI _{SD} /dt=100A/us		80		nC
Dynamic Characteristics ²						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =30V Frequency=1MHz		940		pF
C _{oss}	Output Capacitance			80		
C _{rss}	Reverse Transfer Capacitance			50		
t _{d(on)}	Turn-On Delay Time	V _{DD} =30V, R _L =30Ω I _D =1A, V _{GEN} =10V R _G =6Ω		13	24	ns
t _r	Turn-On Rise Time			10	19	
t _{d(off)}	Turn-Off Delay Time			32	60	
t _f	Turn-Off Fall Time			16	30	
Gate Charge Characteristics ²						
Q _g	Total Gate Charge	V _{DS} =50V, V _{GS} =10V I _D =3.5A		21		nC
Q _{gs}	Gate-Source Charge			4.9		
Q _{gd}	Gate-Drain Charge			5.8		

Note: 1: Pulse test ; pulse width ≤ 300ns, duty cycle ≤ 2%.

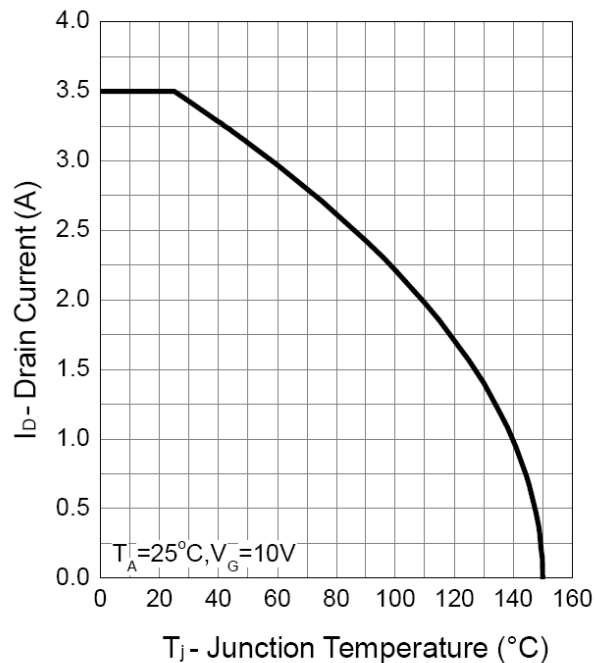
2: Guaranteed by design, not subject to production testing.

Typical Characteristics

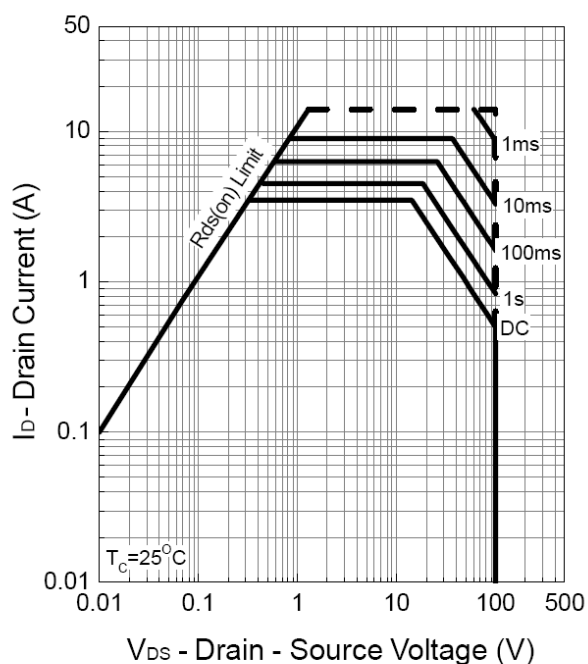
Power Dissipation



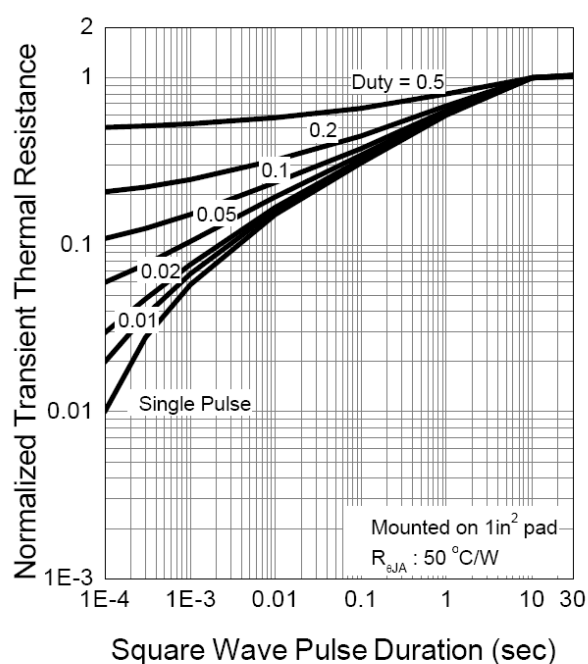
Drain Current



Safe Operation Area

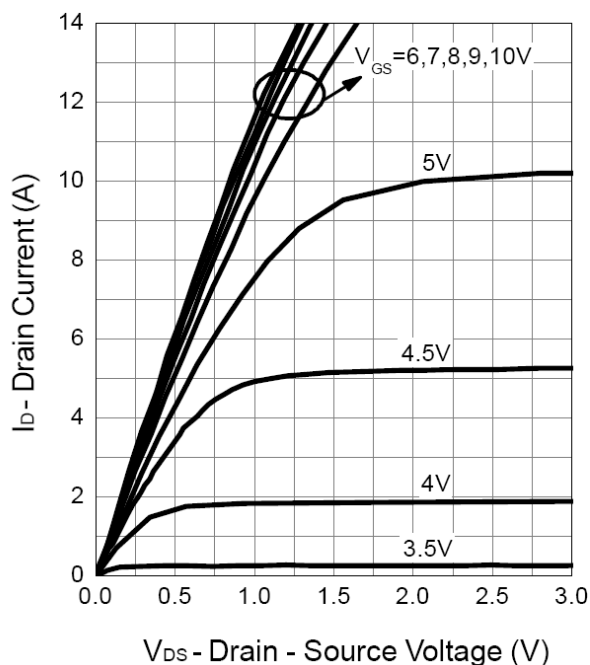


Thermal Transient Impedance

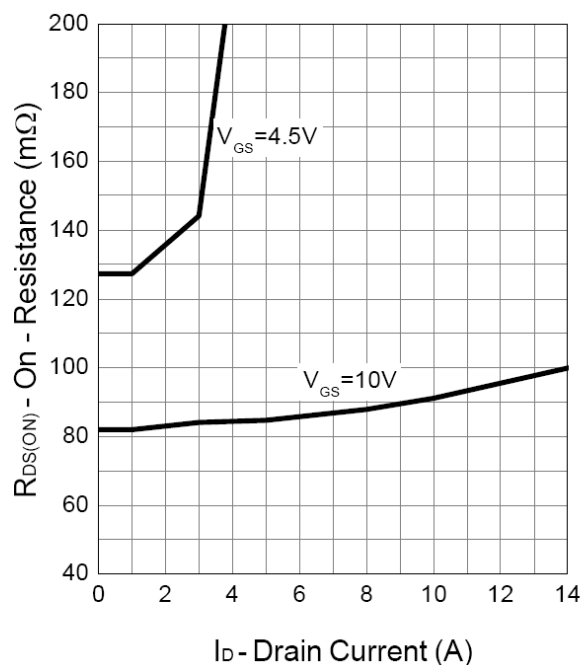


Typical Characteristics (Cont.)

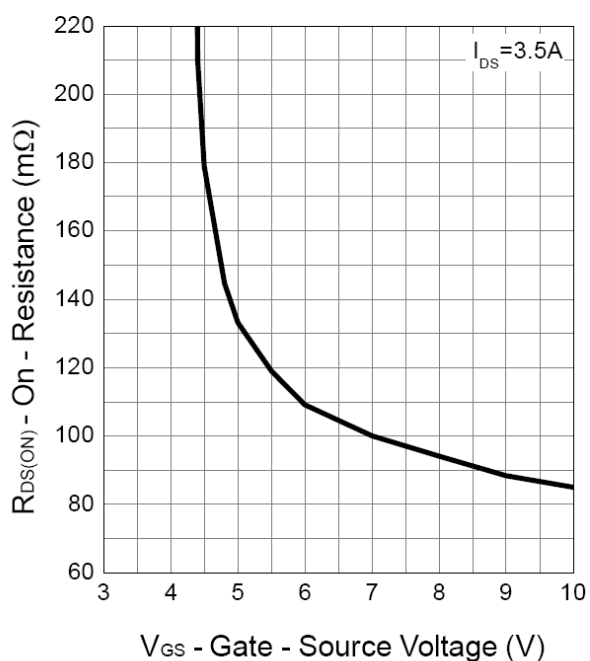
Output Characteristics



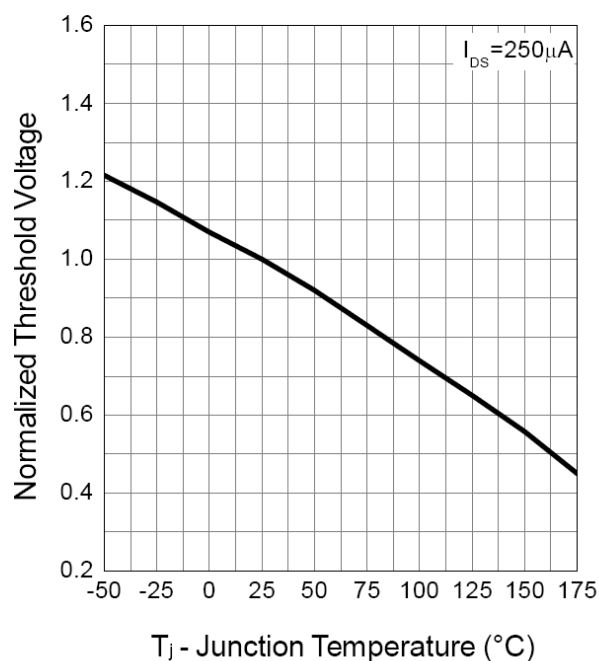
Drain-Source On Resistance



Gate-Source On Resistance

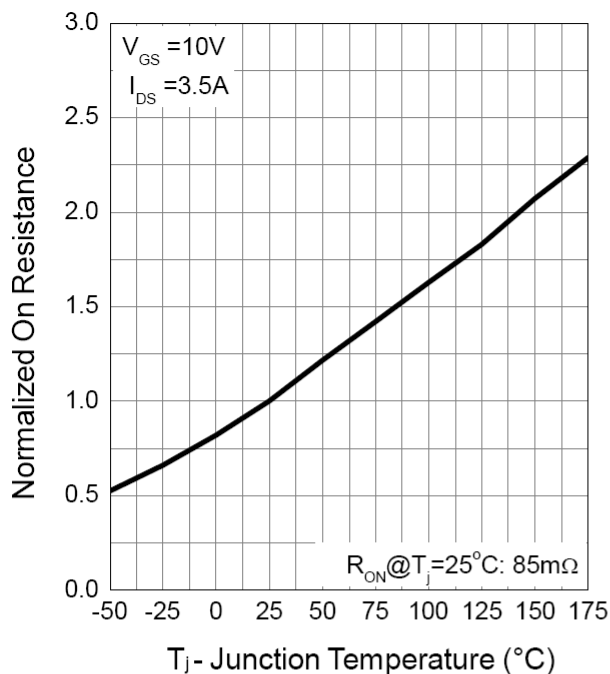


Gate Threshold Voltage

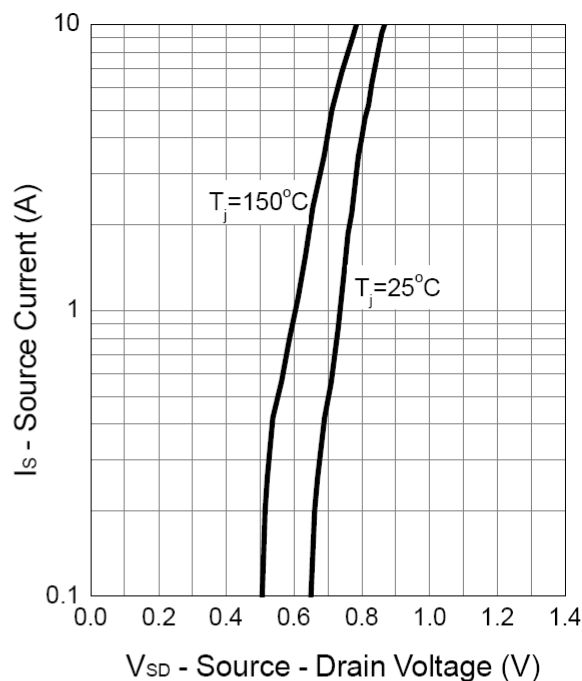


Typical Characteristics (Cont.)

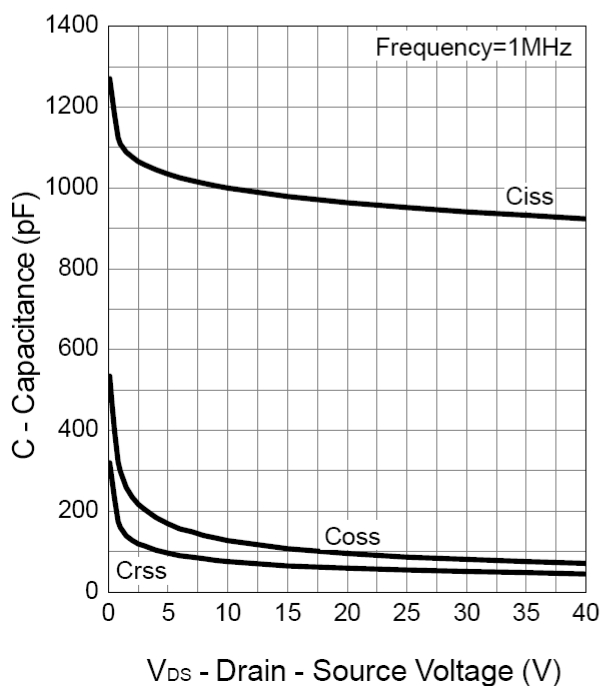
Drain-Source On Resistance



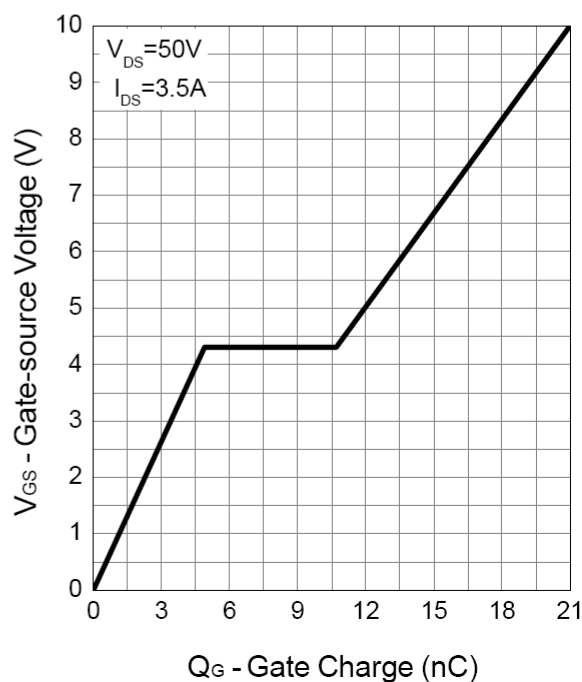
Source-Drain Diode Forward

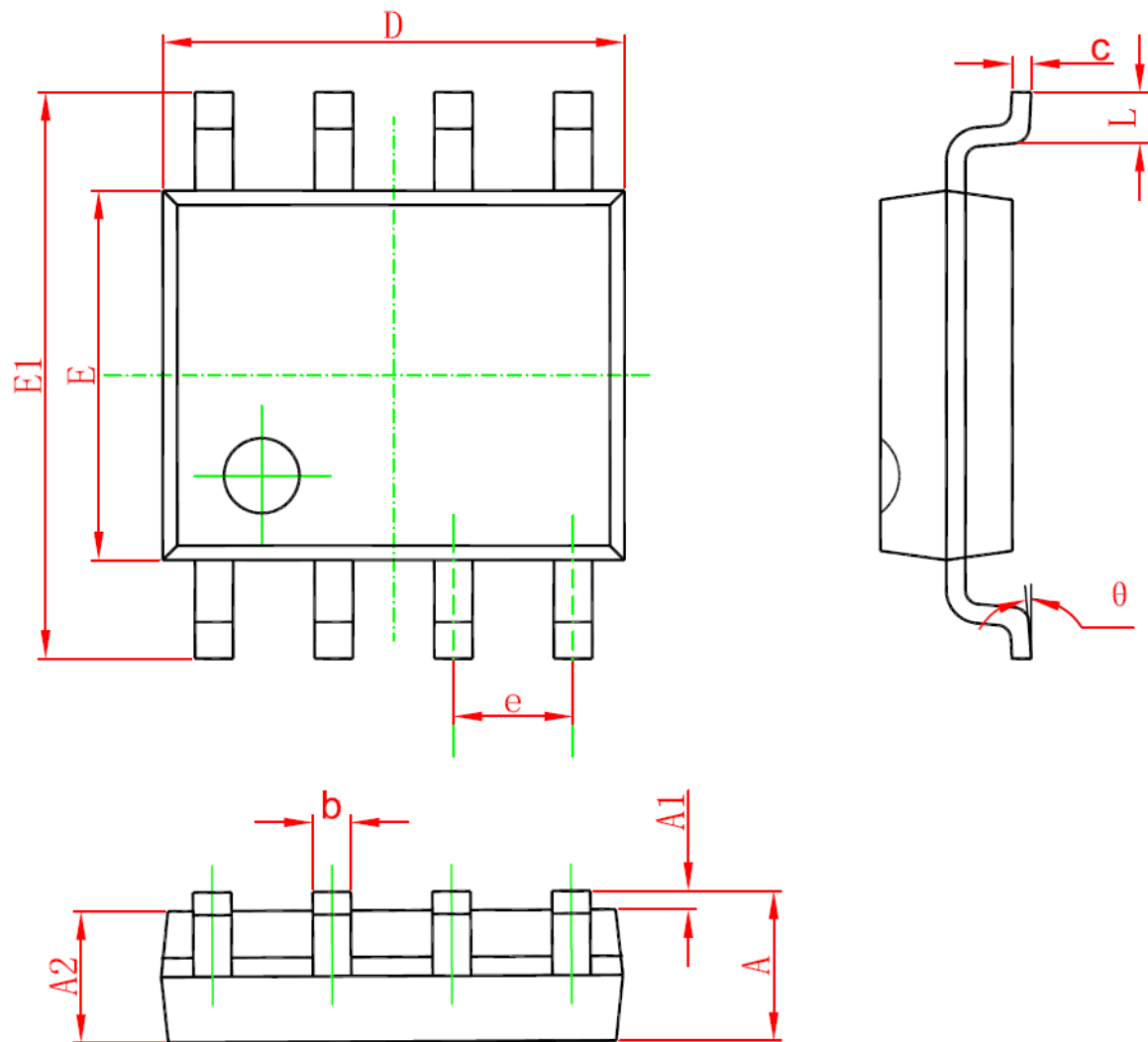


Capacitance



Gate Charge





Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

Attention:

- Any and all H&M SEMI products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your H&M SEMI representative nearest you before using any H&M SEMI products described or contained herein in such applications.
- H&M SEMI assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all H&M SEMI products described or contained herein.
- Specifications of any and all H&M SEMI products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- H&M Semiconductor CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all H&M SEMI products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of H&M Semiconductor CO.,LTD.
- Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. H&M SEMI believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the H&M SEMI product that you intend to use.
- This catalog provides information as of Sep.2010. Specifications and information herein are subject to change without notice.