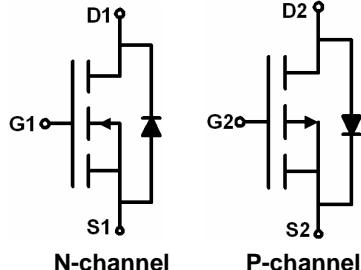
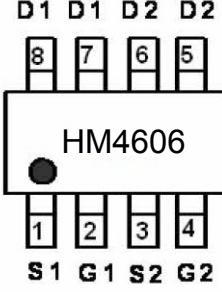
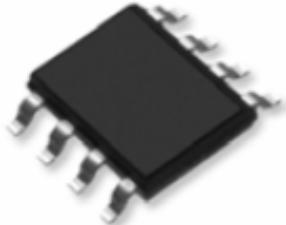


## N and P-Channel Enhancement Mode Power MOSFET

<p><b>Description</b></p> <p>The HM4606 uses advanced trench technology to provide excellent <math>R_{DS(ON)}</math> and low gate charge . This device is suitable for use as a load switch or in PWM applications.</p> <p><b>General Features</b></p> <ul style="list-style-type: none"> <li>● <b>N-Channel</b> <ul style="list-style-type: none"> <li><math>V_{DS} = 30V, I_D = 7.0A</math></li> <li><math>R_{DS(ON)} &lt; 31m\Omega @ V_{GS}=10V</math></li> <li><math>R_{DS(ON)} &lt; 43m\Omega @ V_{GS}=4.5V</math></li> </ul> </li> <li>● <b>P-Channel</b> <ul style="list-style-type: none"> <li><math>V_{DS} = -30V, I_D = -5.1A</math></li> <li><math>R_{DS(ON)} &lt; 95m\Omega @ V_{GS}=-4.5V</math></li> <li><math>R_{DS(ON)} &lt; 65m\Omega @ V_{GS}=-10V</math></li> </ul> </li> <li>● High power and current handing capability</li> <li>● Lead free product is acquired</li> <li>● Surface mount package</li> </ul> <p><b>Application</b></p> <ul style="list-style-type: none"> <li>● PWM applications</li> <li>● Load switch</li> <li>● Power management</li> </ul>	 <p><b>Schematic diagram</b></p>  <p><b>Marking and pin assignment</b></p>  <p><b>SOP-8 top view</b></p>
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### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
HM4606	HM4606	SOP-8	Ø330mm	12mm	2500 units

### Absolute Maximum Ratings ( $T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	$V_{DS}$	30	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	$\pm 20$	V
Continuous Drain Current	$I_D$	7.0	-5.1	A
Pulsed Drain Current (Note 1)	$I_{DM}$	20	-20	A
Maximum Power Dissipation	$P_D$	2.5	2.5	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	-55 To 150	°C

**Thermal Characteristic**

Thermal Resistance,Junction-to-Ambient (Note2)	$R_{\theta JA}$	N-Ch	89	°C/W
		P-Ch	90	

**N-CH Electrical Characteristics ( $T_A=25^\circ C$  unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	30	33	-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=30V, V_{GS}=0V$	-	-	1	$\mu A$
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	$\pm 100$	nA
<b>On Characteristics (Note 3)</b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.2	1.6	2.4	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=5A$	-	25.5	31	$m\Omega$
		$V_{GS}=4.5V, I_D=4A$	-	34	43	$m\Omega$
Forward Transconductance	$g_{FS}$	$V_{DS}=5V, I_D=5A$	-	15	-	S
<b>Dynamic Characteristics (Note4)</b>						
Input Capacitance	$C_{iss}$	$V_{DS}=15V, V_{GS}=0V,$ $F=1.0MHz$	-	255	-	PF
Output Capacitance	$C_{oss}$		-	45	-	PF
Reverse Transfer Capacitance	$C_{rss}$		-	35	-	PF
<b>Switching Characteristics (Note 4)</b>						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=15V, R_L=3\Omega$ $V_{GS}=10V, R_{GEN}=3\Omega$	-	4.5	-	nS
Turn-on Rise Time	$t_r$		-	2.5	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	14.5	-	nS
Turn-Off Fall Time	$t_f$		-	3.5	-	nS
Total Gate Charge	$Q_g$	$V_{DS}=15V, I_D=5A,$ $V_{GS}=10V$	-	5.2	-	nC
Gate-Source Charge	$Q_{gs}$		-	0.85	-	nC
Gate-Drain Charge	$Q_{gd}$		-	1.3	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	$V_{SD}$	$V_{GS}=0V, I_S=5A$	-	-	1.2	V
Diode Forward Current (Note 2)	$I_S$		-	-	5	A

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**.P-CH Electrical Characteristics ( $T_A=25^\circ\text{C}$  unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-30	-33	-	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=-24\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-1	$\mu\text{A}$
Gate-Body Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm100$	nA
<b>On Characteristics (Note 3)</b>						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1	-1.5	-3	V
Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-4.1\text{A}$	-	55	65	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-4\text{A}$	-	75	95	$\text{m}\Omega$
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}}=-5\text{V}, I_{\text{D}}=-4.1\text{A}$	5.5	-	-	S
<b>Dynamic Characteristics (Note 4)</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$	-	700	-	PF
Output Capacitance	$C_{\text{oss}}$		-	120	-	PF
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	75	-	PF
<b>Switching Characteristics (Note 4)</b>						
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=-15\text{V}, R_{\text{L}}=3.6\Omega$ $V_{\text{GS}}=-10\text{V}, R_{\text{GEN}}=3\Omega$	-	9	-	nS
Turn-on Rise Time	$t_{\text{r}}$		-	5	-	nS
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	28	-	nS
Turn-Off Fall Time	$t_{\text{f}}$		-	13.5	-	nS
Total Gate Charge	$Q_{\text{g}}$	$V_{\text{DS}}=-15\text{V}, I_{\text{D}}=-4\text{A}, V_{\text{GS}}=-10\text{V}$	-	14	-	nC
Gate-Source Charge	$Q_{\text{gs}}$		-	3.1	-	nC
Gate-Drain Charge	$Q_{\text{gd}}$		-	3.	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}, I_{\text{s}}=-1\text{A}$	-	-	-1.2	V

**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production

### N- Channel Typical Electrical and Thermal Characteristics (Curves)

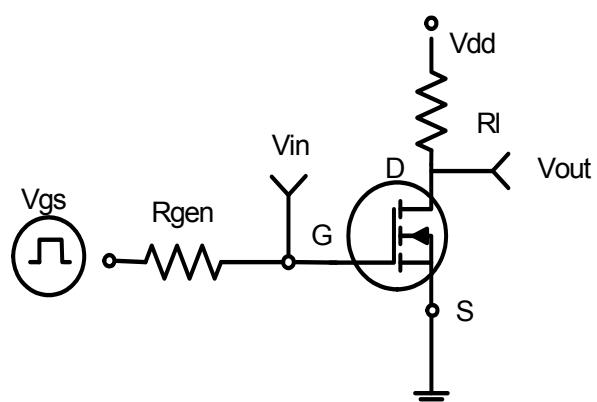


Figure 1:Switching Test Circuit

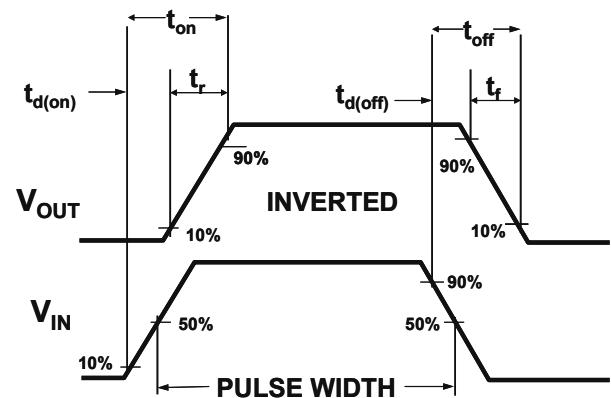


Figure 2:Switching Waveforms

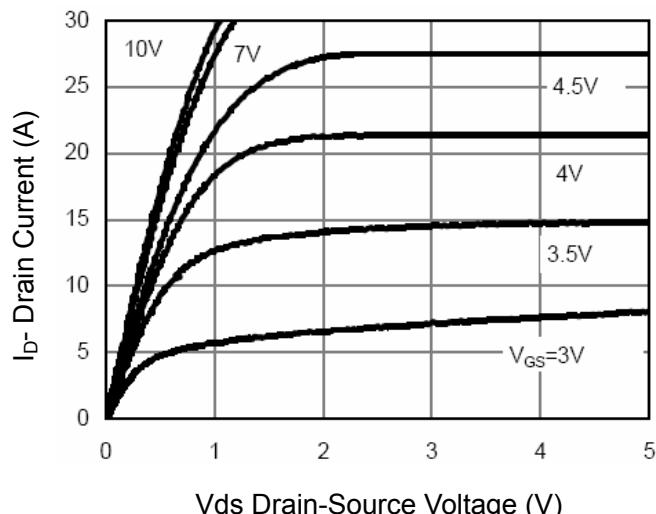


Figure 3 Output Characteristics

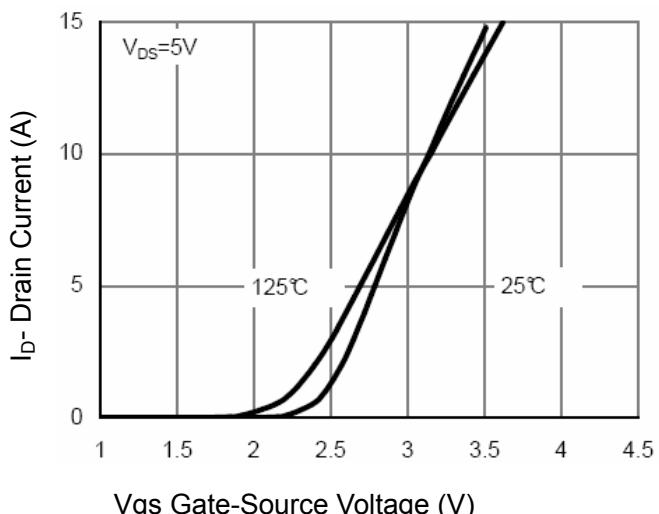


Figure 4 Transfer Characteristics

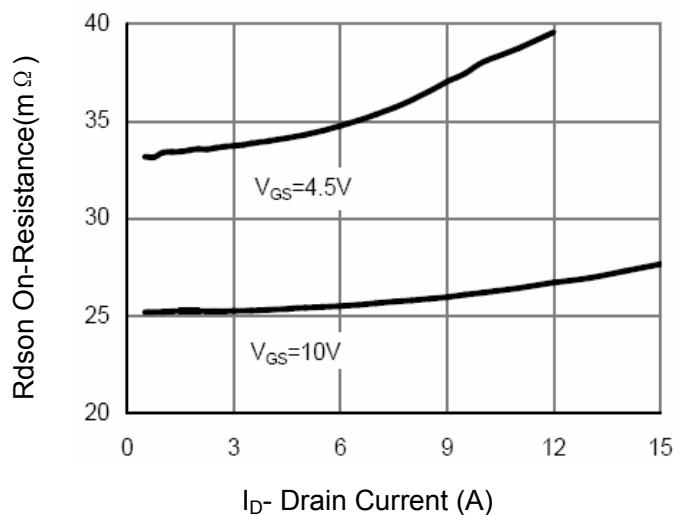


Figure 5 Drain-Source On-Resistance

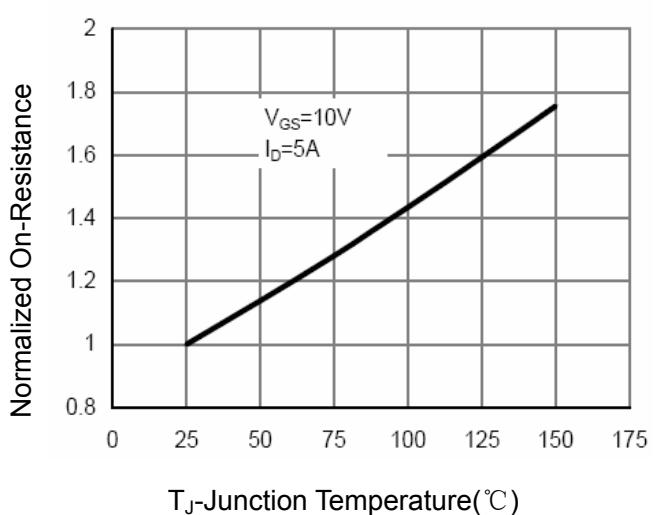


Figure 6 Drain-Source On-Resistance

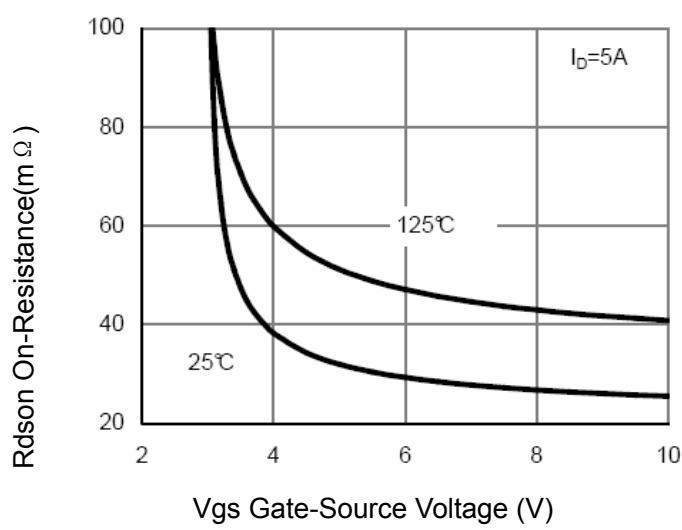


Figure 7 Rdson vs Vgs

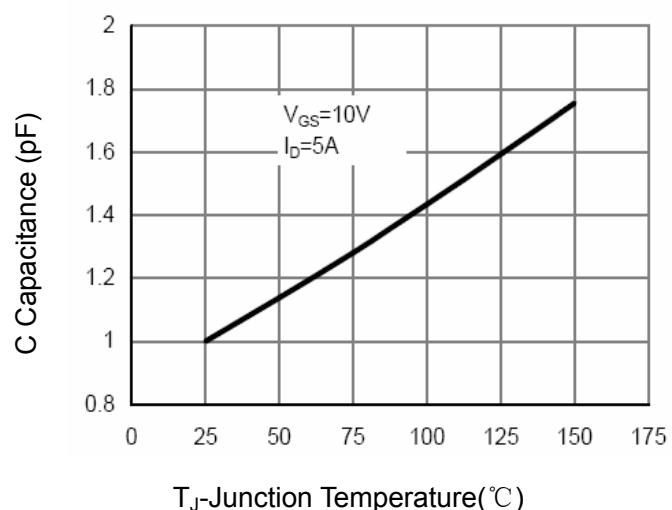


Figure 8 Drain-Source On-Resistance

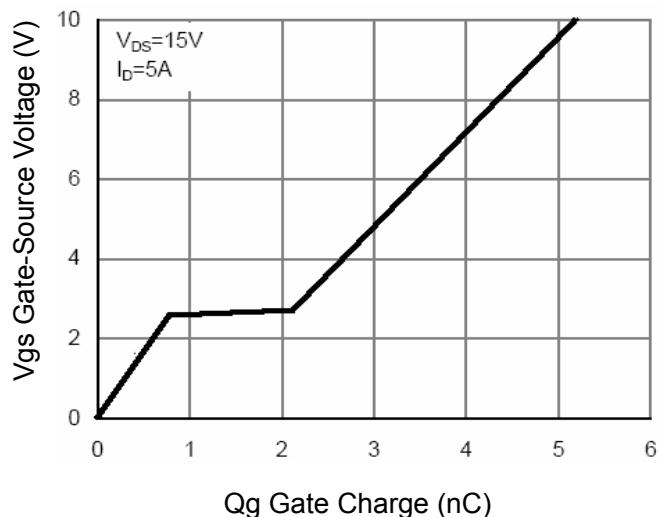


Figure 9 Gate Charge

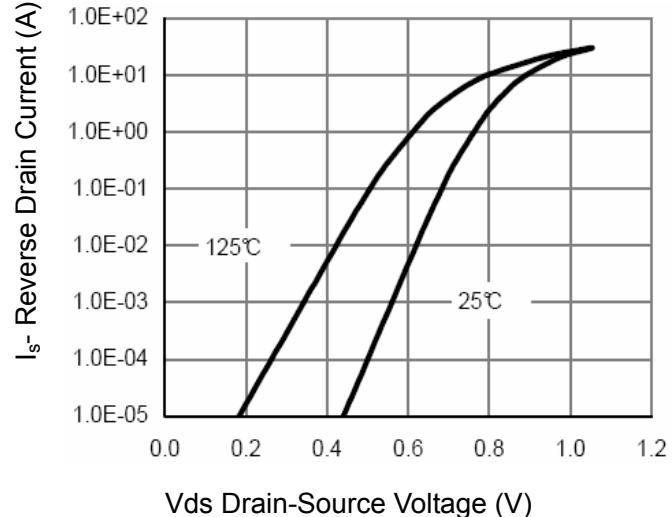


Figure 10 Source-Drain Diode Forward

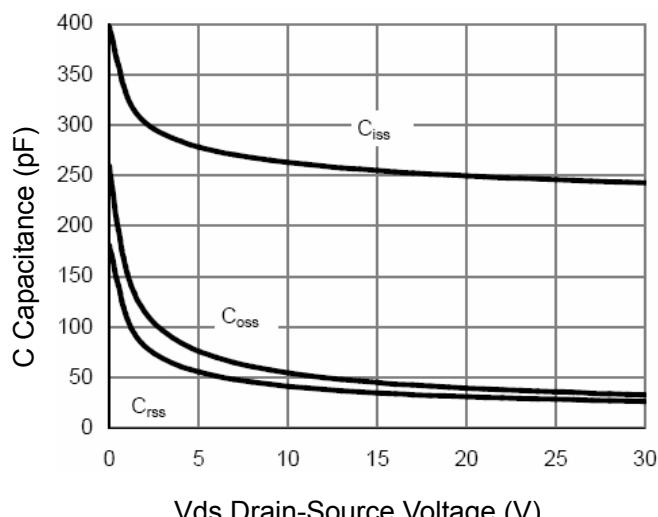


Figure 11 Capacitance vs Vds

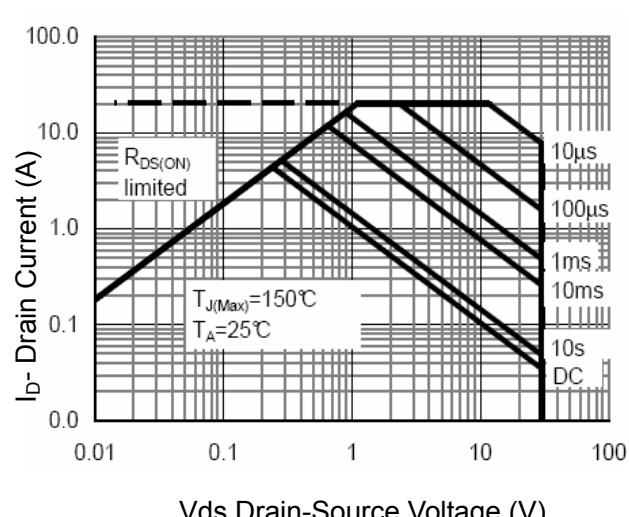
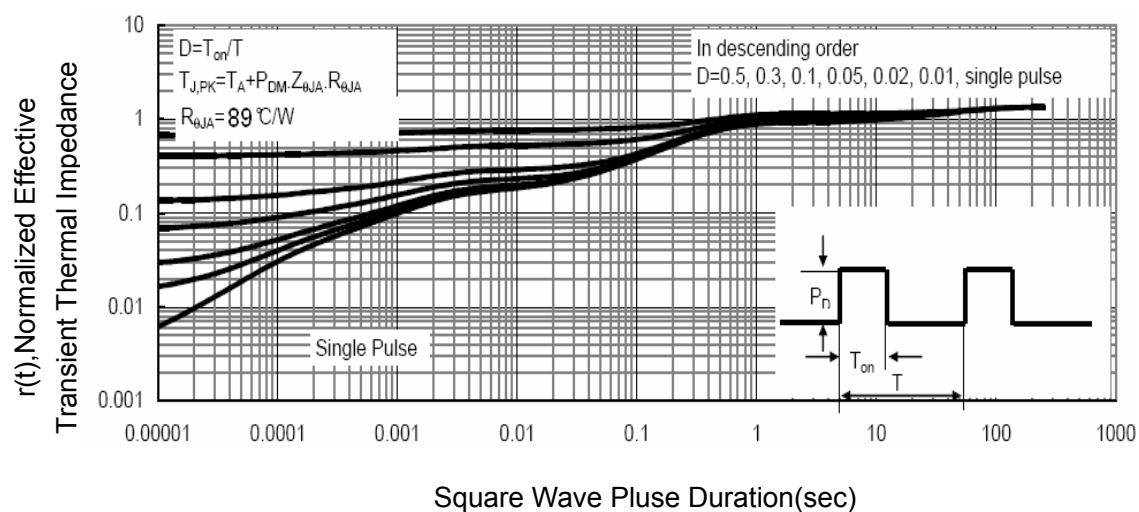


Figure 12 Safe Operation Area



**Figure 13 Normalized Maximum Transient Thermal Impedance**

### P-Channel Typical Electrical and Thermal Characteristics

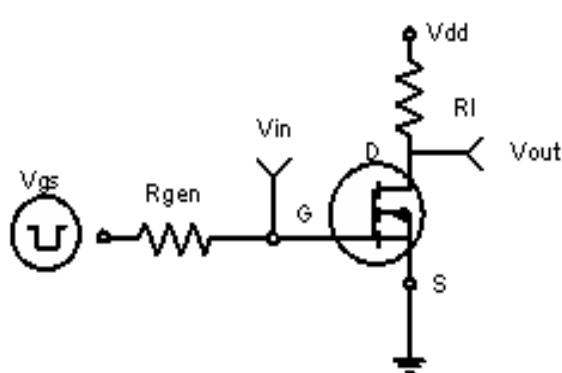


Figure 1:Switching Test Circuit

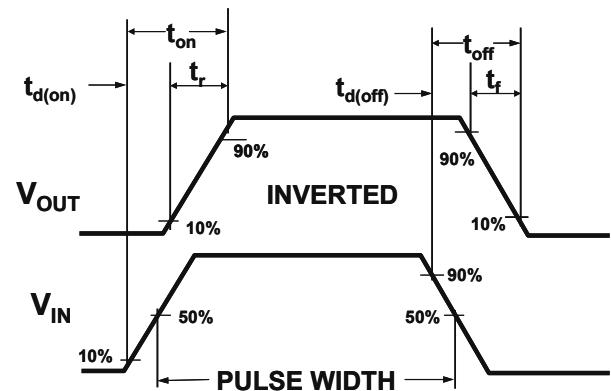


Figure 2:Switching Waveforms

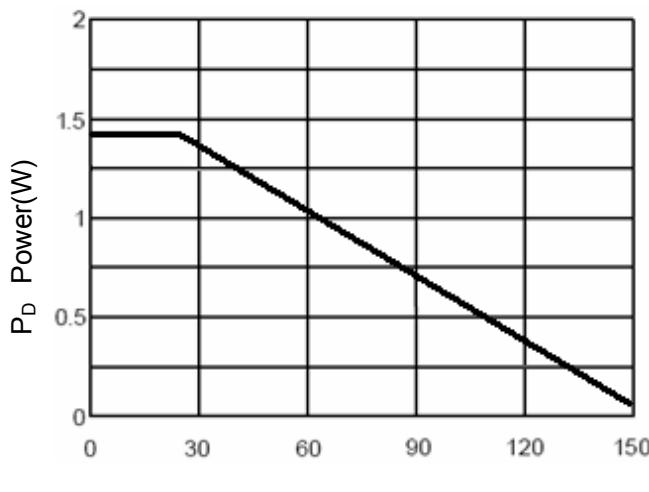


Figure 3 Power Dissipation

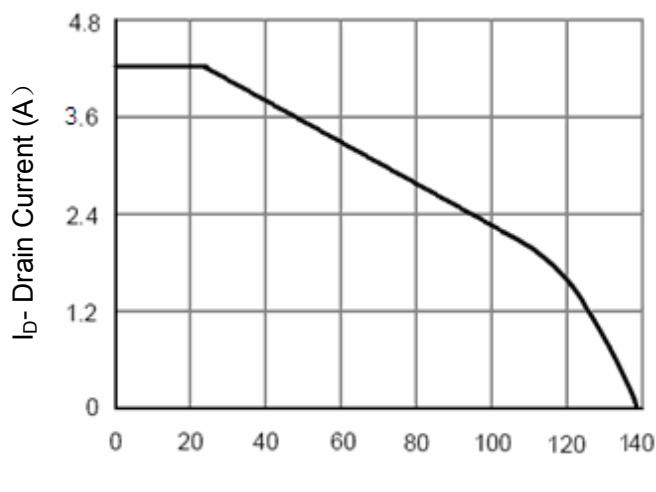


Figure 4 Drain Current

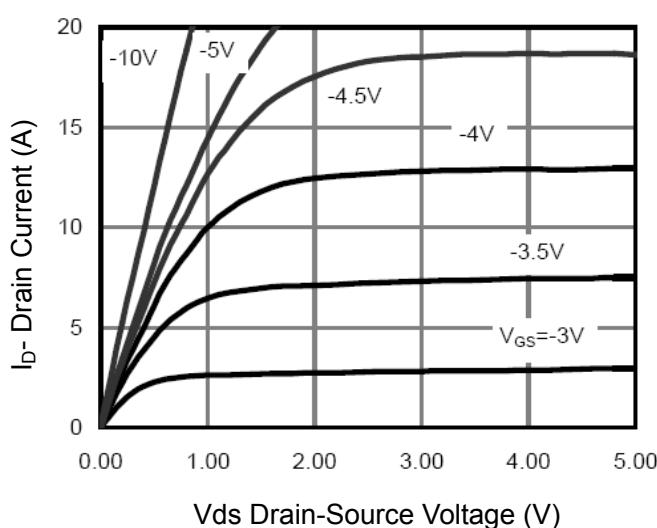


Figure 5 Output CHARACTERISTICS

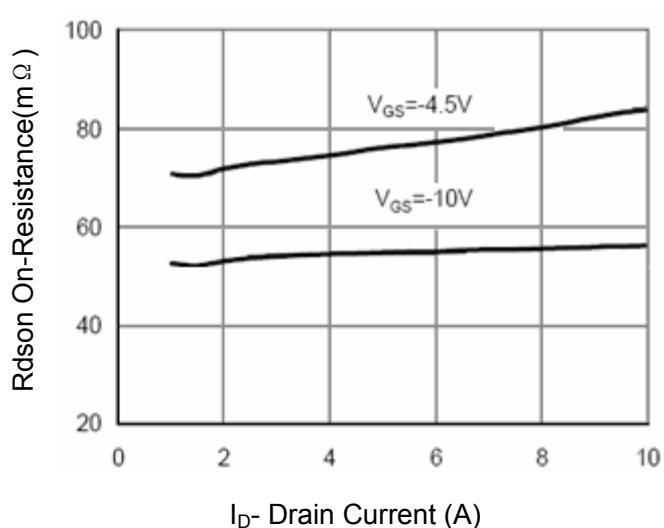


Figure 6 Drain-Source On-Resistance

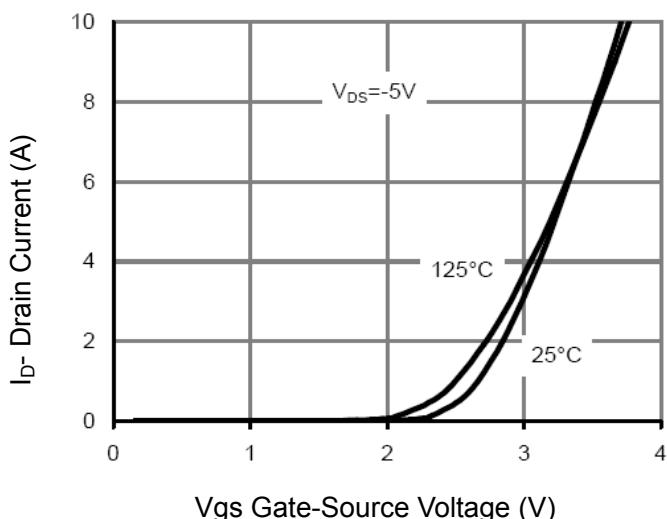


Figure 7 Transfer Characteristics

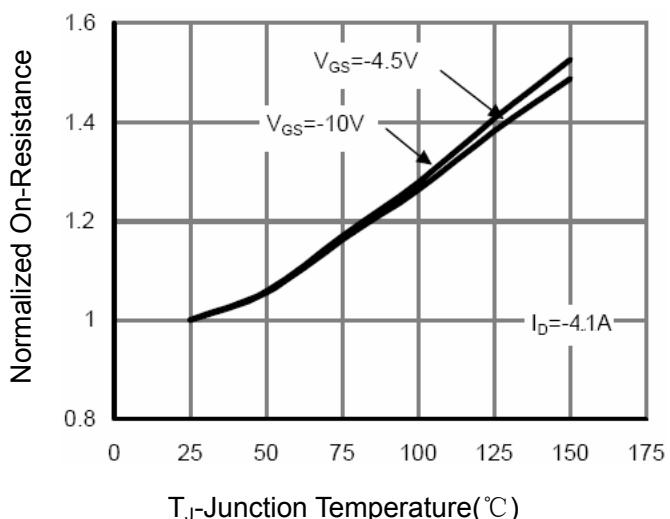


Figure 8 Drain-Source On-Resistance

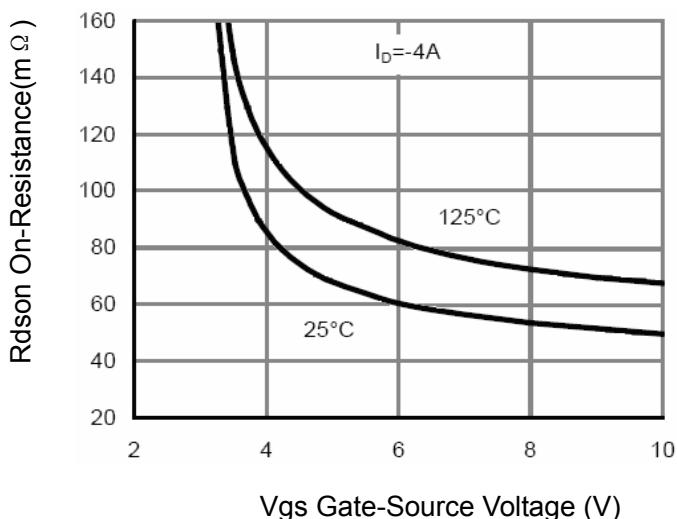


Figure 9  $R_{DSON}$  vs  $V_{GS}$

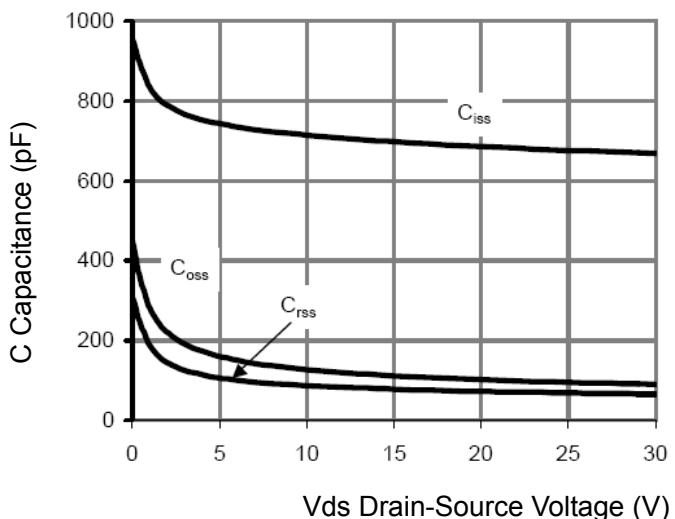


Figure 10 Capacitance vs  $V_{DS}$

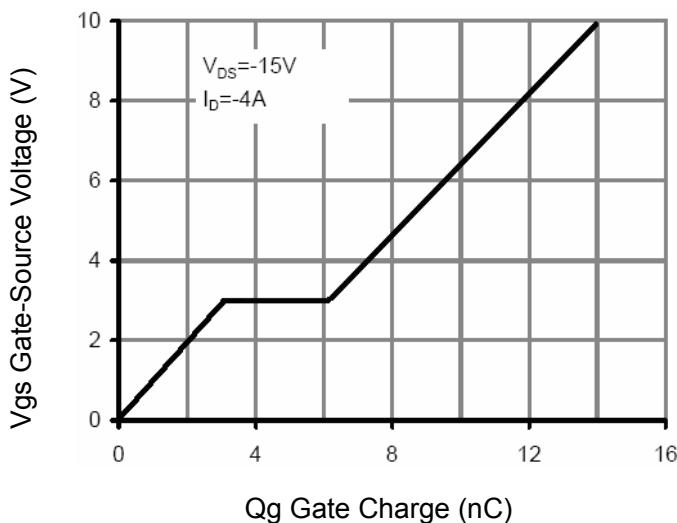


Figure 11 Gate Charge

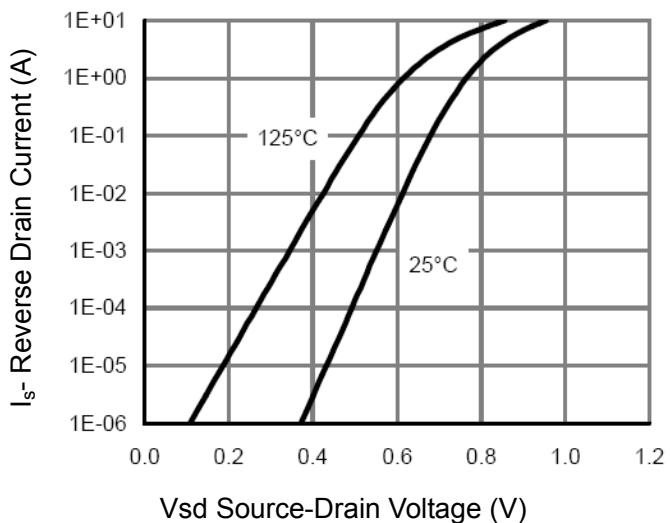


Figure 12 Source-Drain Diode Forward

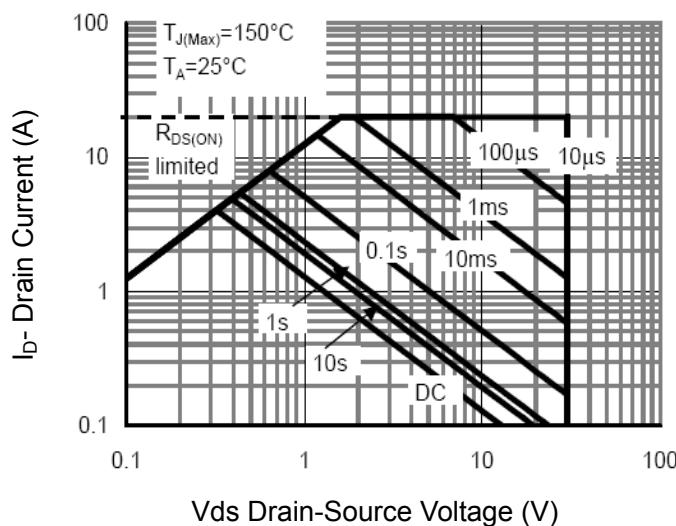


Figure 13 Safe Operation Area

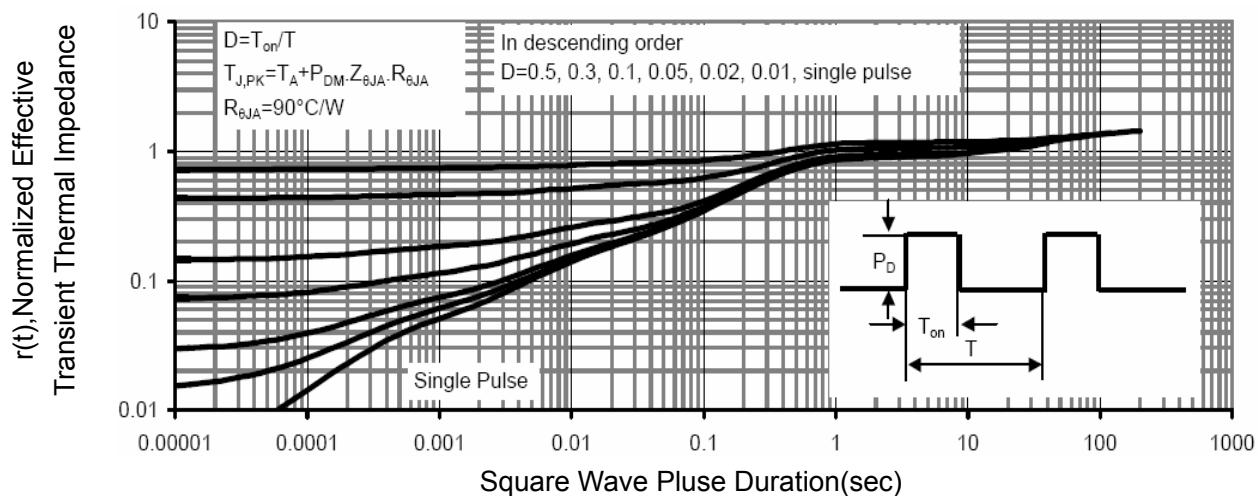
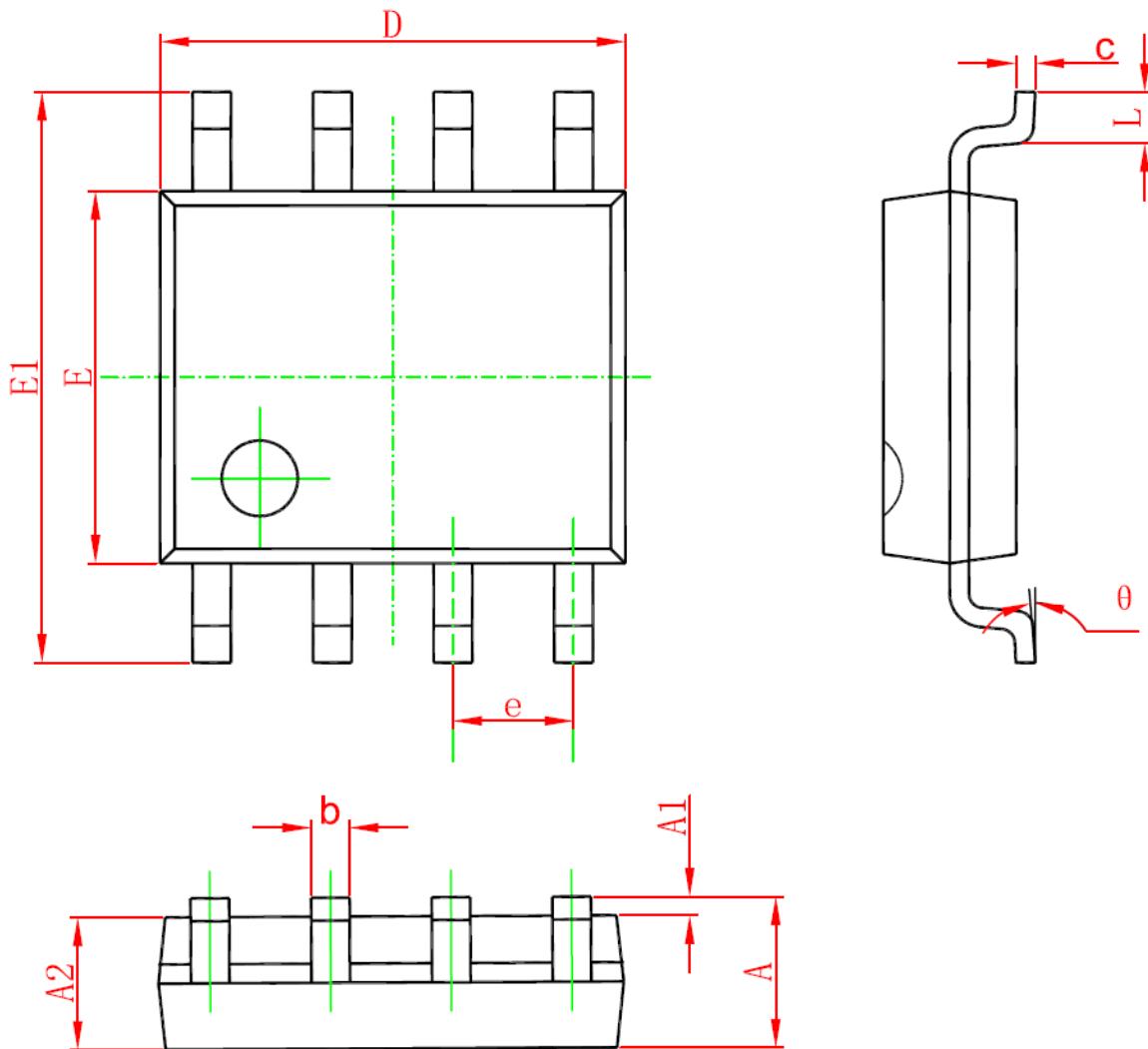


Figure 14 Normalized Maximum Transient Thermal Impedance

SOP-8 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

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