

### N and P-Channel Enhancement Mode Power MOSFET

### **Description**

The HM4611A uses advanced trench technology to provide excellent  $R_{\text{DS(ON)}}$  and low gate charge . It can be used in a wide variety of applications.

#### **General Features**

#### N-Channel

 $V_{DS} = 60V, I_{D} = 9.0A$ 

 $R_{DS(ON)}$  < 16m $\Omega$  @  $V_{GS}$ =10V (Typ:12m $\Omega$ )

 $R_{DS(ON)} < 24m\Omega @ V_{GS} = 4.5V \text{ Å} ^{\circ} ] \text{ M 8} { \hat{o} D}$ 

#### P-Channel

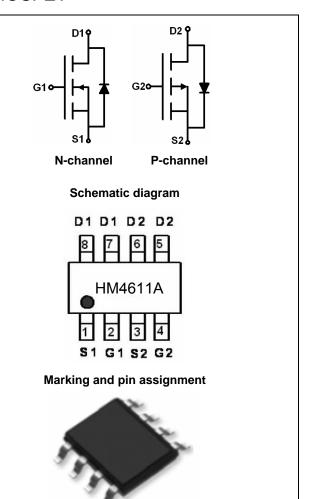
 $V_{DS} = -60V, I_{D} = -6.5A$ 

 $R_{DS(ON)}$  < 45m $\Omega$  @  $V_{GS}$ =-10V

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### **Application**

- Power switching application
- PælåÁn, ãn&@ åÁæd åÁ@ã @Á\^``^} & Á&ã& ão Á
- DC-DC Converter



SOP-8 top view

### **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
HM4611A	HM4611A	SOP-8	Ø330mm	12mm	2500 units

### Absolute Maximum Ratings (T<sub>A</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	V <sub>DS</sub>	60	-55	٧
Gate-Source Voltage	V <sub>GS</sub>	±20	±20	٧
Continuous Drain Curren	I <sub>D</sub>	9	-6.5	А
Pulsed Drain Current (Note 1)	I <sub>DM</sub>	36	-32	Α
Maximum Power Dissipation	P <sub>D</sub>	3.1	3	W
Operating Junction and Storage Temperature Range	$T_{J}$ , $T_{STG}$	-55 To 150	-55 To 150	$^{\circ}$ C



## **Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note2)	$R_{ hetaJA}$	N-Ch	62.5	°C/W	l
Thermal Nesistance, Juniculon-to-Ambient (Note2)		P-Ch	42	CIVV	

# N-CH Electrical Characteristics ( $T_A$ =25 $^{\circ}$ C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	·					
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	60	69	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V,V <sub>GS</sub> =0V	-	-	1	μΑ
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)	·					
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	2	3	4	V
Dunin Course On Otata Desistance	Б	V <sub>GS</sub> =10V, I <sub>D</sub> =9A		12	16	
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =6A	-	18	24	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =4.5A	11	-	-	S
Dynamic Characteristics (Note4)			•			
Input Capacitance	C <sub>lss</sub>	\/ -25\/\/ -0\/		450		PF
Output Capacitance	Coss	$V_{DS}$ =25V, $V_{GS}$ =0V, F=1.0MHz		60		PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F = 1.0WII IZ		25		PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	4.7	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{Ds}$ =30 $V$ , $I_{D}$ =4.5 $A$	-	2.3	-	nS
Turn-Off Delay Time	$t_{d(off)}$	$V_{GS}$ =10 $V$ , $R_{GEN}$ =3 $\Omega$	-	15.7	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	1.9	-	nS
Total Gate Charge	Qg	\/ 00\/L 4.5A	-	8.5	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =30V, $I_{D}$ =4.5A, $V_{GS}$ =10V	-	1.6	-	nC
Gate-Drain Charge	Q <sub>gd</sub>	VGS-1UV	-	2.2	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =3.7A	-	-	1.2	V
Diode Forward Current (Note 2)	I <sub>S</sub>		-	-	4	Α

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# P-CH Electrical Characteristics (T\_A=25 $^{\circ}\text{C}\text{ unless}$ otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-55	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-55V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =-250μA	-2.0	-2.9	-3.5	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-6.5A	-	39	45	mΩ
Forward Transconductance	<b>g</b> Fs	V <sub>DS</sub> =-15V,I <sub>D</sub> =-6.5A	16	-	-	S
Dynamic Characteristics (Note4)			•			
Input Capacitance	C <sub>lss</sub>	V - 20V/V -0V	-	1450	-	PF
Output Capacitance	Coss	$V_{DS}$ =-20V, $V_{GS}$ =0V, F=1.0MHz	-	145	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	r=1.0IVID2	-	110	-	PF
Switching Characteristics (Note 4)			•			
Turn-on Delay Time	t <sub>d(on)</sub>		-	8	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =-30V, , $R_L$ =30 $\Omega$	-	9	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =-10V, $R_{GEN}$ =6 $\Omega$	-	65	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	30	-	nS
Total Gate Charge	Qg	\/ - 20\/ L - C.EA	-	26	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =-30V, $I_{D}$ =-6.5A, $V_{GS}$ =-10V	-	4.5	-	nC
Gate-Drain Charge	Q <sub>gd</sub>	v <sub>GS</sub> 10v	-	7	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	$V_{SD}$	$V_{GS}$ =0 $V$ , $I_{S}$ =-3 $A$	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	-6.5	Α

#### Notes:

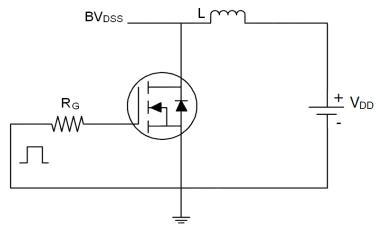
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- 3. Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production



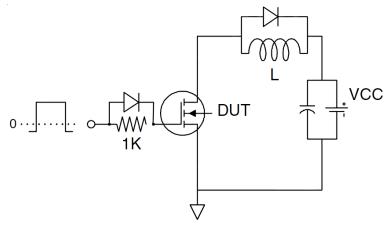
## N- Channel Typical Electrical and Thermal Characteristics (Curves)

## **Test circuit**

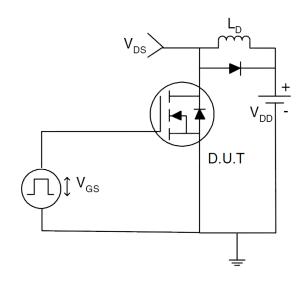
1) E<sub>AS</sub> test Circuits



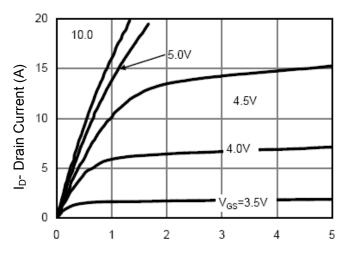
2) Gate charge test Circuit:



3) Switch Time Test Circuit:

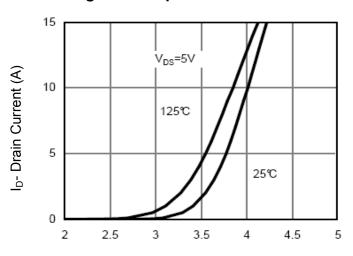


### TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (Curves)



Vds Drain-Source Voltage (V)

**Figure 1 Output Characteristics** 



Vgs Gate-Source Voltage (V)

**Figure 2 Transfer Characteristics** 

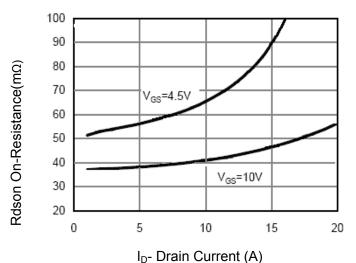
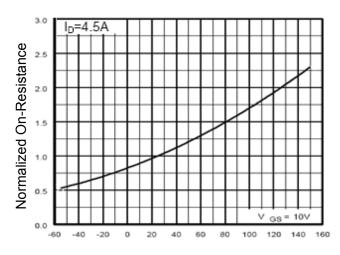
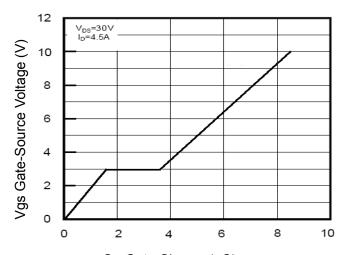


Figure 3 Rdson- Drain Current



 $T_J$ -Junction Temperature( $^{\circ}$ C)

Figure 4 Rdson-JunctionTemperature



Qg Gate Charge (nC)

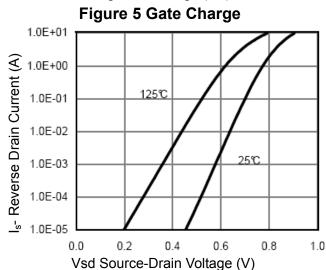


Figure 6 Source- Drain Diode Forward

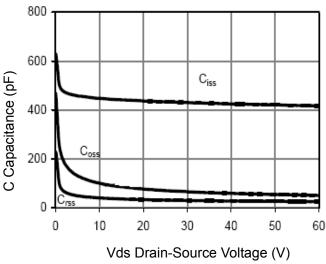


Figure 7 Capacitance vs Vds

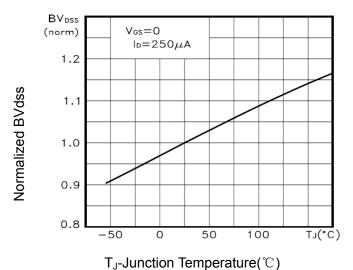


Figure 9 BV<sub>DSS</sub> vs Junction Temperature

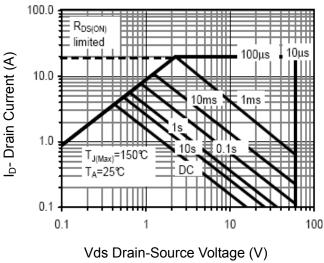


Figure 8 Safe Operation Area

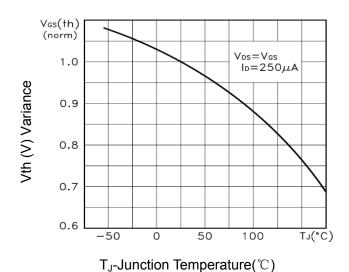
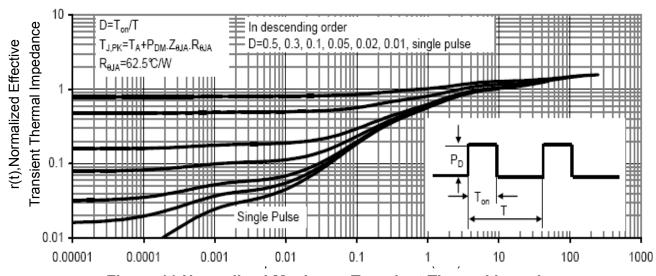


Figure 10 V<sub>GS(th)</sub> vs Junction Temperature



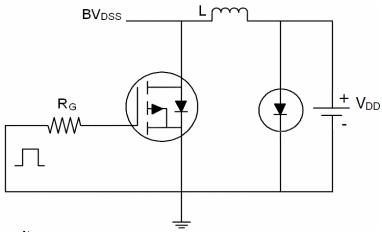
**Figure 11 Normalized Maximum Transient Thermal Impedance** 



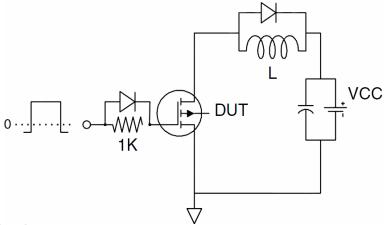
## P-Channel Typical Electrical and Thermal Characteristics

### **Test Circuit**

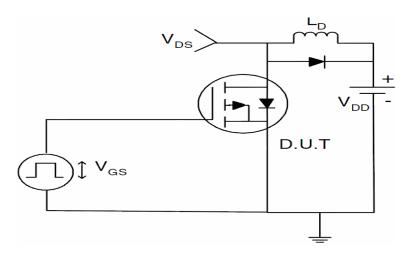
### 1) E<sub>AS</sub> Test Circuit



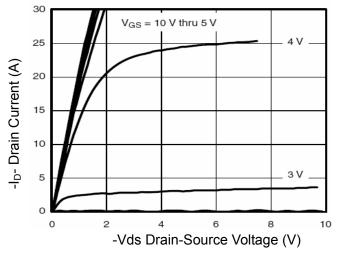
### 2) Gate Charge Test Circuit



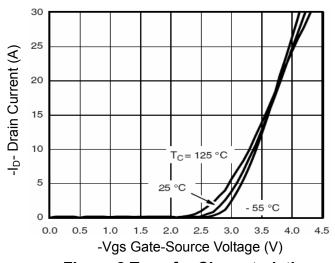
### 3) Switch Time Test Circuit



### **Typical Electrical and Thermal Characteristics**



**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 

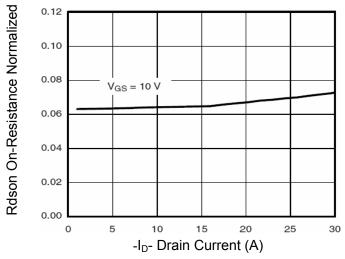


Figure 3 Rdson- Drain Current

### (Curves)

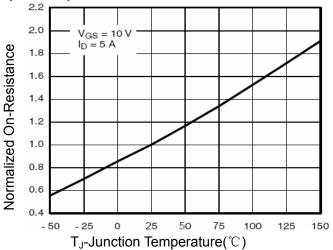


Figure 4 Rdson-Junction Temperature

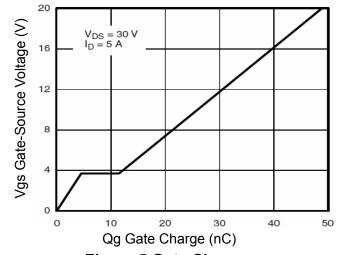


Figure 5 Gate Charge

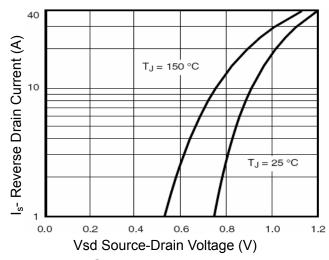


Figure 6 Source- Drain Diode Forward

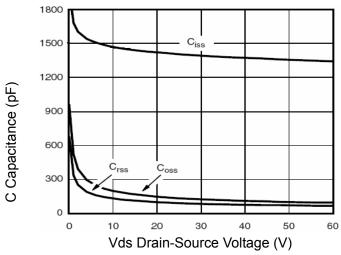


Figure 7 Capacitance vs Vds

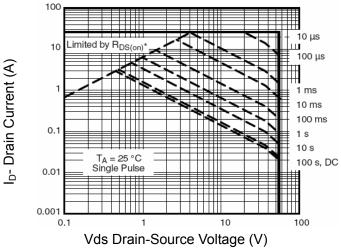


Figure 8 Safe Operation Area

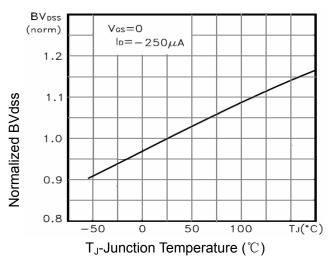


Figure 9 BV<sub>DSS</sub> vs Junction Temperature

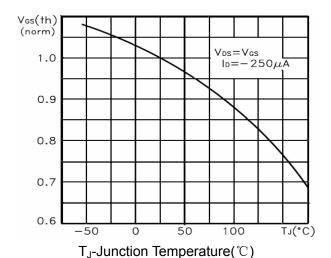


Figure 10 V<sub>GS(th)</sub> vs Junction Temperature

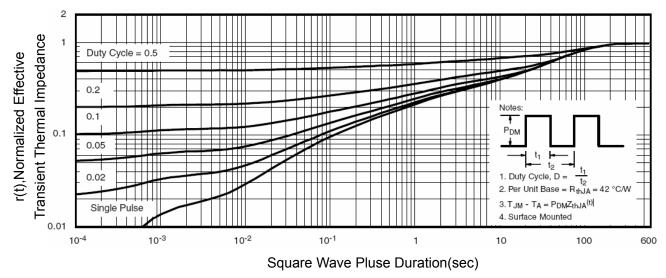
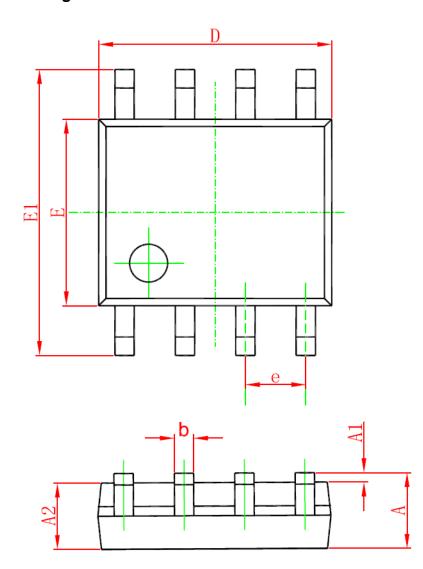
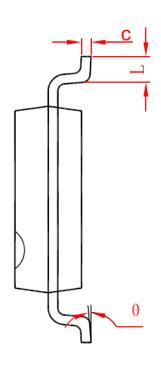


Figure 11 Normalized Maximum Transient Thermal Impedance

# **SOP-8 Package Information**





C. mh a l	Dimensions I	n Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	1. 350	1. 750	0. 053	0. 069	
A1	0. 100	0. 250	0. 004	0. 010	
A2	1. 350	1. 550	0. 053	0. 061	
b	0. 330	0. 510	0. 013	0. 020	
С	0. 170	0. 250	0.006	0. 010	
D	4. 700	5. 100	0. 185	0. 200	
E	3. 800	4. 000	0. 150	0. 157	
E1	5. 800	6. 200	0. 228	0. 244	
е	1. 270 (BSC)		0. 050 (BSC)		
L	0. 400	1. 270	0. 016	0. 050	
θ	0°	8°	0°	8°	



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