

## HM5N60 / HM5N60F

### 600V N-Channel MOSFET

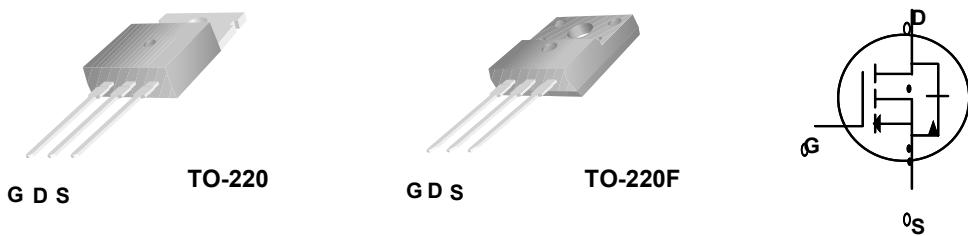
#### General Description

This Power MOSFET is produced using SL semi's advanced planar stripe DMOS technology.

This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switched mode power supplies, active power factor correction based on half bridge topology.

#### Features

- 4.5A, 600V,  $R_{DS(on)} = 2.50\Omega$  @  $V_{GS} = 10\text{ V}$
- Low gate charge ( typical 16nC)
- High ruggedness
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



#### Absolute Maximum Ratings

$T_C = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	HM5N60	HM5N60F	Units	
$V_{DSS}$	Drain-Source Voltage	600		V	
$I_D$	Drain Current - Continuous ( $T_C = 25^\circ\text{C}$ )	4.5	4.5*	A	
	- Continuous ( $T_C = 100^\circ\text{C}$ )	2.6	2.6 *	A	
$I_{DM}$	Drain Current - Pulsed	(Note 1)	18	18 *	A
$V_{GSS}$	Gate-Source Voltage		$\pm 30$	V	
$E_{AS}$	Single Pulsed Avalanche Energy	(Note 2)	210	mJ	
$E_{AR}$	Repetitive Avalanche Energy	(Note 1)	10.0	mJ	
$dv/dt$	Peak Diode Recovery $dv/dt$	(Note 3)	4.5	V/ns	
$P_D$	Power Dissipation ( $T_C = 25^\circ\text{C}$ )	100	33	W	
	- Derate above $25^\circ\text{C}$	0.8	0.26	W/ $^\circ\text{C}$	
$T_J, T_{STG}$	Operating and Storage Temperature Range		-55 to +150	$^\circ\text{C}$	
$T_L$	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		300	$^\circ\text{C}$	

\* Drain current limited by maximum junction temperature.

#### Thermal Characteristics

Symbol	Parameter	HM5N60	HM5N60F	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	1.25	3.79	$^\circ\text{C}/\text{W}$
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink Typ.	0.5	--	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.5	62.5	$^\circ\text{C}/\text{W}$

### Electrical Characteristics

$T_C = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}} = 0 \text{ V}$ , $I_D = 250 \mu\text{A}$	600	--	--	V
$\Delta \text{BV}_{\text{DSS}} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 250 \mu\text{A}$ , Referenced to $25^\circ\text{C}$	--	0.6	--	$\text{V}/^\circ\text{C}$
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}} = 600 \text{ V}$ , $V_{\text{GS}} = 0 \text{ V}$	--	--	1	$\mu\text{A}$
		$V_{\text{DS}} = 480 \text{ V}$ , $T_C = 125^\circ\text{C}$	--	--	10	$\mu\text{A}$
$I_{\text{GSSF}}$	Gate-Body Leakage Current, Forward	$V_{\text{GS}} = 30 \text{ V}$ , $V_{\text{DS}} = 0 \text{ V}$	--	--	100	nA
$I_{\text{GSSR}}$	Gate-Body Leakage Current, Reverse	$V_{\text{GS}} = -30 \text{ V}$ , $V_{\text{DS}} = 0 \text{ V}$	--	--	-100	nA
<b>On Characteristics</b>						
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}} = V_{\text{GS}}$ , $I_D = 250 \mu\text{A}$	2.0	--	4.0	V
$R_{\text{DS(on)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}} = 10 \text{ V}$ , $I_D = 2.25 \text{ A}$	--	2.0	2.5	$\Omega$
<b>Dynamic Characteristics</b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}} = 25 \text{ V}$ , $V_{\text{GS}} = 0 \text{ V}$ , $f = 1.0 \text{ MHz}$	--	560	--	pF
$C_{\text{oss}}$	Output Capacitance		--	55	--	pF
$C_{\text{rss}}$	Reverse Transfer Capacitance		--	7	--	pF
<b>Switching Characteristics</b>						
$t_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}} = 300 \text{ V}$ , $I_D = 4.5 \text{ A}$ , $R_G = 25 \Omega$	--	10	--	ns
$t_r$	Turn-On Rise Time		--	40	--	ns
$t_{\text{d(off)}}$	Turn-Off Delay Time		--	40	--	ns
$t_f$	Turn-Off Fall Time		--	50	--	ns
$Q_g$	Total Gate Charge	$V_{\text{DS}} = 480 \text{ V}$ , $I_D = 4.5 \text{ A}$ , $V_{\text{GS}} = 10 \text{ V}$	--	16	-	nC
$Q_{\text{gs}}$	Gate-Source Charge		--	2.5	--	nC
$Q_{\text{gd}}$	Gate-Drain Charge		--	6.5	--	nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_S$	Maximum Continuous Drain-Source Diode Forward Current	--	--	4.5	--	A
$I_{\text{SM}}$	Maximum Pulsed Drain-Source Diode Forward Current	--	--	18.0	--	A
$V_{\text{SD}}$	Drain-Source Diode Forward Voltage	$V_{\text{GS}} = 0 \text{ V}$ , $I_S = 4.5 \text{ A}$	--	--	1.4	V
$t_{\text{rr}}$	Reverse Recovery Time	$V_{\text{GS}} = 0 \text{ V}$ , $I_S = 4.5 \text{ A}$ , $dI_F / dt = 100 \text{ A}/\mu\text{s}$	--	300	--	ns
$Q_{\text{rr}}$	Reverse Recovery Charge		(Note 4)	--	2.0	$\mu\text{C}$

**Notes:**

1. Repetitive Rating : Pulse width limited by maximum junction temperature
2.  $L = 19 \text{ mH}$ ,  $I_{AS} = 4.5 \text{ A}$ ,  $V_{DD} = 50 \text{ V}$ ,  $R_G = 25 \Omega$ . Starting  $T_J = 25^\circ\text{C}$
3.  $I_{Sp} \leq 4.5 \text{ A}$ ,  $d/dt \leq 200 \text{ A}/\mu\text{s}$ ,  $V_{DD} \leq \text{BV}_{\text{DSS}}$ , Starting  $T_J = 25^\circ\text{C}$
4. Pulse Test : Pulse width  $\leq 300 \mu\text{s}$ , Duty cycle  $\leq 2\%$
5. Essentially independent of operating temperature

### Typical Characteristics

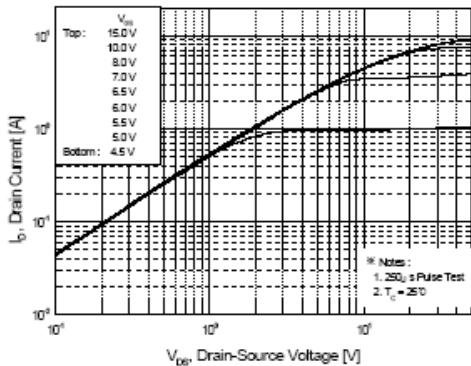


Figure 1. On-Region Characteristics

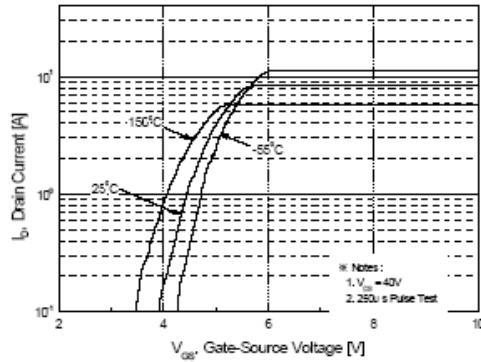


Figure 2. Transfer Characteristics

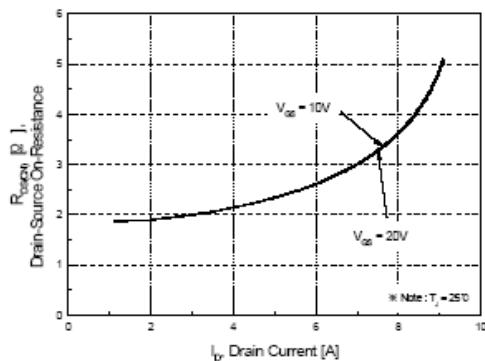


Figure 3. On-Resistance Variation vs  
Drain Current and Gate Voltage

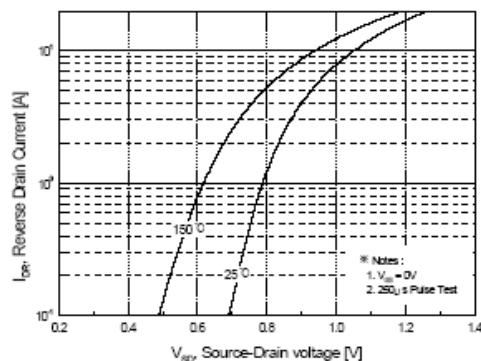


Figure 4. Body Diode Forward Voltage  
Variation with Source Current

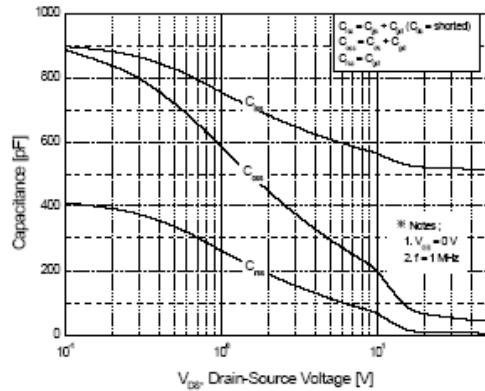


Figure 5. Capacitance Characteristics

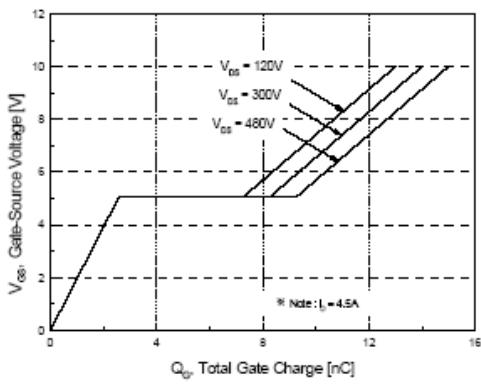
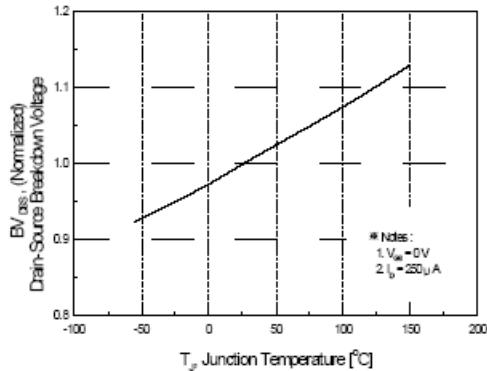
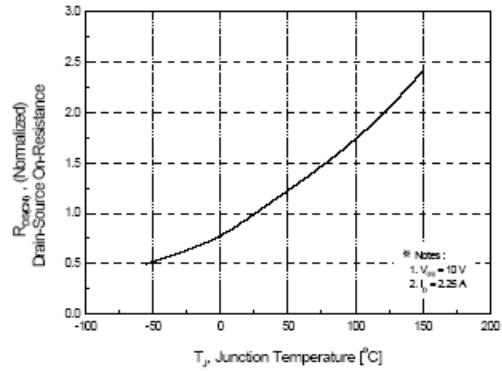


Figure 6. Gate Charge Characteristics

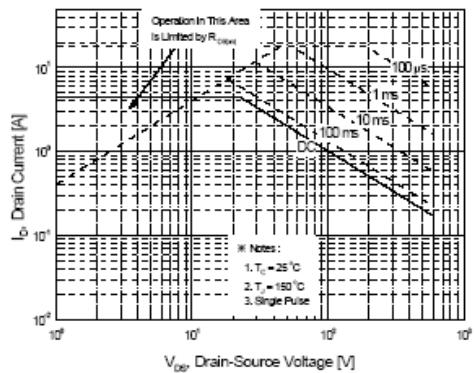
**Typical Characteristics** (Continued)



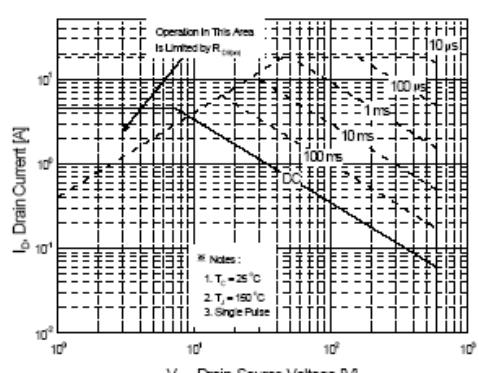
**Figure 7. Breakdown Voltage Variation  
vs Temperature**



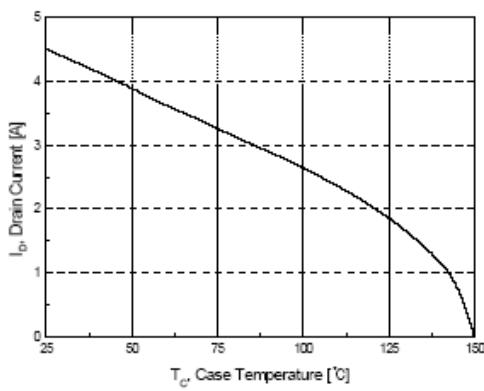
**Figure 8. On-Resistance Variation  
vs Temperature**



**Figure 9-1. Maximum Safe Operating Area  
for HM5N60**



**Figure 9-2. Maximum Safe Operating Area  
for HM5N60F**



**Figure 10. Maximum Drain Current  
vs Case Temperature**

Typical Characteristics (Continued)

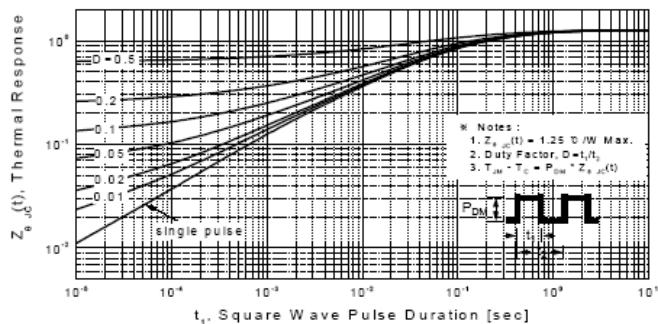


Figure 11-1. Transient Thermal Response Curve  
for HM5N60

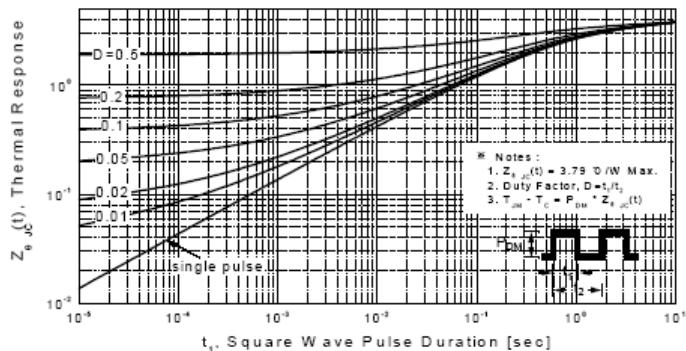
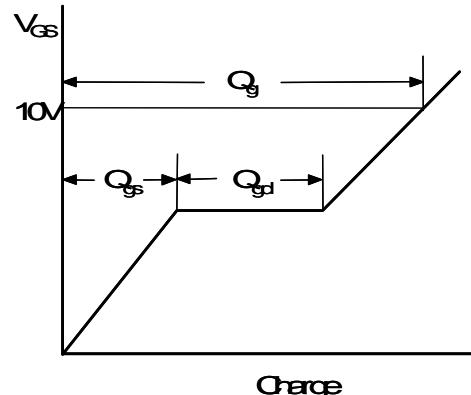
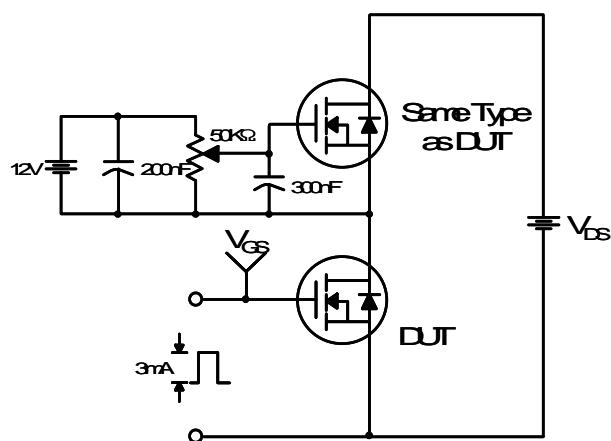
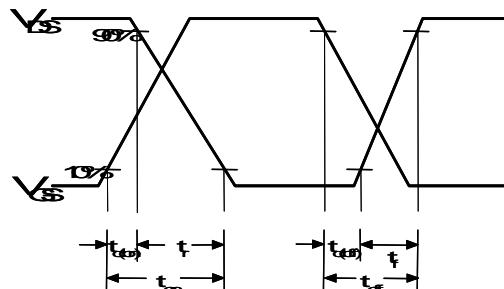
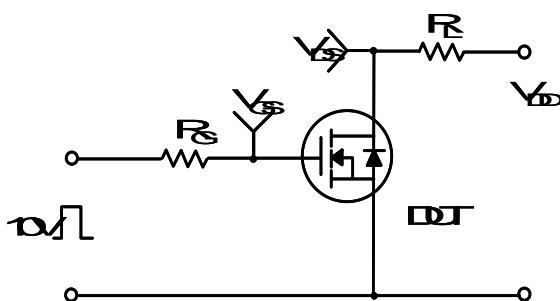


Figure 11-2. Transient Thermal Response Curve  
for HM5N60F

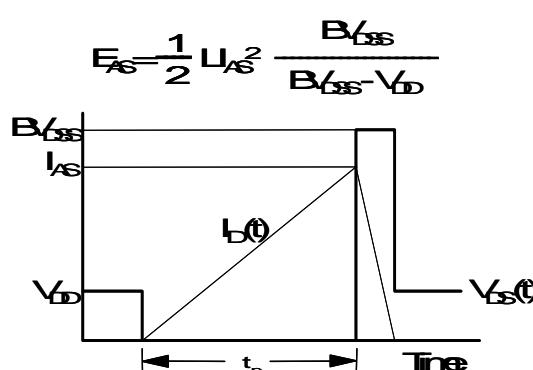
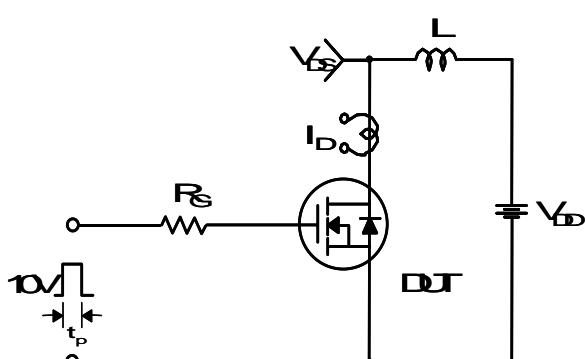
Gate Charge Test Circuit & Waveform



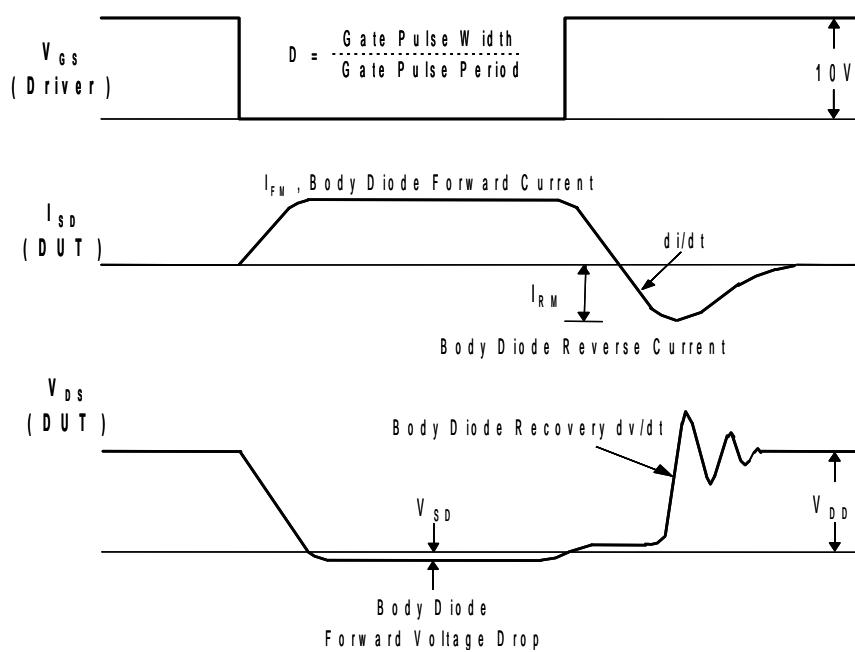
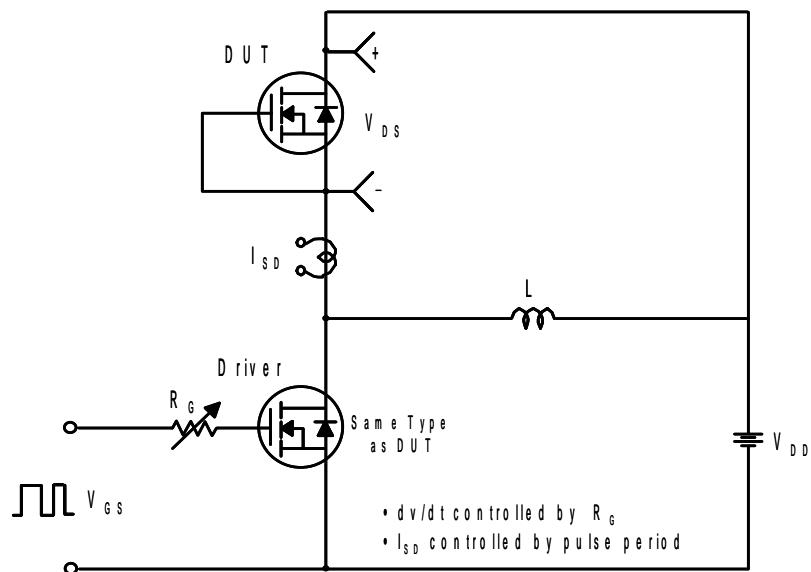
Resistive Switching Test Circuit & Waveforms



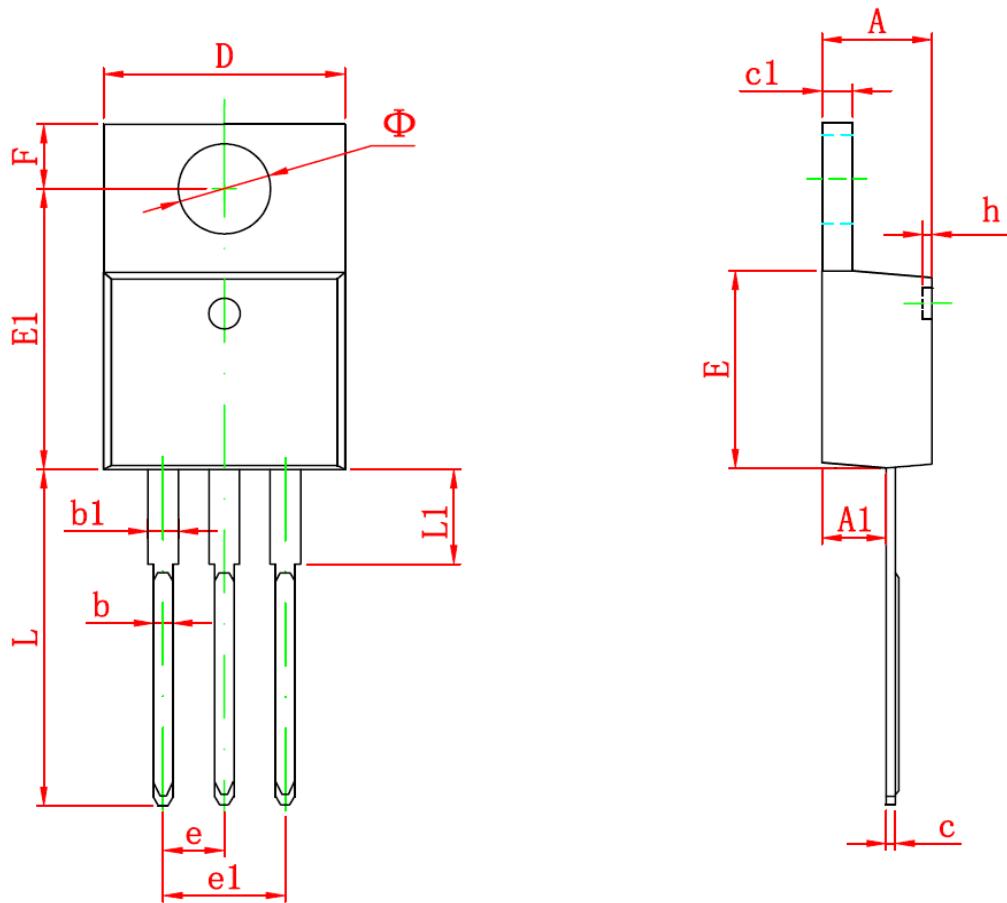
Unclamped Inductive Switching Test Circuit & Waveforms



Peak Diode Recovery dv/dt Test Circuit & Waveforms



**TO-220-3L PACKAGE OUTLINE DIMENSIONS**

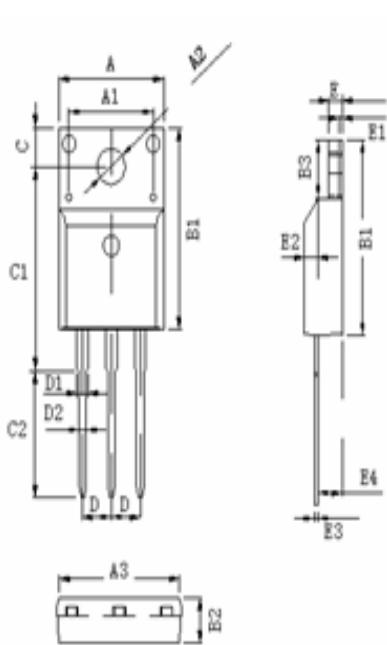


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	4.470	4.670	0.176	0.184
A1	2.520	2.820	0.099	0.111
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.310	0.530	0.012	0.021
c1	1.170	1.370	0.046	0.054
D	10.010	10.310	0.394	0.406
E	8.500	8.900	0.335	0.350
E1	12.060	12.460	0.475	0.491
e	2.540 TYP		0.100 TYP	
e1	4.980	5.180	0.196	0.204
F	2.590	2.890	0.102	0.114
h	0.000	0.300	0.000	0.012
L	13.400	13.800	0.528	0.543
L1	3.560	3.960	0.140	0.156
Φ	3.735	3.935	0.147	0.155

## Package Dimensions

TO-220F

### TO-220F 外形尺寸图



DIM.	MILLIMETERS
A	10.03 ± 0.20
A1	7.00
A2	3.12 ± 0.10
A3	9.70 ± 0.20
B1	15.75 ± 0.20
B2	4.72 ± 0.20
B3	6.70 ± 0.20
C	3.30 ± 0.10
C1	15.80 ± 0.20
C2	9.80 ± 0.2
D	Typical 2.54
D1	1.47 (MAX)
D2	0.80 ± 0.10
E	2.55 ± 0.20
E1	0.70
E2	1.00 × 45°
E3	0.50
E4	2.80 ± 0.20