

## P-Channel Enhancement Mode Power MOSFET

### **Description**

The HM5P02MR uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications.

#### **General Features**

•  $V_{DS} = -20V, I_{D} = -5.0A$ 

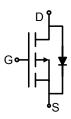
 $R_{DS(ON)}$  <35m $\Omega$  @  $V_{GS}$ =-2.5V

 $R_{DS(ON)}$  < 25m $\Omega$  @  $V_{GS}$ =-4.5V

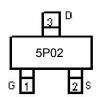
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

## **Application**

- PWM applications
- Load switch
- Power management



Schematic diagram



Marking and pin assignment



SOT-23!' @top view

## **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
5P02	HM5P02MR	SOT23-3L	Ø180mm	8 mm	3000 units

### Absolute Maximum Ratings (T<sub>A</sub>=25°Cunless otherwise noted)

Paramete	r	Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	-20	V	
Gate-Source Voltage		V <sub>G</sub> S	±12	V	
	T <sub>C</sub> =25°C		-5.0		
Continuous Drain Current	T <sub>C</sub> =70°C		-3.5	A	
Continuous Drain Current	T <sub>A</sub> =25°C	l <sub>D</sub>	-3	A	
	T <sub>A</sub> =70°C		-2.3		
Drain Current -Pulsed (Note 1)	·	I <sub>DM</sub>	-15	Α	
Maximum Power Dissipation		P <sub>D</sub>	1.7	W	
Operating Junction and Storage Tempe	erature Range	$T_{J}, T_{STG}$	-55 To 150	$^{\circ}$ C	

#### **Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	74	°C/W	l
--	-----------------	----	------	---

Page 1 v1.1



Electrical Characteristics (T<sub>A</sub>=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Off Characteristics	·						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-20	-	-	V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-20V,V <sub>GS</sub> =0V	-	-	-1	μΑ	
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±12V,V <sub>DS</sub> =0V	-	-	±100	nA	
On Characteristics (Note 3)	•			•		•	
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =-250μA	-0.6	-0.85	-1.2	V	
	-	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-5.0A	-	21.5	25		
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-3A	-	30	35	mΩ	
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =-5V,I <sub>D</sub> =-2A	6	-	1	S	
Dynamic Characteristics (Note4)	•			•		•	
Input Capacitance	C <sub>lss</sub>	\/ - 4\/\/ -0\/	-	740	-	PF	
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =-4V, $V_{GS}$ =0V, F=1.0MHz	-	290	-	PF	
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.UIVID2	-	190	-	PF	
Switching Characteristics (Note 4)	·						
Turn-on Delay Time	t <sub>d(on)</sub>		-	12	-	nS	
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =-4V, $I_{D}$ =-3.3A ,	-	35	-	nS	
Turn-Off Delay Time	t <sub>d(off)</sub>	$R_L$ =-1.2 $\Omega$ , $V_{GEN}$ =-4.5 $V$ , $R_g$ =1 $\Omega$	-	30	-	nS	
Turn-Off Fall Time	t <sub>f</sub>		-	10	-	nS	
Total Gate Charge	Qg		-	7.8	-	nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =-4V, $I_{D}$ =-5.0A, $V_{GS}$ =-4.5V	-	1.2	-	nC	
Gate-Drain Charge	$Q_{gd}$	]	-	1.6	-	nC	
Drain-Source Diode Characteristics			-	•		•	
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =-1.6A	-	-	-1.2	V	
Diode Forward Current (Note 2)	Is		-	-	1.6	Α	

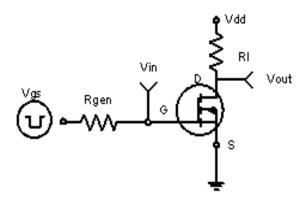
### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production

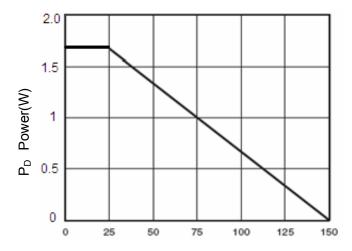
Page 2 v1.1



## **Typical Electrical and Thermal Characteristics**

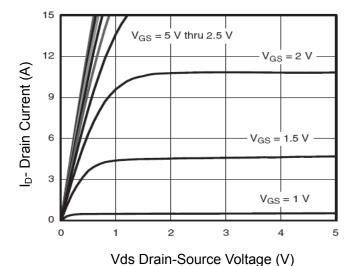


**Figure 1:Switching Test Circuit** 



T<sub>J</sub>-Junction Temperature(°C)

**Figure 3 Power Dissipation** 



**Figure 5 Output Characteristics** 

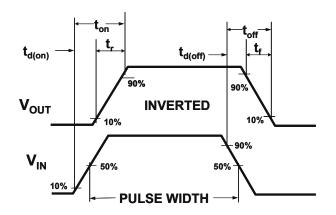
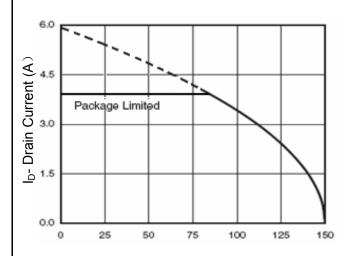
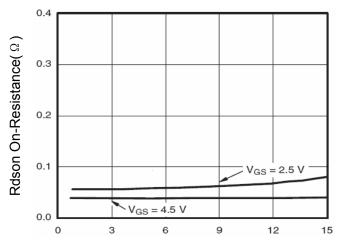


Figure 2:Switching Waveforms



 $T_J$ -Junction Temperature( $^{\circ}$ C)

Figure 4 Drain Current

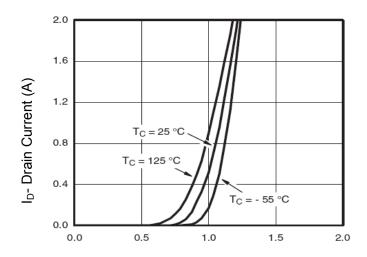


I<sub>D</sub>- Drain Current (A)

Figure 6 Drain-Source On-Resistance

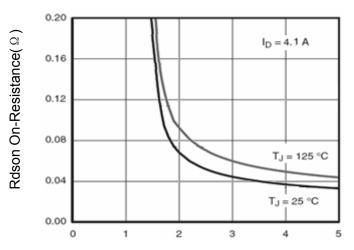
Page 3 v1.1





Vgs Gate-Source Voltage (V)

Figure 7 Transfer Characteristics



Vgs Gate-Source Voltage (V)

Figure 9 Rdson vs Vgs

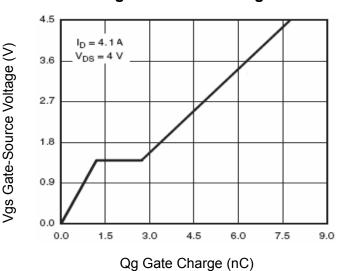
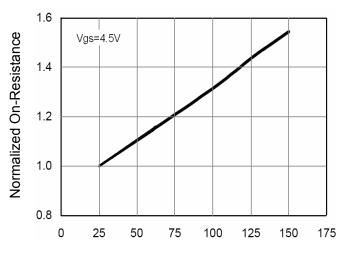
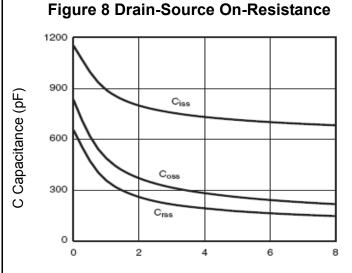


Figure 11 Gate Charge

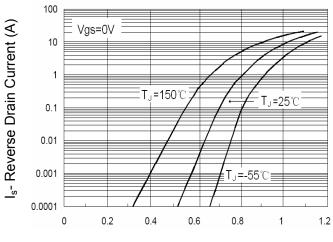


 $T_J$ -Junction Temperature( $^{\circ}$ C)



Vds Drain-Source Voltage (V)

Figure 10 Capacitance vs Vds



Vsd Source-Drain Voltage (V)

Figure 12 Source- Drain Diode Forward

Page 4 v1.1



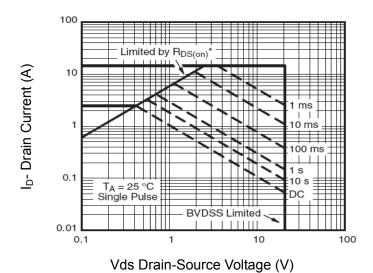
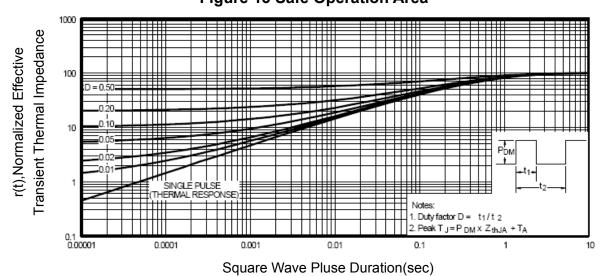


Figure 13 Safe Operation Area

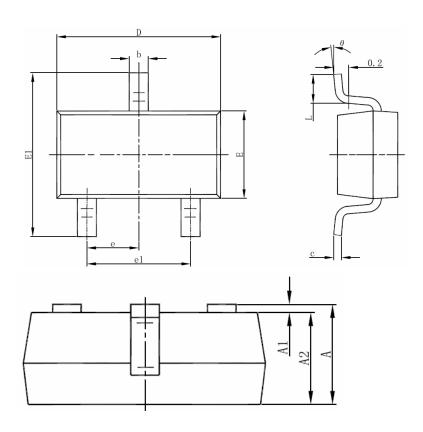


**Figure 14 Normalized Maximum Transient Thermal Impedance** 

Page 5 v1.1



# **SOT-23-3L PACKAGE INFORMATION**



Country of	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
С	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E	1.500	1.700	0.059	0.067	
E1	2.650	2.950	0.104	0.116	
е	0.950	(BSC)	0.037(BSC)		
e1	1.800	2.000	0.071	0.079	
Ĺ	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	

#### **NOTES**

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- $3.\ Package\ body\ sizes\ exclude\ mold\ flash\ and\ gate\ burrs.\ Mold\ flash\ at\ the\ non-lead\ sides\ should\ be\ less\ than\ 5\ mils.$
- 4. Dimension L is measured in gauge plane.
- 5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

Page 6 v1.1



#### ATTENTION:

- Any and all H&M SEMI products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your H&M SEMI representative nearest you before using any H&M SEMI products described or contained herein in such applications.
- H&M SEMI assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all H&M SEMI products described or contained herein.
- Specifications of any and all H&M SEMI products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- H&M Semiconductor CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all H&M SEMI products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of H&M Semiconductor CO.,LTD.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. H&M SEMI believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the H&M SEMI product that you intend to use.
- This catalog provides information as of Sep.2010. Specifications and information herein are subject to change without notice.