

N-Channel Super Trench Power MOSFET

Description

The HMS35DN10DA uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(on)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

Application

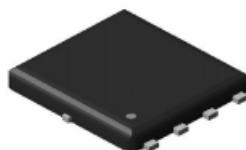
- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

General Features

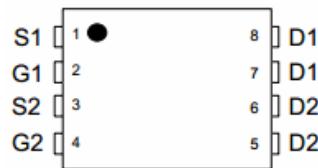
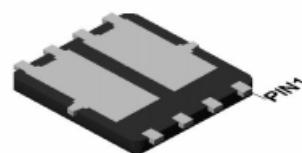
- $V_{DS} = 100V, I_D = 35A$
- $R_{DS(on)} = 18m\Omega$ (typical) @ $V_{GS} = 10V$
- $R_{DS(on)} = 22m\Omega$ (typical) @ $V_{GS} = 4.5V$
- Excellent gate charge x $R_{DS(on)}$ product(FOM)
- Very low on-resistance $R_{DS(on)}$
- 150 °C operating temperature
- Pb-free lead plating

100% UIS TESTED!

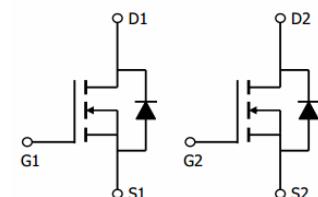
100% ΔVds TESTED!



DFN 5X6



Top View



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
HMS35DN10DA	HMS35DN10DA	DFN5X6-8L			

Absolute Maximum Ratings ($T_c=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	35	A
Drain Current-Continuous($T_c=100^\circ C$)	$I_D (100^\circ C)$	24.8	A
Pulsed Drain Current	I_{DM}	140	A
Maximum Power Dissipation	P_D	50	W
Derating factor		0.4	W/°C
Single pulse avalanche energy ^(Note 5)	E_{AS}	200	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	$R_{\theta JC}$	2.5	°C/W
--	-----------------	-----	------

Electrical Characteristics ($T_c=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	100	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}}=100\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$\text{V}_{\text{GS}}=\pm20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	1.2	2.0	2.8	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(ON)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=20\text{A}$	-	18	23	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=20\text{A}$	-	22	27	$\text{m}\Omega$
Forward Transconductance	g_{FS}	$\text{V}_{\text{DS}}=5\text{V}, \text{I}_D=20\text{A}$	-	35	-	S
Dynamic Characteristics (Note 4)						
Input Capacitance	C_{iss}	$\text{V}_{\text{DS}}=50\text{V}, \text{V}_{\text{GS}}=0\text{V},$ $F=1.0\text{MHz}$	-	1600	-	PF
Output Capacitance	C_{oss}		-	139	-	PF
Reverse Transfer Capacitance	C_{rss}		-	11	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{\text{d(on)}}$	$\text{V}_{\text{DD}}=50\text{V}, \text{I}_D=20\text{A}$ $\text{V}_{\text{GS}}=10\text{V}, \text{R}_G=1.6\Omega$	-	6	-	nS
Turn-on Rise Time	t_r		-	2	-	nS
Turn-Off Delay Time	$t_{\text{d(off)}}$		-	18	-	nS
Turn-Off Fall Time	t_f		-	2	-	nS
Total Gate Charge	Q_g	$\text{V}_{\text{DS}}=50\text{V}, \text{I}_D=20\text{A},$ $\text{V}_{\text{GS}}=10\text{V}$	-	26	-	nC
Gate-Source Charge	Q_{gs}		-	7.4	-	nC
Gate-Drain Charge	Q_{gd}		-	3.8	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_s=35\text{A}$	-		1.2	V
Diode Forward Current (Note 2)	I_s		-	-	35	A
Reverse Recovery Time	t_{rr}	$T_J = 25^\circ\text{C}, I_F = 20\text{A}$ $dI/dt = 500\text{A}/\mu\text{s}$ (Note 3)	-		26	nS
Reverse Recovery Charge	Q_{rr}		-		98	nC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production
5. EAS condition : $T_J=25^\circ\text{C}, V_{\text{DD}}=20\text{V}, V_G=10\text{V}, L=0.5\text{mH}, R_g=25\Omega$

Typical Electrical and Thermal Characteristics

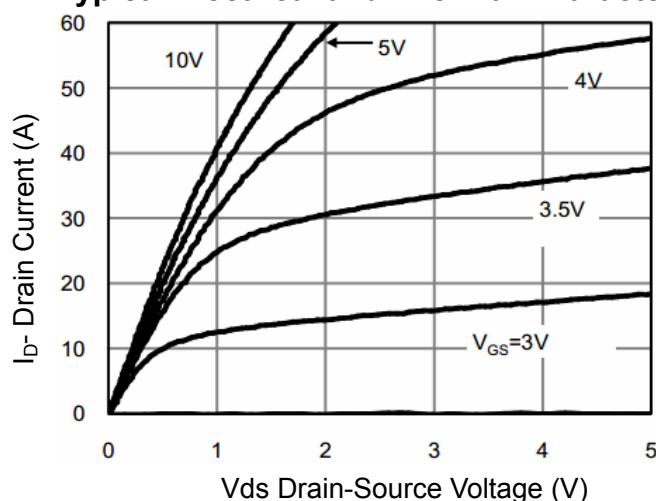


Figure 1 Output Characteristics

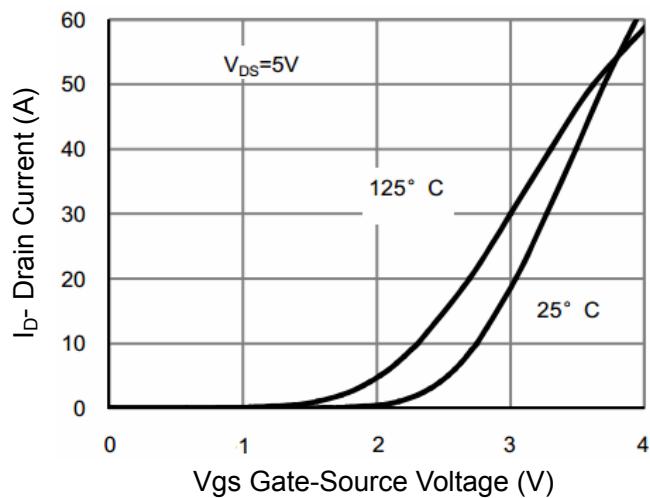


Figure 2 Transfer Characteristics

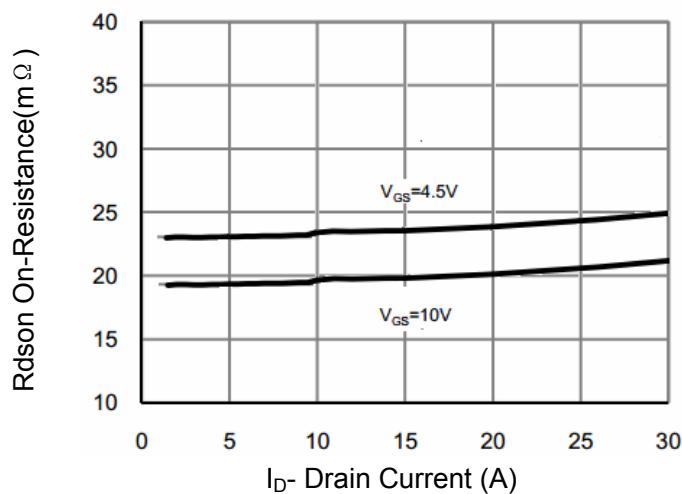


Figure 3 Rdson- Drain Current

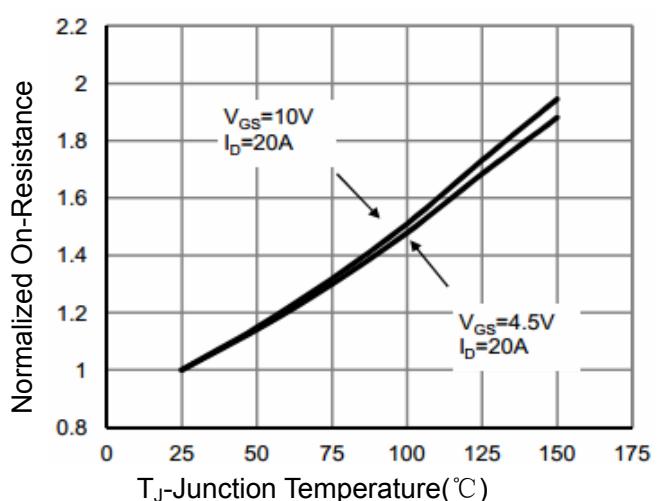


Figure 4 Rdson-Junction Temperature

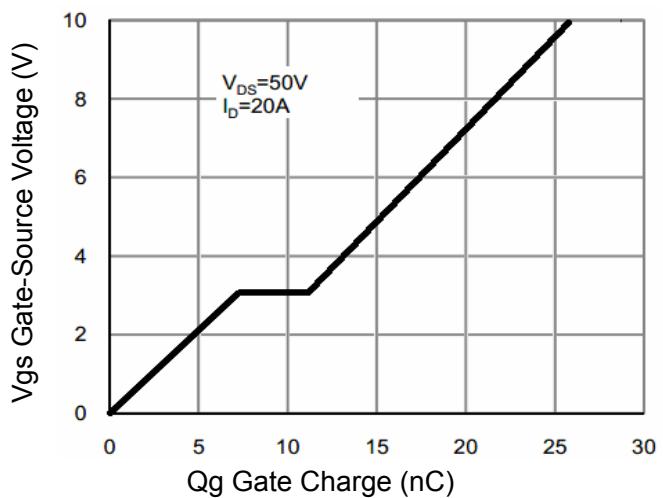


Figure 5 Gate Charge

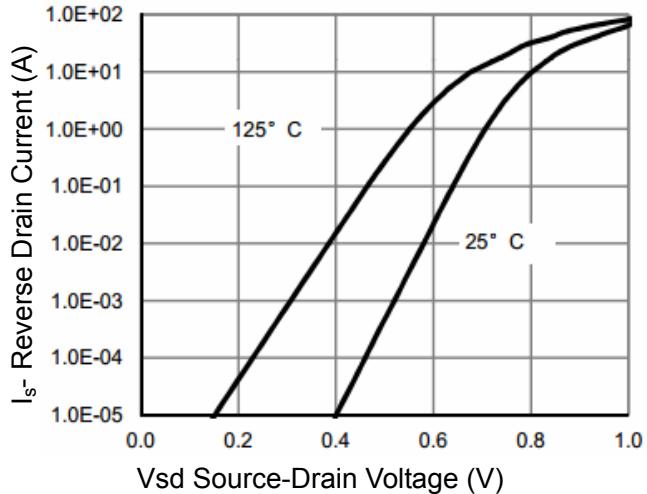


Figure 6 Source- Drain Diode Forward

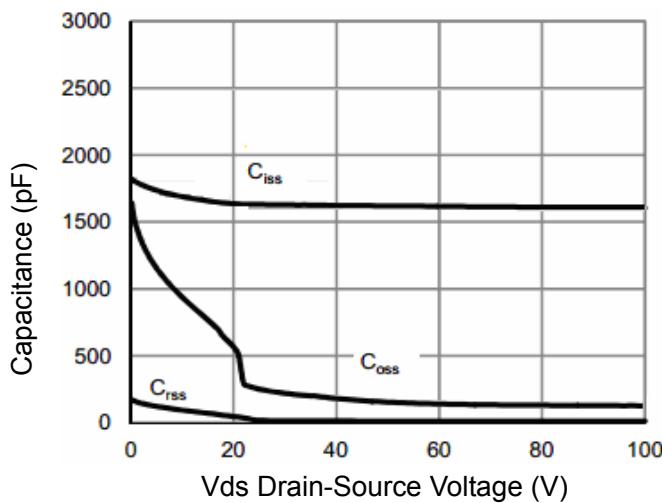


Figure 7 Capacitance vs Vds

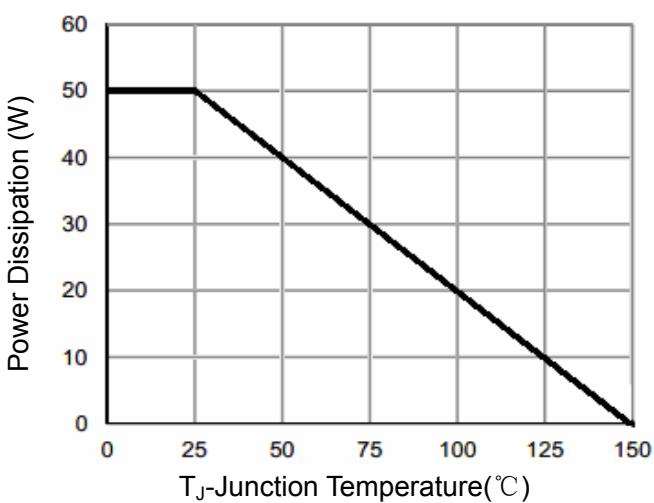


Figure 9 Power De-rating

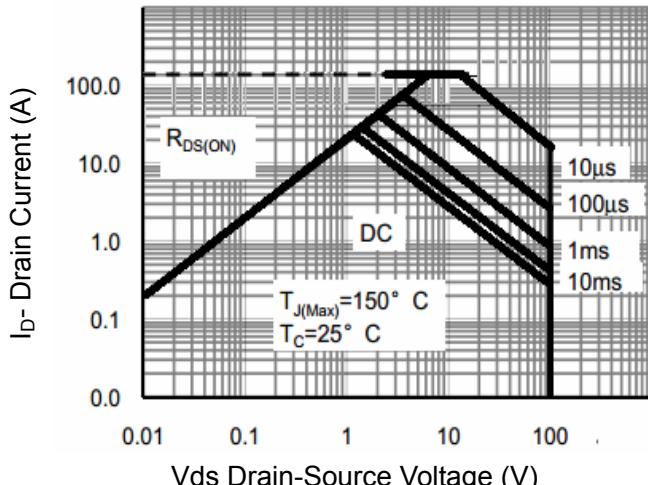


Figure 8 Safe Operation Area

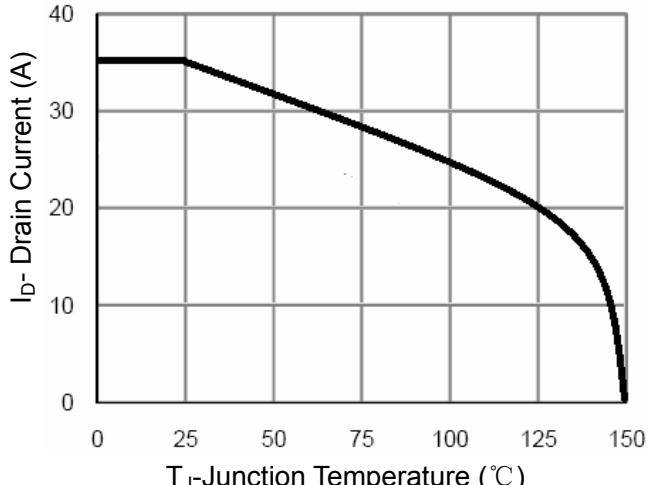


Figure 10 Current De-rating

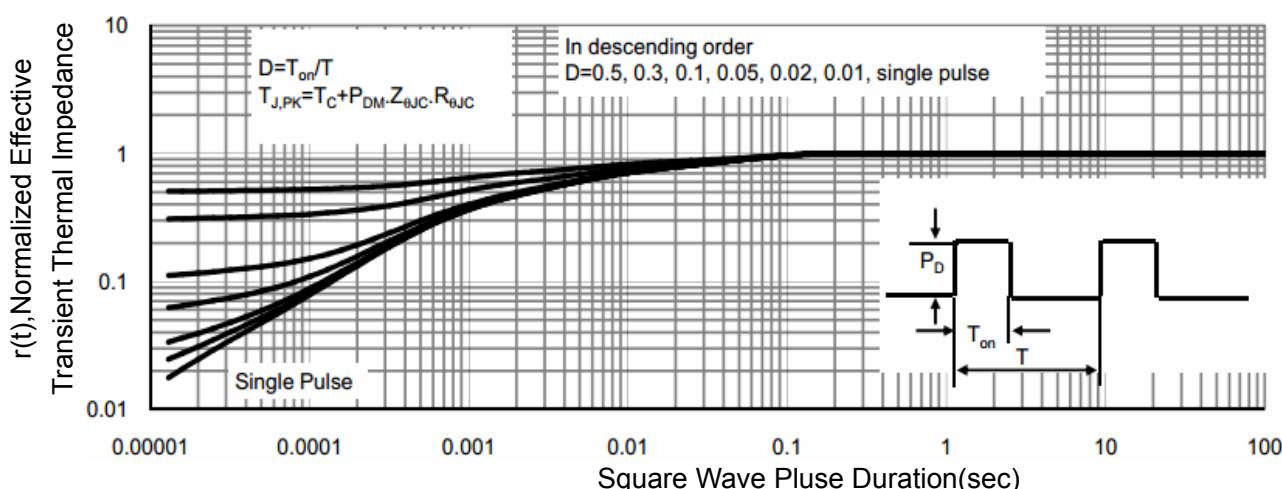
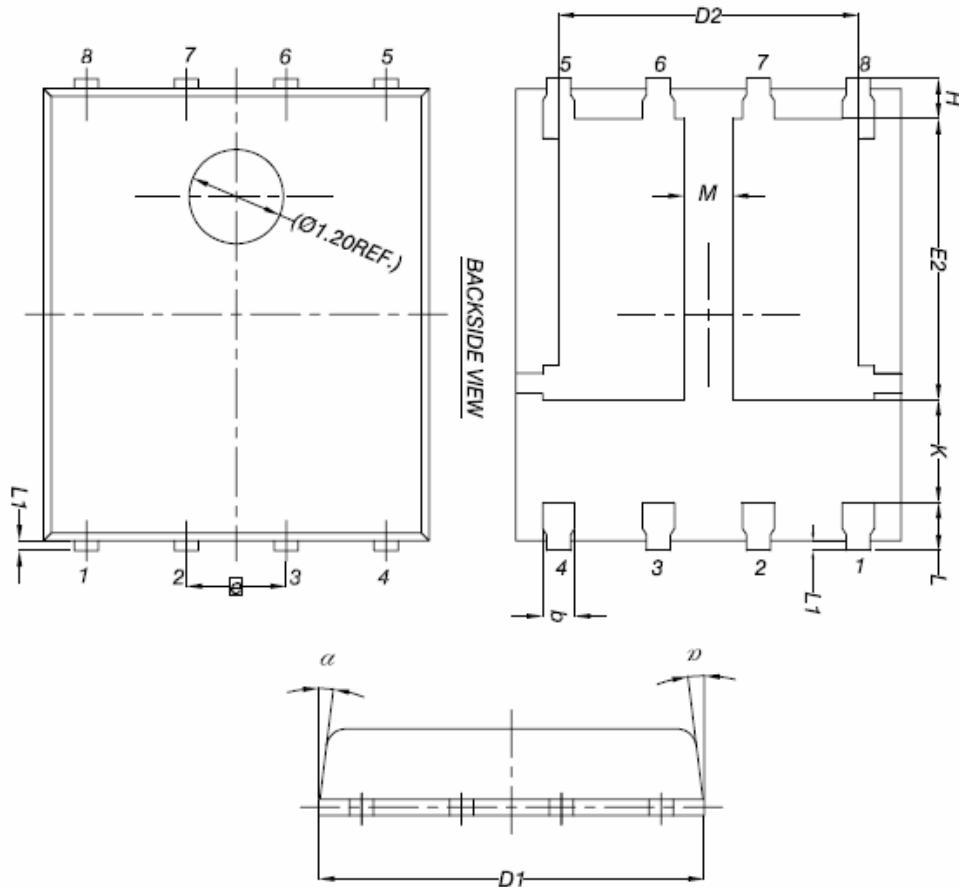


Figure 11 Normalized Maximum Transient Thermal Impedance

DFN5X6-8L Package Information



DIM.	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.10
b	0.33	0.41	0.51
C	0.20	0.25	0.30
D ₁	4.80	4.90	5.00
D ₂	3.61	3.81	3.96
E	5.90	6.00	6.10
E ₁	5.70	5.75	5.80
E ₂	3.38	3.58	3.78
e 1.27 BSC			
H	0.41	0.51	0.61
K	1.10	-	-
L	0.51	0.61	0.71
L ₁	0.06	0.13	0.20
M	0.50	-	-
α	0°	-	12°

