

## TEA2025BSOP16

## LINEAR INTEGRATED CIRCUIT

### STEREO AUDIO AMPLIFIER

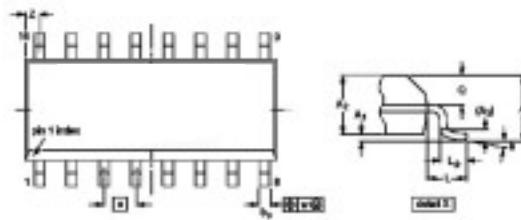
#### DESCRIPTION

The TEA2025B is a monolithic integrated audio amplifier in a 16-pin plastic dual in line package. It is designed for portable cassette players and radios.

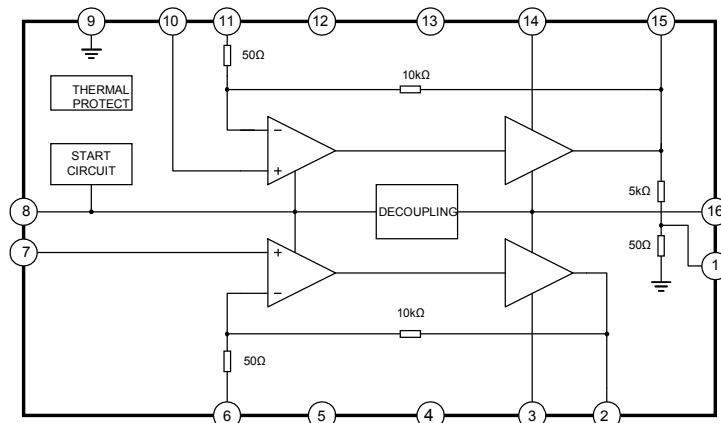


#### FEATURES

- \*Working Voltage down to 3V
- \*Few External components
- \*High Channel isolation
- \*Voltage gain up to 45dB(Adjustable with external resistor)
- \*Soft clipping
- \*Internal Thermal protection



#### BLOCK DIAGRAM



#### ABSOLUTE MAXIMUM RATINGS

Characteristic	Symbol	Value	Unit
Supply Voltage	V <sub>s</sub>	12	V
Output Peak Current	I <sub>o</sub>	1.5	A
Junction Temperature	T <sub>j</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-40--+150	°C

**ELECTRICAL CHARACTERISTICS**

(Tamb=25°C, Vcc=9V, Stereo, Unless otherwise specified)

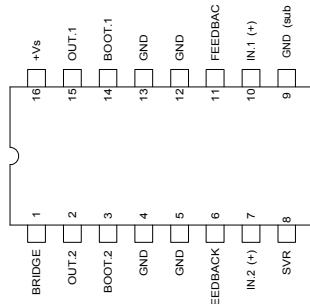
Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Supply Voltage	V <sub>s</sub>		3		12	V
Quiescent Current	I <sub>Q</sub>			40	50	mA
Quiescent output voltage	V <sub>O</sub>			4.5		V
Voltage gain	A <sub>V</sub>	Stereo	43	45	47	dB
		Bridge	49	51	53	
Voltage gain difference	ΔA <sub>V</sub>				±1	dB
Input impedance	R <sub>i</sub>			30		kΩ
Output Power	P <sub>O</sub>	f=1kHz;d=10% Stereo per channel Vcc=9V;RL=4Ω RL=8Ω	1.7	2.3		
		Vcc=6V;RL=4Ω RL=8Ω	0.7	1		
		Vcc=3V;RL=4Ω		0.6		W
		Bridge Vcc=9V;RL=8Ω		0.1		
		Vcc=6V;RL=4Ω		4.7		
Distortion	d	Vcc=9V;RL=4Ω f=1kHz;P <sub>O</sub> =250mW Stereo	-	0.3	1.5	%
		Bridge	-	0.5	-	
Supply voltage Rejection	SVR	R <sub>G</sub> =0;A <sub>V</sub> =45dB Vripple=150mVRMS Fripple=100Hz	40	46	-	dB
Input noise Voltage	V <sub>n</sub>	Av=200 Bandwidth: 20Hz to 20kHz				
		R <sub>G</sub> =0 R <sub>G</sub> =10kΩ	-	1.5	3	μV
Cross-Talk	C.T.	R <sub>G</sub> =10kΩ; f=1kHz;RL=4Ω P <sub>O</sub> =1W	40	55	-	dB

**THERMAL RESISTANCE**

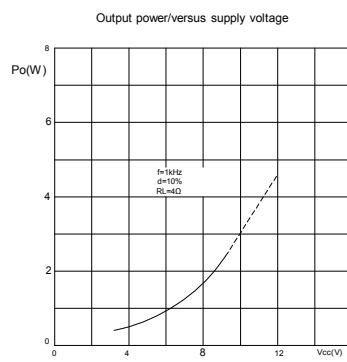
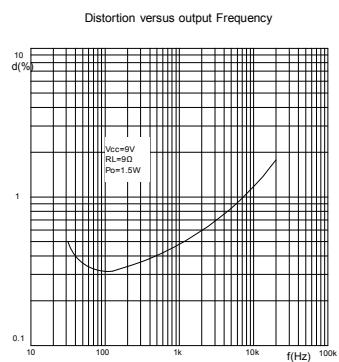
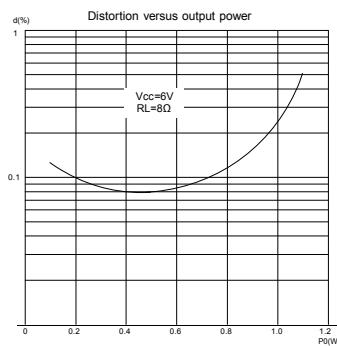
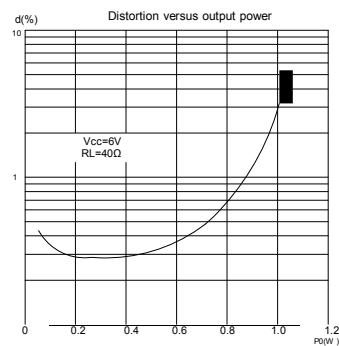
R<sub>th(j-c)</sub>:Junction to case thermal resistance 15°C /W

R<sub>th(j-a)</sub>:Junction to ambient thermal resistance 60°C /w

PIN CONNECTION



TYPICAL PERFORMANCE CHARACTERISTICS



APPLICATION CIRCUITS

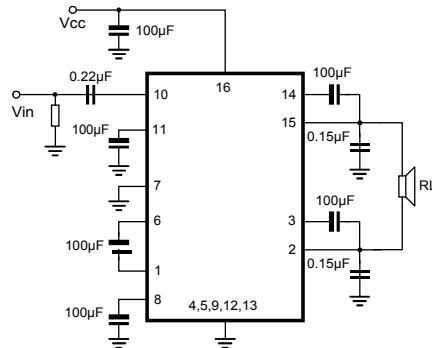


Fig. 5 Bridge Application

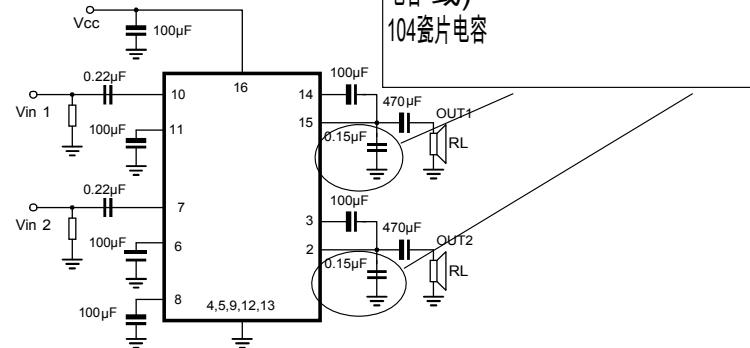


Fig.6 Stereo Application

SCHEMATIC DIAGRAM

