

P-Channel Enhancement Mode Power MOSFET

DESCRIPTION

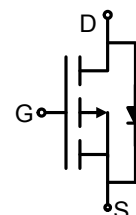
The HM45P03 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V.

GENERAL FEATURES

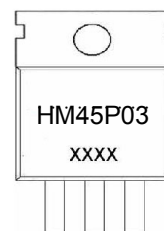
- $V_{DS} = -30V, I_D = -45A$
 $R_{DS(ON)} < 10m\Omega @ V_{GS} = -10V$
 $R_{DS(ON)} < 15m\Omega @ V_{GS} = -4.5V$
- High Power and current handling capability
- Lead free product is acquired
- Surface Mount Package

Application

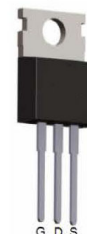
- Battery Switch
- Load switch
- Power management



Schematic diagram



Marking and pin assignment



TO-220-3L top view

Package Marking And Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
HM45P03	HM45P03	VUEGHS	Ø330mm	12mm	3000 units

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	±20	V
Drain Current-Continuous	I_D	-45	A
Drain Current-Pulsed (Note 1)	I_{DM}	-150	A
Maximum Power Dissipation	P_D	75	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	40	°C/W
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Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=-250\mu A$	-30	-33	-	V

Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V,V _{GS} =0V	-	-	-1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =-250μA	-1	-1.75	-2.2	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-10V, I _D =-10A	-	8	10	mΩ
		V _{GS} =-4.5V, I _D =-7.0A	-	11.5	15	mΩ
Forward Transconductance	g _{FS}	V _{DS} =-15V,I _D =-9.1A	10	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{iss}	V _{DS} =-15V,V _{GS} =0V, F=1.0MHz	-	1600	-	PF
Output Capacitance	C _{oss}		-	350	-	PF
Reverse Transfer Capacitance	C _{rss}		-	300	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}	V _{DD} =-15V, I _D =-1A, V _{GS} =-10V,R _{GEN} =6Ω	-	10	-	nS
Turn-on Rise Time	t _r		-	15	-	nS
Turn-Off Delay Time	t _{d(off)}		-	110	-	nS
Turn-Off Fall Time	t _f			70	-	nS
Total Gate Charge	Q _g	V _{DS} =-15V,I _D =-9.1A V _{GS} =-10V	-	30	-	nC
Gate-Source Charge	Q _{gs}		-	5.5	-	nC
Gate-Drain Charge	Q _{gd}		-	8	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-2.1A	-	-	-1.2	V

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS



Figure 1: Switching Test Circuit



Figure 2: Switching Waveforms

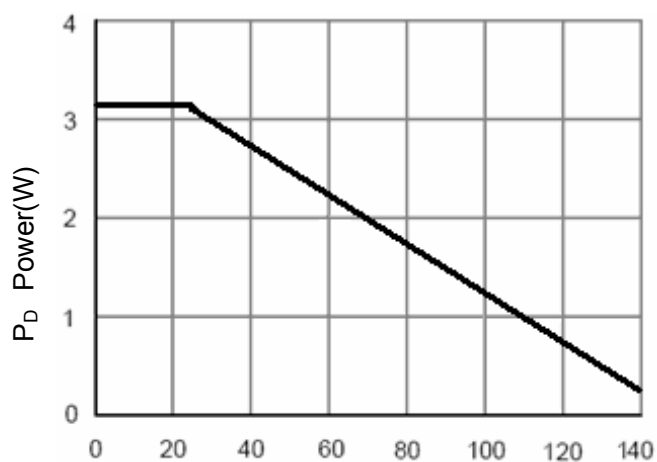


Figure 3 Power Dissipation

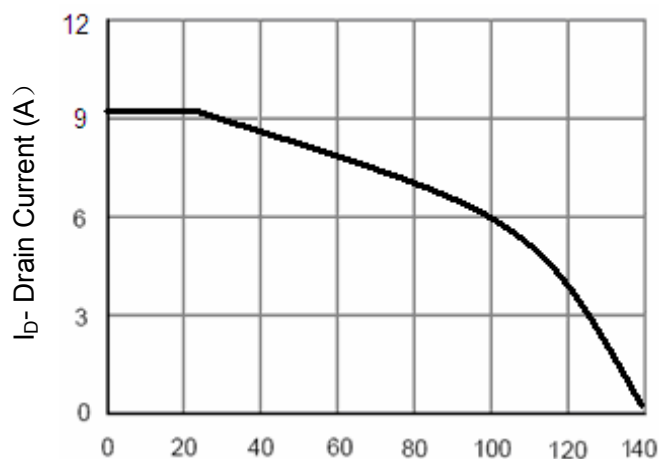


Figure 4 Drain Current

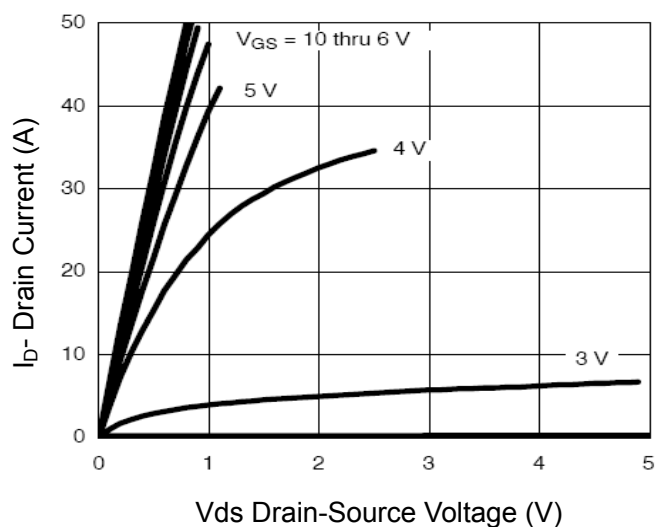


Figure 5 Output CHARACTERISTICS

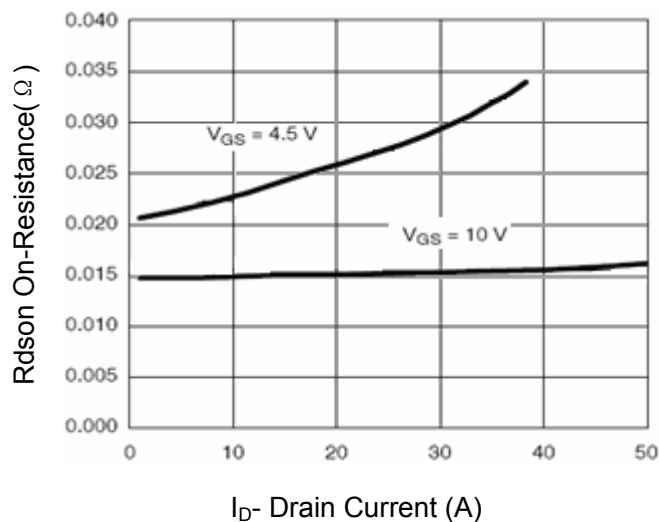


Figure 6 Drain-Source On-Resistance

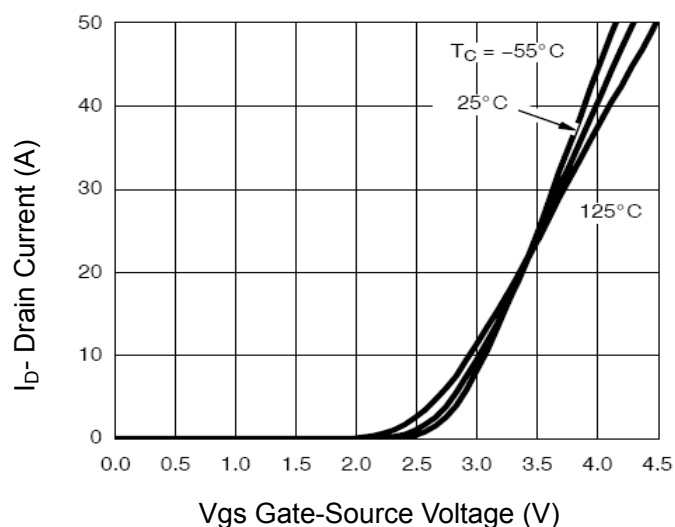


Figure 7 Transfer Characteristics

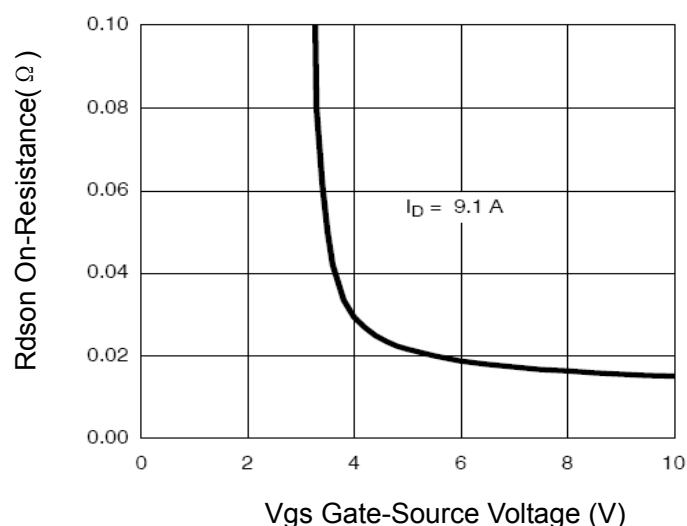


Figure 9 $R_{DS(on)}$ vs V_{GS}

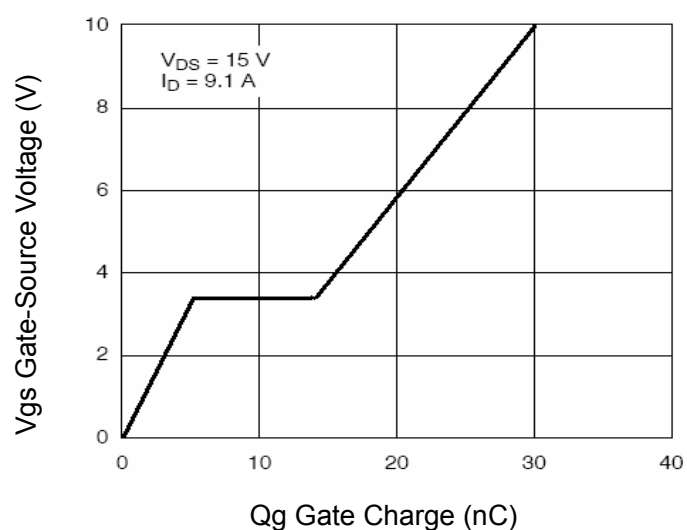


Figure 11 Gate Charge

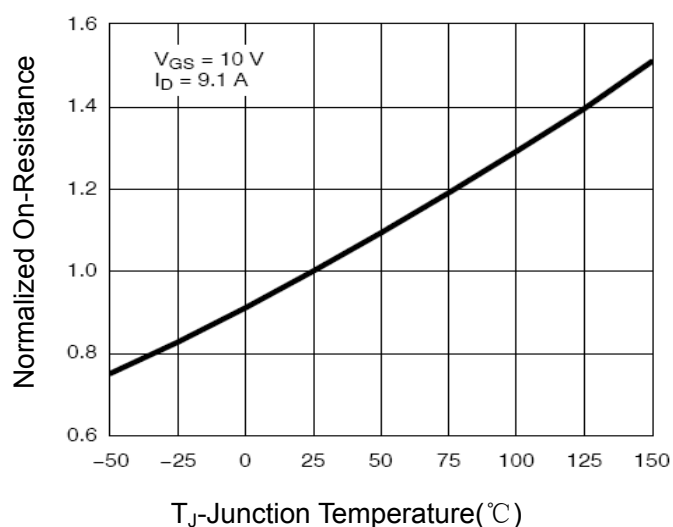


Figure 8 Drain-Source On-Resistance

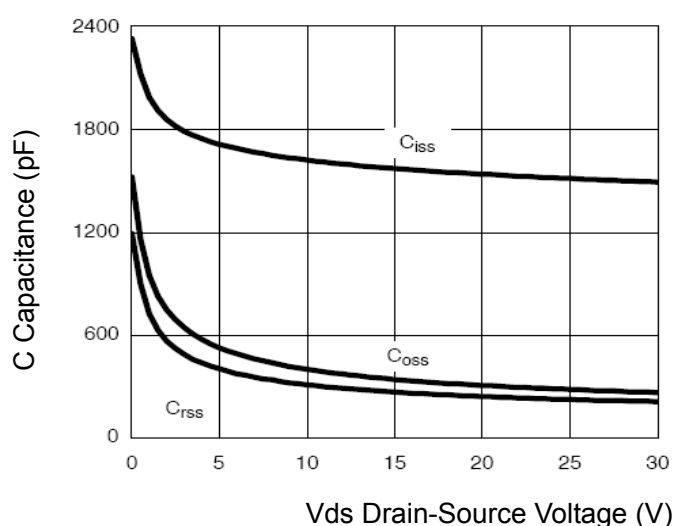


Figure 10 Capacitance vs V_{DS}

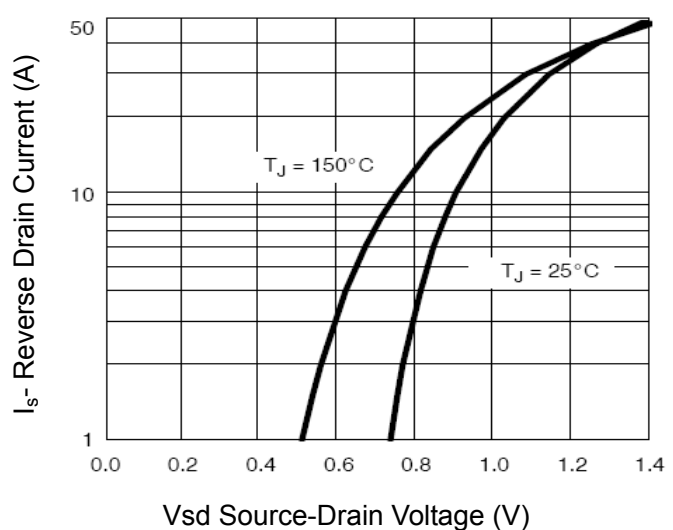


Figure 12 Source- Drain Diode Forward

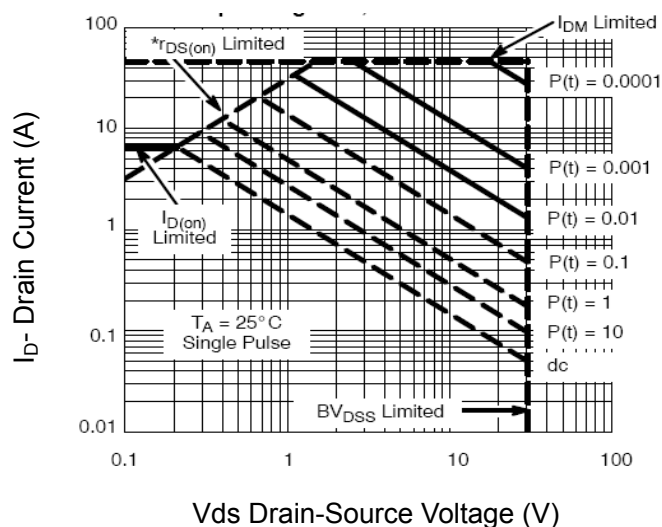


Figure 13 Safe Operation Area

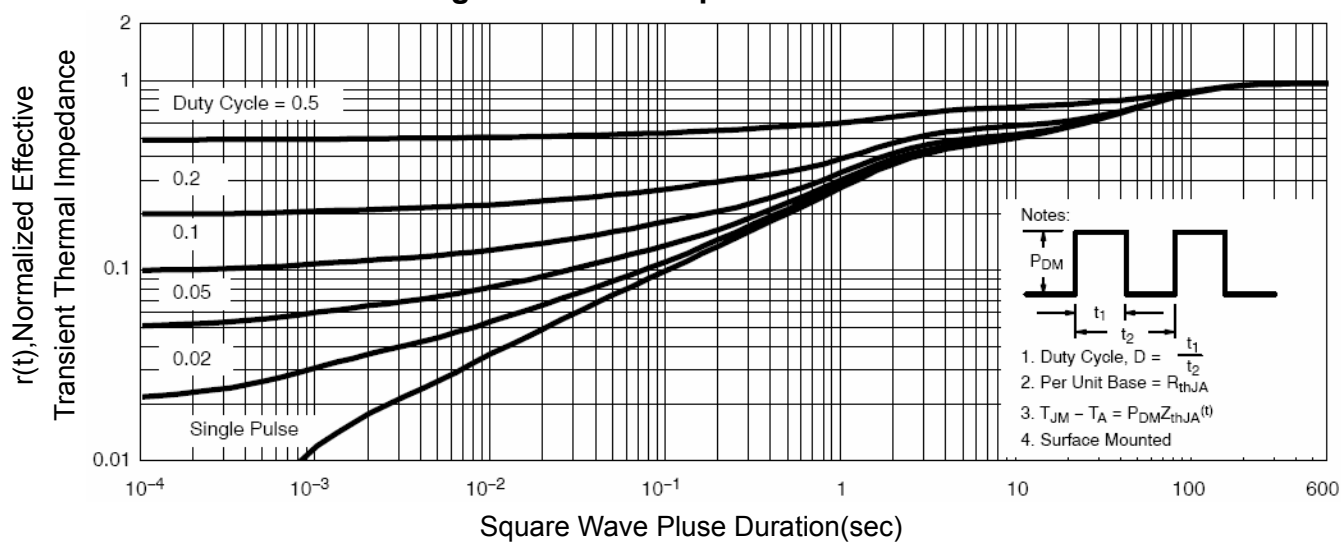
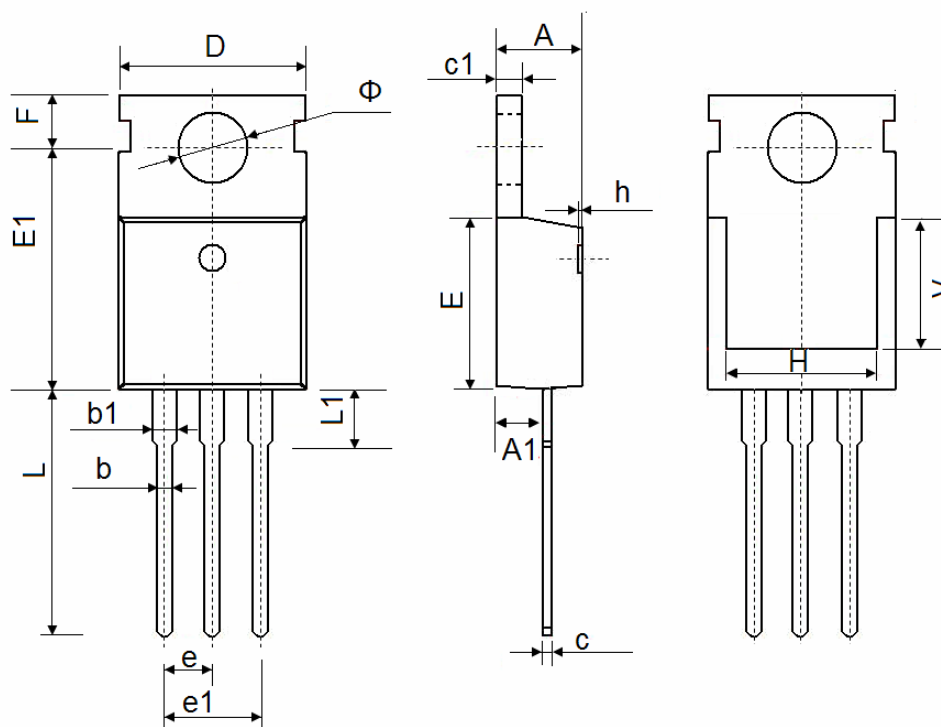


Figure 14 Normalized Maximum Transient Thermal Impedance

TO-220-3L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.400	4.600	0.173	0.181
A1	2.250	2.550	0.089	0.100
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.330	0.650	0.013	0.026
c1	1.200	1.400	0.047	0.055
D	9.910	10.250	0.390	0.404
E	8.9500	9.750	0.352	0.384
E1	12.650	12.950	0.498	0.510
e	2.540 TYP.		0.100 TYP.	
e1	4.980	5.180	0.196	0.204
F	2.650	2.950	0.104	0.116
H	7.900	8.100	0.311	0.319
h	0.000	0.300	0.000	0.012
L	12.900	13.400	0.508	0.528
L1	2.850	3.250	0.112	0.128
V	7.500 REF.		0.295 REF.	
Φ	3.400	3.800	0.134	0.150

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