

# 1.6MHz, 26V Step-up DC/DC Converter

## GENERAL DESCRIPTION

The PT F110 is a high frequency, high efficiency DC to DC converter with an integrated 3A, 0.1Ω power switch capable of providing an output voltage up to 26V. The fixed 1.6MHz allows the use of small external inductors and capacitors and provides fast transient response. It integrates Soft start, Comp., only need few components outside.

It can be adjusted SW current limit by one resistor or one analog voltage.

- 26V Boost converter with adjusted switch current
- 1.6Mhz fixed Switching Frequency
- Integrated soft-start
- Adjustable current limit
- Thermal Shutdown
- Under voltage Lockout
- SOT23-6 Package

## FEATURES

- 2.5V to 6V input voltage Range
- Efficiency up to 96%

## APPLICATIONS

- Handheld Devices
- GPS Receiver
- Digital Still Camera
- Portable Applications
- DSL Modem
- PCMCIA Card
- TFT LCD Bias Supply

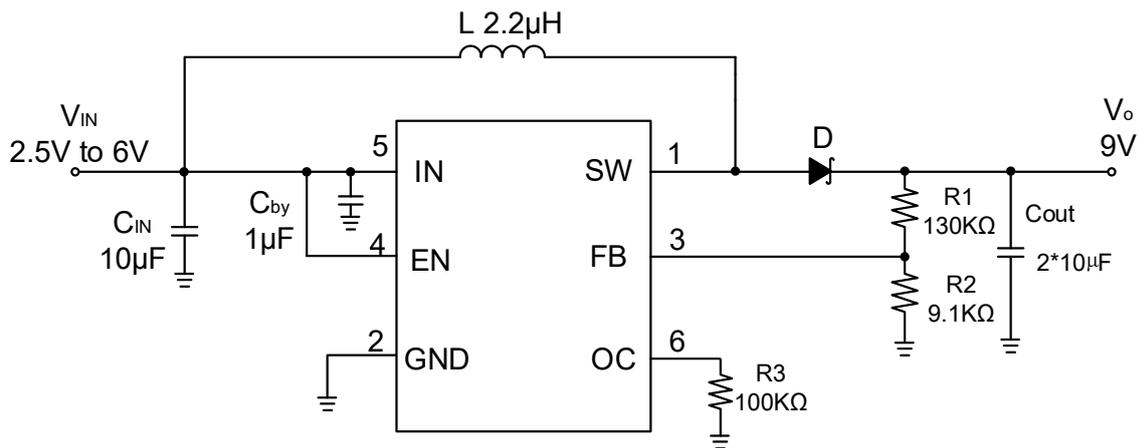


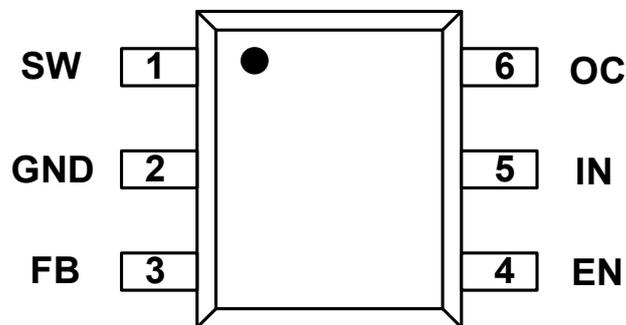
Figure 1. Typical Application Circuit1

## ORDERING INFORMATION

PART NUMBER	TEMP RANGE	SWITCHING FREQUENCY	OUTPUT VOLTAGE(V)	ILIM(A)	PACKAGE	MARK
PTF110	-40°C~85°C	0.6MHz	1.2V	1.0A	UOT23-6	XXYWW(note)

Note: 1)“YWW” is manufacture date code.  
 2)“XX” is internal product code of H&M Semi.

## PIN CONFIGURATION



TOP VIEW

Figure 2. PIN Configuration

## PIN DESCRIPTION

< A % ( +6 PIN NUMBER	PIN NAME	PIN DESCRIPTION
1	SW	Switch pin
2	GND	Ground
3	FB	Feedback pin
4	EN	Shutdown control input. Connect this pin to logic high level to enable the device
5	IN	Input power supply pin
6	OC	SW current limit setting pin, Set current limit by Rset or analog voltage

## ABSOLUTE MAXIMUM RATINGS

(NOTE: DO NOT EXCEED THESE LIMITS TO PREVENT DAMAGE TO THE DEVICE. EXPOSURE TO ABSOLUTE MAXIMUM RATING CONDITIONS FOR LONG PERIODS MAY AFFECT DEVICE RELIABILITY.)

PARAMETER	VALUE	UNIT
Supply Voltage VIN	-0.3 to 6.5	V
FB, EN Voltage	-0.3 to VIN+0.3	V
SW Voltage	Vin+0.3 to 28V	V
Operating Ambient Temperature	-40 to 85	°C
Maximum Junction Temperature	150	°C
Storage Temperature	-55 to 150	°C
Lead Temperature (Soldering, 10 sec)	300	°C

## FUNCTIONAL BLOCK DIAGRAM

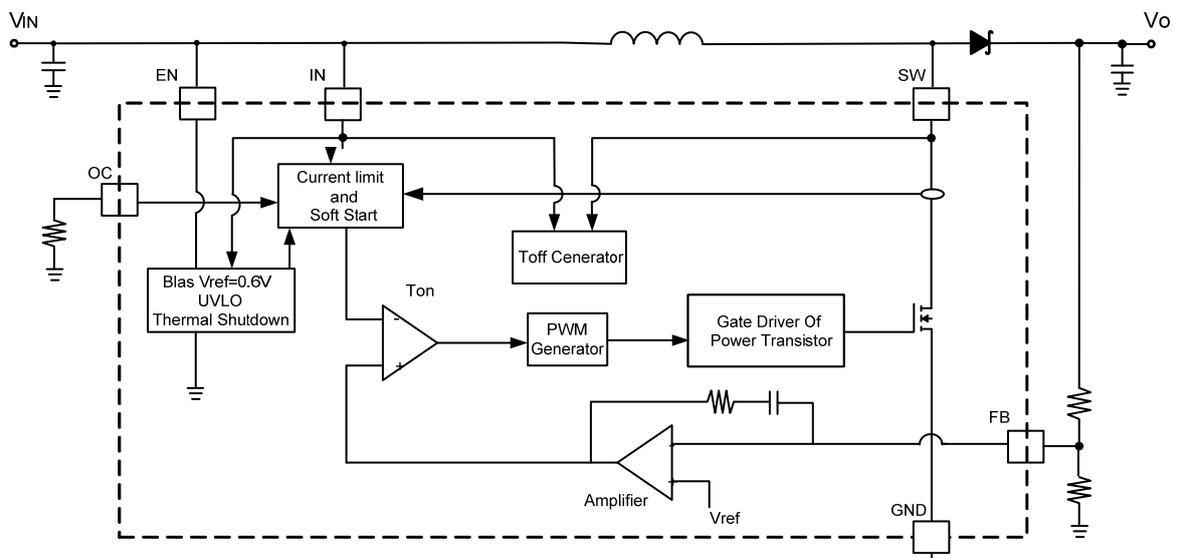


Figure 3. Functional Block Diagram

## ELECTRICAL CHARACTERISTICS

Typical and limits appearing in normal type apply for  $V_{IN}=3.6V$ ,  $T_A = 25^{\circ}C$ , unless otherwise specified.

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Voltage Range	$V_{IN}$		2.5		6.0	V
Boost output voltage range	$V_{OUT}$				26	V
UVLO Threshold	$V_{UVLO}$	$V_{HYSTERESIS} = 100mV$	2.1	2.2	2.3	V
Vin OVP voltage	$V_{ovp-VIN}$	$I_o=10mA$	6	6.1	6.2	V
Operating Supply Current	$I_{SUPPLY}$	$V_{FB}=0.7V$ , $EN=Vin$ , $I_{Load} = 0$		50	100	$\mu A$
Shutdown Supply Current		$V_{EN} = 0V$ , $V_{IN} = 4.2V$		0.02	1	$\mu A$
Regulated Feedback Voltage	$V_{FB}$		0.588	0.6	0.612	V
Peak Inductor Current limit (N-MOSFET Current limit)	$I_{lim}$	$R_{set}=51K$ $V_{in}=3.6V$ $V_{out}=9V$	1	1.3	1.6	A
Peak Inductor Current limit (N-MOSFET Current limit)	$I_{lim}$	$R_{set}=82K$ $V_{in}=3.6V$ $V_{out}=9V$	1.6	2.05	2.5	A
Oscillator Frequency	$F_{OSC}$		1.1	1.6	2.1	MHz
Rds(ON) of N-channel FET		$I_{SW} = -100mA$		0.1	0.2	Ohm
Enable OFF Threshold		$V_{EN}$ Falling			0.3	V
Enable ON Threshold		$V_{EN}$ Rising	1.5			V
Enable Leakage Current*			-0.1		0.1	$\mu A$
SW Leakage Current*		$V_{EN} = 0V$ , $V_{SW} = 0V$ or $5V$ , $V_{IN} = 5V$			1	$\mu A$

Note1: \*---The parameter is guaranteed by design.

## FUNCTIONAL DESCRIPTION

### Normal operating mode

The boost converter is designed for output voltage up to 26V with a switch MAX peak current limit of 3 A. The device, which operates in a current mode scheme with quasi-constant frequency, is externally 1.6MHz and the minimum input voltage is 2.5V. To control the inrush current at start-up a soft-start pin is available.

During the on-time, the voltage across the inductor causes the current in it to rise. When the current reaches a threshold value set by the internal GM amplifier, the power transistor is turned off, the energy stored into the inductor is then released and the current flows through the Schottky diode towards the output of the boost converter. The off-time is fixed for a certain  $V_{in}$  and  $V_s$ , and therefore maintains the same frequency when varying these parameters.

However, for different output loads, the frequency may slightly change due to the voltage drop across the  $R_{dson}$  of the power transistor which will have an effect on the voltage across the inductor and thus on  $T_{ON}$  ( $T_{OFF}$  remains fixed). Some slight frequency changes might also appear with a fixed output load due to the fact that the output voltage  $V_s$  is not sensed directly but via the SW Pin, which affects accuracy.

Because of the quasi-constant frequency behavior of the device, the PTFIIO eliminates the need for an internal oscillator and slope compensation, which provides better stability for the system over a wide of input and output voltages range, and more stable and accurate current limiting operation compared to boost converters operating with a conventional PWM scheme. The PTFIIO topology has also the benefits of providing very good load and line regulations, and excellent load transient response.

### Undervoltage lockout (UVLO)

To avoid mis-operation of the device at low input voltages an under voltage lockout is included that disables the device, if the input voltage falls below 2.4V.

### Thermal shutdown

A thermal shutdown is implemented to prevent damages due to excessive heat and power dissipation. Typically the thermal shutdown threshold is 150°C. When the thermal shutdown is triggered the device stops switching until the temperature falls typically 136°C. Then the device starts switching again.

### OC SETTING ( SETTING CURRENT LIMIT BY ONE RESISTOR)

HM1547B can be adjusted SW current limit by one resistor connected with OC pin just like Typical Application Circuit2. The setting sheet is as below(  $V_{in}=3.6V, V_{out}=9V$  )---Only list the typical  $I_{lim}$  number, the actual data may be in +/- 20% above them because of some discrete data from IC and resistor. Please do not let it float. Typically choose  $R_{set}$  and determine  $I_{lim}$  from the following equation:

$$I_{LIM} = \frac{0.25 * 10^{-9} * R_{set}}{K} \text{ and } K = 1 * 10^{-5}$$

RSET(OHM)	ILIM(TYP)
100K	2.5A
91K	2.275A
82K	2.05A
62K	1.55A
51K	1.275A
39K	0.975A
30K	0.75A
20K	0.5A

For 3.6V~4.2Vin 2A lin to 9V out, 82Kohm is suitable. If the current limit exceeds 2 A, maybe the IC will be go in to over temperature protection status without good layout.

## APPLICATION INFORMATION

### INDUCTOR SELECTION

In normal operation, the inductor maintains continuous current to the output. The inductor current has a ripple that is dependent on the inductance value. The high inductance reduces the ripple current.

**Selected inductor by actual application:**

Manufacturer	Part Number	Inductance(uH)	DRC max (Ohms)	Dimensions L*W*H (mm3)
Murata	LQH44PN	2.2	0.049	4*4*1.7
		3.3	0.065	
		4.7	0.08	
	LQH5BP	2.2	0.030	5*5*2
		3.3	0.044	
		4.7	0.058	
TDK	SPM6530T	2.2	0.017	7.1*6.5*3
		3.3	0.027	
		4.7	0.036	
	VLP6045 LT	2.2	0.020	6*6*4.5
		3.3	0.025	
		4.7	0.029	
WURTH	744373324022	2.2	0.061	4.4*4.05
	744777004	4.7	0.025	7.3*7.3*4.5

**Table 1. Recommend Surface Mount Inductors**

If output voltage is 5V, you can use 2.2uH, If output voltage is 9V, 2.2uH~ 4.7uH is OK, if 24V ,maybe need 4.7uH

Normal application: Input 3.3V (3.6V or 4.2V) to Output 5V 9V 12V 24V;

Input 5V to Output 9V 12V 24V;

Notes: Please select inductor according to I in. The IL need to be 1.5~2\*I in. For getting higher efficiency, need to use low DRC inductors.

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### INPUT CAPACITOR SELECTION

The input capacitor reduces input voltage ripple to the converter, low ESR ceramic capacitor is highly recommended. For most applications, a 10uF capacitor is used. The input capacitor should be placed as close as possible to VIN and GND. Such as Murata GRM21BR60J106 or TDK C3216X5R1A106M.

### OUTPUT CAPACITOR SELECTION

A low ESR output capacitor is required in order to maintain low output voltage ripple. In the case of ceramic output capacitors, capacitor ESR is very small and does not contribute to the ripple, so a lower capacitance value is acceptable when ceramic capacitors are used. A 10uF/22uF or two 10uF ceramic output capacitor is suitable for most applications. Such as Murata GRM21BR60J226\ GRM21BR60J106 or TDK C3216X5R1A226M\ C3216X5R1A106M.

### OUTPUT VOLTAGE PROGRAMMING

In the adjustable version, the output voltage is set by a resistive divider according to the following equation: Typically choose R2=10K and determine R1 from the following equation:

$$R1 = R2 * \left( \frac{V_{out}}{0.6} - 1 \right)$$

### Diode SELECTION

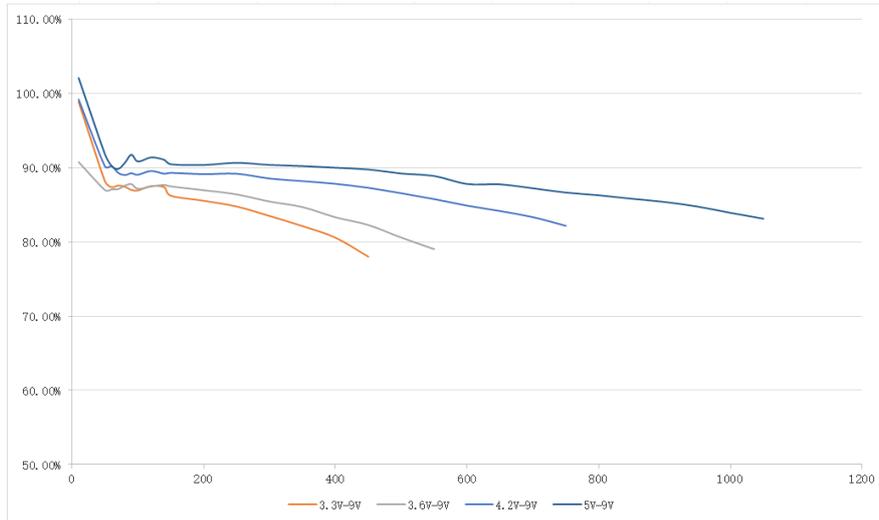
According to max Iout and max Vout, you can select suitable diode.

Normally we select diode If=(1.5~2)\*Ioutmax and VR=(1.5~2)\*Voutmax. For high efficiency, suggest that you select low Vf Schottky diode. ONsemi's MBRA210LT3 is prefer.

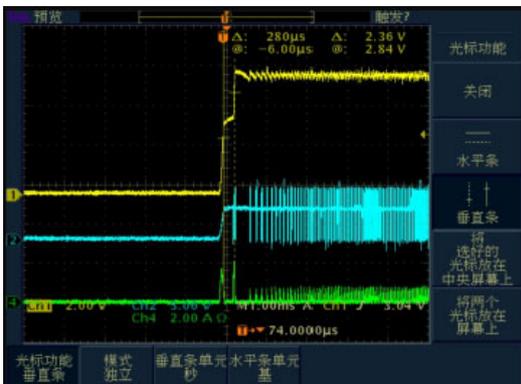
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## TYPICAL PERFORMANCE CHARACTERISTICS

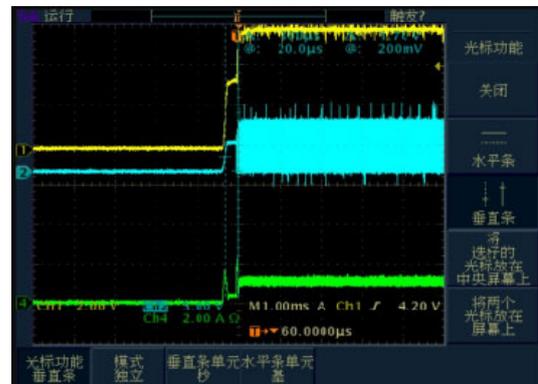
(L=2.2uH, Ilim=2.4A, CIN=10uF, COUT=2\*10uF, if not mentioned)



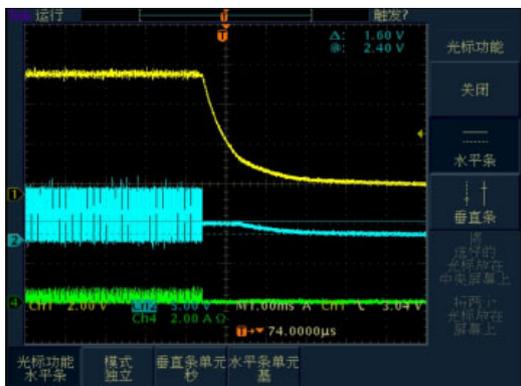
### START UP & SHUT DOWN VS. OUTPUT CURRENT



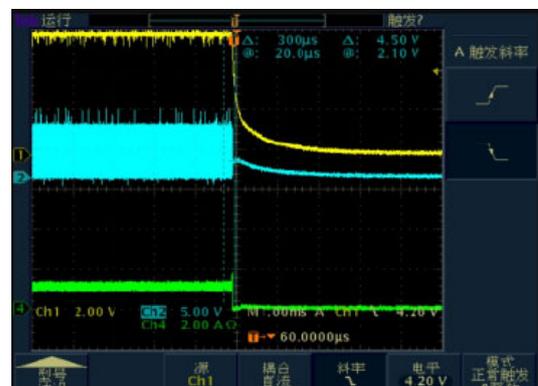
STARTUP (3.3V IN 9V 50MA OUT)



STARTUP (3.3V IN 9V 500MA OUT)

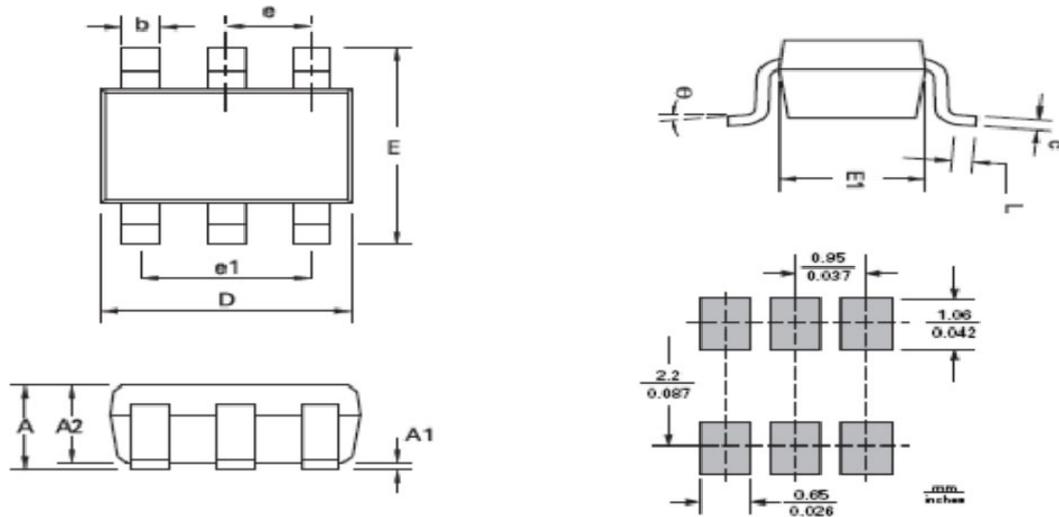


SHUTDOWN (3.3V IN 9V 50MA OUT)



SHUTDOWN (3.3V IN 9V 500MA OUT)

**PACKAGE OUTLINE(SOT23-6)**



DM	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	0.90	1.45	0.0354	0.057
A1	0.00	0.15	0.00	0.0059
A2	0.90	1.30	0.0354	0.0511
b	0.30	0.50	0.0118	0.0196
C	0.09	0.26	0.0035	0.0102
D	2.70	3.10	0.1063	0.1220
E	2.20	3.20	0.0866	0.1260
E1	1.30	1.80	0.0512	0.0709
L	0.10	0.60	0.0039	0.0236
e	0.95 REF		0.0374 REF	
e1	1.90 REF		0.0748 REF	
L	0°	30°	0°	30°