

High Voltage Low Power Consumption LDO

" HM7218 Series

CMOS Voltage Regulator

1A

HM7218 series is a high voltage (up to 15V) ultra-low quiescent current low dropout voltage regulator (LDO) manufactured in CMOS processes. It can deliver up to 1A of current while consuming only 1.6uA of quiescent current. It consists of a reference voltage generator, an error amplifier, a current foldback circuit, and a phase compensation circuit plus a driver transistor. The HM7218 series is designed specifically for applications where very-low I_Q is a critical parameter. This device maintains low quiescent current consumption even in dropout mode to further increase the battery life.

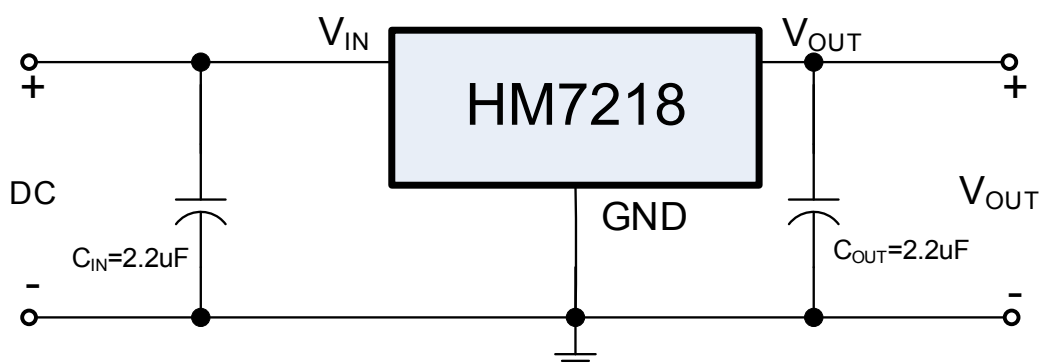
■ Features:

- Ultra-low Quiescent Current: 1.6uA
- Maximum Input Voltage: 15V
- Output Voltage Highly Accurate: $\pm 2\%$
- Maximum Output Current: 1A
- Dropout Voltage: 650mV@ $I_{OUT}=1A$
- Temperature Stability: $\pm 50\text{ppm}/^\circ\text{C}$
- Protections Circuits: Current Limiter, Short Circuit, Foldback, Thermal shutdown
- Output Capacitor: Low ESR Ceramic Capacitor Compatible

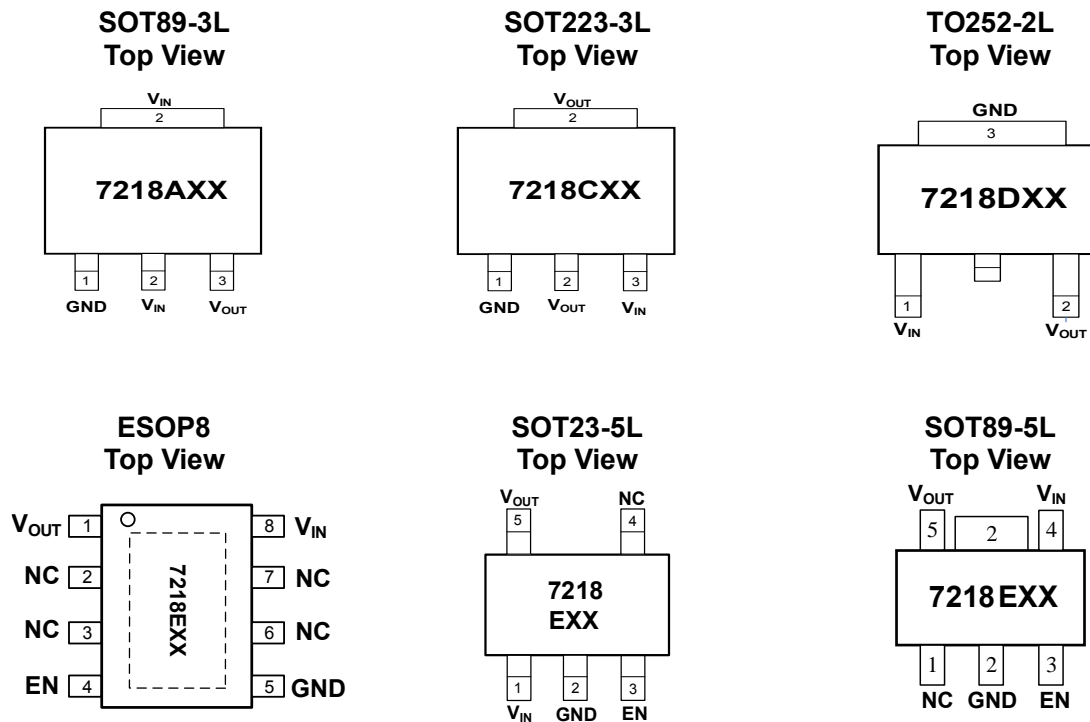
■ Applications:

- Smart wearer
- Long-life battery-powered devices
- Portable mobile devices, such as mobile phones, cameras, and so on
- Wireless communication equipment

■ Typical Application:



■ Pin Configuration (Top View):



■ Product Selections

Product Name	V_{OUT} (V)	Package	Ordering Name	Marking	Package Information
HM7218A30ÜÜ	3.0	SOT89-3L	HM7218A30ÜÜ	7218A30	Tape and Reel, 1000pcs
HM7218A33ÜÜ	3.3	SOT89-3L	HM7218A33ÜÜ	7218A33	
HM7218A36ÜÜ	3.6	SOT89-3L	HM7218A36ÜÜ	7218A36	
HM7218A38ÜÜ	3.8	SOT89-3L	HM7218A38ÜÜ	7218A38	
HM7218A40ÜÜ	4.0	SOT89-3L	HM7218A40ÜÜ	7218A40	
HM7218A50ÜÜ	5.0	SOT89-3L	HM7218A50ÜÜ	7218A50	
HM7218AC0ÜÜ	12.0	SOT89-3L	HM7218AC0ÜÜ	7218AC0	
HM7218E33ÜÍ	3.3	SOT89-5L	HM7218E33ÜÍ	7218E33	
HM7218E36ÜÍ	3.6	SOT89-5L	HM7218E36ÜÍ	7218E36	
HM7218E50ÜÍ	5.0	SOT89-5L	HM7218E50ÜÍ	7218E50	Tape and Reel, 2500pcs
HM7218C30	3.0	SOT223-3L	HM7218C30	7218C30	
HM7218C33	3.3	SOT223-3L	HM7218C33	7218C33	
HM7218C36	3.6	SOT223-3L	HM7218C36	7218C36	
HM7218C40	4.0	SOT223-3L	HM7218C40	7218C40	
HM7218C50	5.0	SOT223-3L	HM7218C50	7218C50	
HM7218CC0	12.0	SOT223-3L	HM7218CC0	7218CC0	Tape and Reel, 2500pcs
HM7218D30V	3.0	TO252-2L	HM7218D30V	7218D30	
HM7218D33V	3.3	TO252-2L	HM7218D33V	7218D33	
HM7218D36V	3.6	TO252-2L	HM7218D36V	7218D36	
HM7218D40V	4.0	TO252-2L	HM7218D40V	7218D40	
HM7218D50V	5.0	TO252-2L	HM7218D50V	7218D50	
HM7218D80V	8.0	TO252-2L	HM7218D80V	7218D80	
HM7218DC0V	12.0	TO252-2L	HM7218DC0V	7218DC0	

HM7218E300Ü	3.0	ESOP8	HM7218E300Ü	7218E30	Tape and Reel, 4000pcs
HM7218E330Ü	3.3	ESOP8	HM7218E330Ü	7218E33	
HM7218E360Ü	3.6	ESOP8	HM7218E360Ü	7218E36	
HM7218E400Ü	4.0	ESOP8	HM7218E400Ü	7218E40	
HM7218E500Ü	5.0	ESOP8	HM7218E500Ü	7218E50	
HM7218EC00Ü	12.0	ESOP8	HM7218EC00Ü	7218EC0	Tape and Reel, 3000pcs
HM7218E30T Ü	3.0	SOT23-5L	HM7218E30T Ü	7218E30	
HM7218E33T Ü	3.3	SOT23-5L	HM7218E33T Ü	7218E33	
HM7218E36T Ü	3.6	SOT23-5L	HM7218E36T Ü	7218E36	
HM7218E40T Ü	4.0	SOT23-5L	HM7218E40T Ü	7218E40	
HM7218E50T Ü	5.0	SOT23-5L	HM7218E50T Ü	7218E50	

Notes:

1* Customer can request to customize the output voltage ranged from 1.8V to 12V, if desired voltage is not found in the selections.

2* Customer can request customization of package choice.

3* Please pay attention to the MARKING of the product package type.

■ **Absolute Maximum Ratings** (Unless otherwise indicated: $T_a=25^{\circ}\text{C}$)

PARAMETER	SYMBOL	RATINGS		UNITS
Input Voltage	V _{IN}	-0.3 ~ 15		V
Output Voltage	V _{OUT}	V _{SS} -0.3 ~ V _{IN} +0.3V		
Power Dissipation	P _D	SOT89-3L	1000	mW
		SOT89-5L	1000	
		SOT223-3L	1500	
		TO252-2L	1800	
		ESOP8	800	
		SOT23-5L	250	
Thermal Resistance	R _{θJB} ⁽¹⁾	SOT89-3L	100	°C/W
		SOT89-5L	100	
		SOT223-3L	66	
		TO252-2L	55	
		ESOP8	80	
		SOT23-5L	180	
Operating Ambient Temperature	T _{opr}	-40 ~ +85		°C
Storage Temperature	T _{stg}	-40 ~ +125		
ESD Protection	ESD HBM	4000		V

Note: Stresses above those listed under “Absolute Maximum Ratings” may cause permanent damage to the device.

⁽¹⁾ Mounted on JEDEC standard 4layer (2s2p) PCB test board

■ **Electrical Characteristics:**

HM7218 Series (Unless otherwise indicated: $T_a=25^{\circ}\text{C}$)

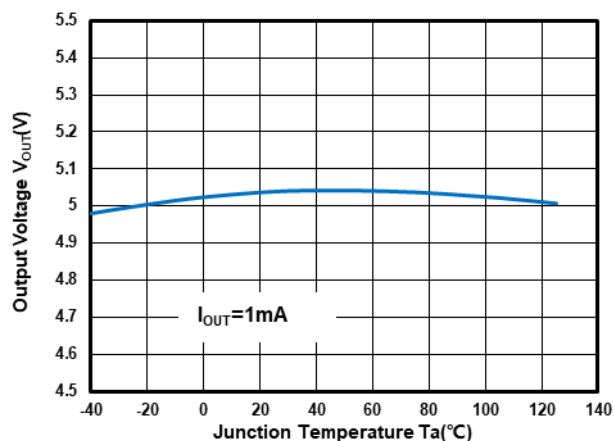
PARAMETER	SYMBOL	CONDITIONS		MIN.	TYP.	MAX.	UNIT
Output Voltage ^{*1}	$V_{\text{OUT(S)}}$	$V_{\text{IN}} = V_{\text{OUT(S)}} + 2\text{V}, I_{\text{OUT}} = 1\text{mA}$		$V_{\text{OUT(S)}} \times 0.98$	$V_{\text{OUT(S)}}$	$V_{\text{OUT(S)}} \times 1.02$	V
Dropout Voltage ^{*2}	V_{DROP}	$I_{\text{OUT}} = 1\text{mA}$			4	8	mV
		$I_{\text{OUT}} = 1\text{A}$			650	900	
Line Regulation	$\frac{\Delta V_{\text{OUT}}}{\Delta V_{\text{IN}} \cdot V_{\text{OUT(S)}}}$	$V_{\text{OUT(S)}} + 2\text{V} \leq V_{\text{IN}} \leq 15\text{V}$ $I_{\text{OUT}} = 1\text{mA}$			0.01	0.02	%/V
Load Regulation	ΔV_{OUT2}	$V_{\text{IN}} = V_{\text{OUT(S)}} + 2\text{V}$ $1\text{mA} \leq I_{\text{OUT}} \leq 1\text{A}$	$V_{\text{OUT(S)}} \leq 5.0\text{V}$		80		mV
			$V_{\text{OUT(S)}} > 5.0\text{V}$		90		
Temperature Stability	$\frac{\Delta V_{\text{OUT}}}{\Delta T_a \cdot V_{\text{OUT(S)}}}$	$V_{\text{IN}} = V_{\text{OUT(S)}} + 2\text{V}, I_{\text{OUT}} = 1\text{mA}$ $-40^{\circ}\text{C} \leq T_a \leq 125^{\circ}\text{C}$			± 50		ppm/ $^{\circ}\text{C}$
GND Current	I_{GND}	no load	$V_{\text{OUT(S)}} \leq 5.0\text{V}$	1	1.6	2	μA
			$V_{\text{OUT(S)}} > 5.0\text{V}$	1.5	2.3	3	
		$I_{\text{OUT}} = 100\text{mA}$			420		
Input Voltage	V_{IN}	---		2.2		18	V
Maximum Output Current	I_{OUTMAX}			1			A
Current Limit ^{*3}	I_{LIM}	$V_{\text{IN}} = V_{\text{OUT(S)}} + 2\text{V},$ $V_{\text{OUT}} = 0.95 \times V_{\text{OUT(S)}}$			1.5		
Power Supply Rejection Ratio ^{*4}	PSRR	$f = 10\text{Hz}, I_{\text{OUT}} = 10\text{mA}$			71		dB
		$f = 100\text{Hz}, I_{\text{OUT}} = 10\text{mA}$			70		
		$f = 1\text{kHz}, I_{\text{OUT}} = 10\text{mA}$			51		
Short Circuit Current	I_{SHORT}	$V_{\text{IN}} = V_{\text{OUT(S)}} + 2.0\text{V}$ $V_{\text{OUT}} = 0\text{V}$			30		mA
Over Temperature Protection	OTP	$I_{\text{OUT}} = 1\text{mA}$			150		$^{\circ}\text{C}$

Notes:

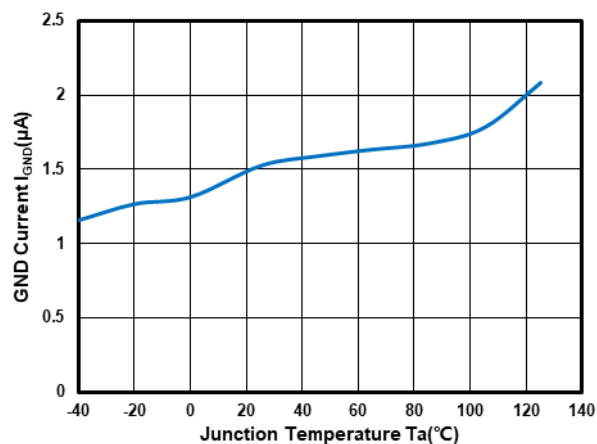
- $V_{\text{OUT(S)}}$: Output voltage when $V_{\text{IN}} = V_{\text{OUT}} + 2\text{V}, I_{\text{OUT}} = 1\text{mA}$.
- $V_{\text{DROP}} = V_{\text{IN1}} - (V_{\text{OUT(S)}} \times 0.98)$ where V_{IN1} is the input voltage when $V_{\text{OUT}} = V_{\text{OUT(S)}} \times 0.98$.
- I_{LIM} : Output current when $V_{\text{IN}} = V_{\text{OUT(S)}} + 2\text{V}$ and $V_{\text{OUT}} = 0.95 \times V_{\text{OUT(S)}}$.
- PSRR was measured for $V_{\text{OUT(S)}} = 5\text{V}$ and $V_{\text{IN}} = 7\text{V}$.

■ Typical Performance Characteristics:

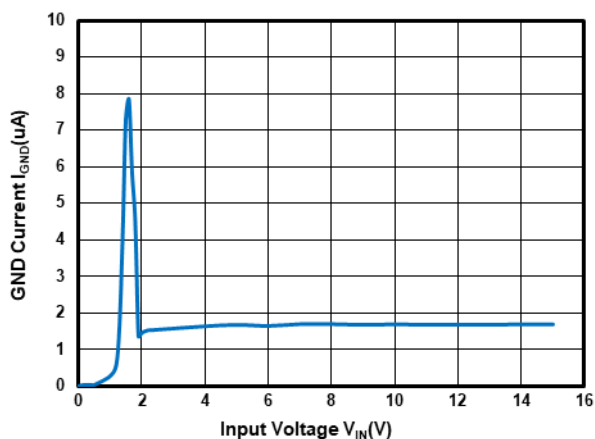
Test Conditions: $V_{IN}=V_{OUT}+2.0V$, $C_{IN}=2.2\mu F$, $C_{OUT}=2.2\mu F$, unless otherwise indicated.



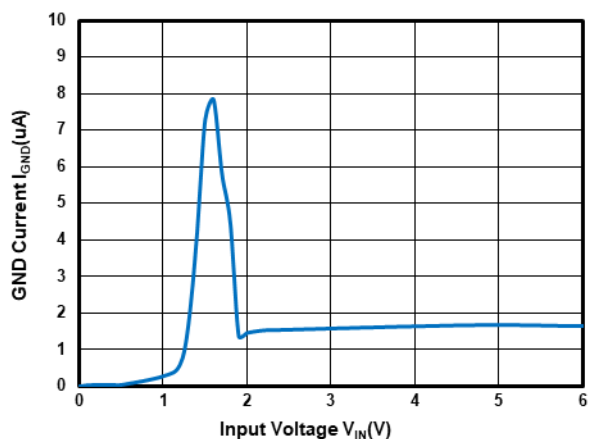
Output Voltage vs Temperature at $V_{OUT}=5.0V$



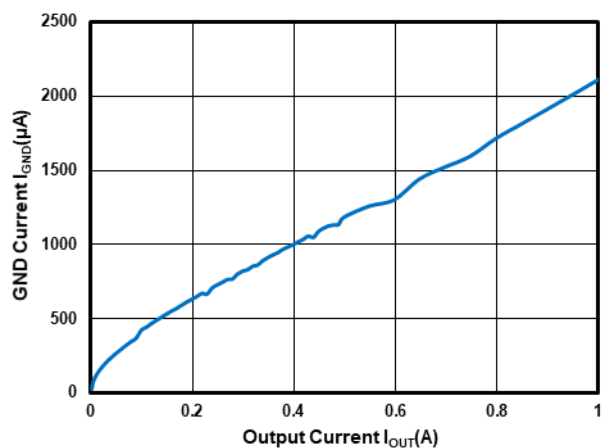
GND Current vs Temperature at $V_{OUT}=5.0V$



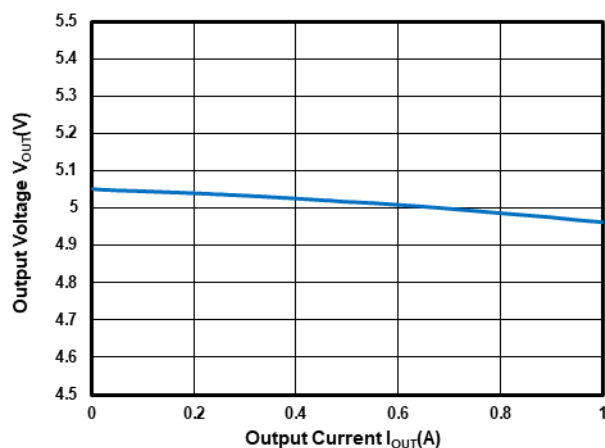
GND Current vs Input Voltage at $V_{OUT}=5.0V$



GND Current vs Input Voltage at $V_{OUT}=5.0V$



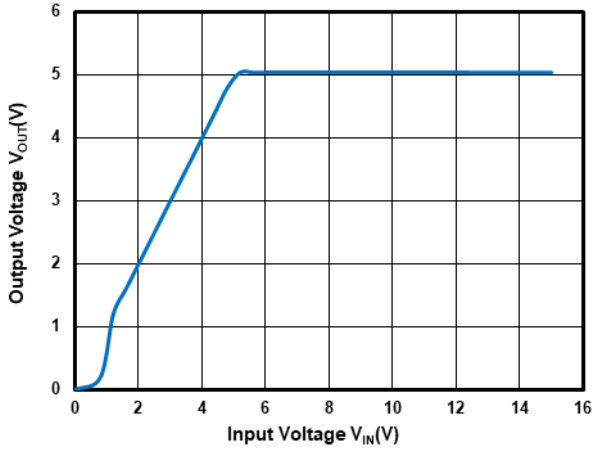
GND Current vs Output Current at $V_{OUT}=5.0V$



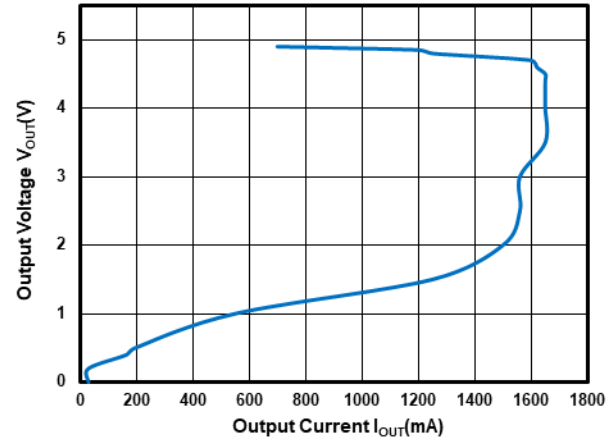
Output Voltage vs Output Current at $V_{OUT}=5.0V$

■ Typical Performance Characteristics (Continued):

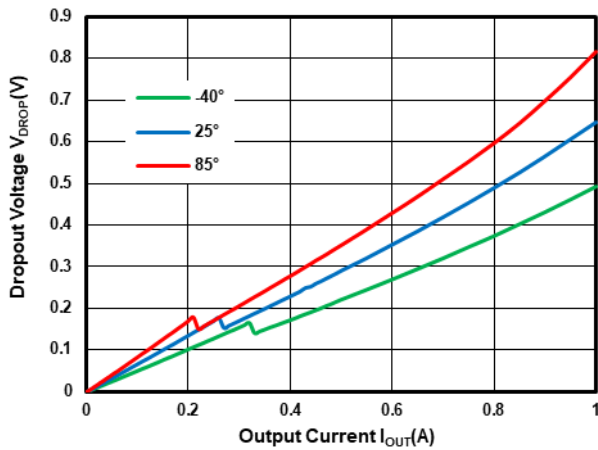
Test Conditions: $V_{IN}=V_{OUT}+2.0V$, $C_{IN}=2.2\mu F$, $C_{OUT}=2.2\mu F$, unless otherwise indicated.



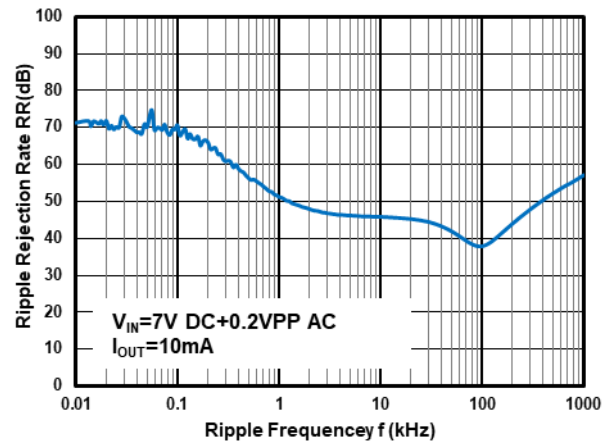
Output Voltage vs Input Voltage at $V_{OUT}=5.0V$



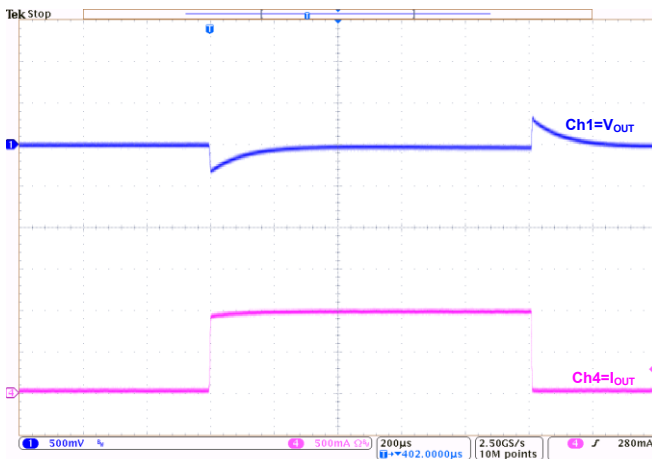
Output Current Fold-back at $V_{OUT}=5.0V$



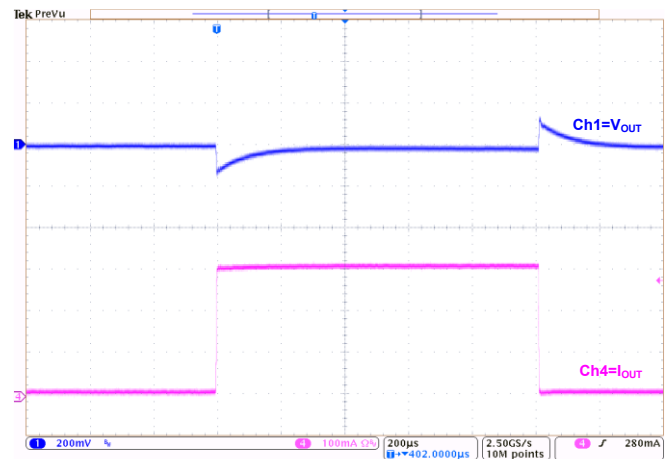
Dropout Voltage vs Temperature at $V_{OUT}=5.0V$



Power Supply Rejection Ratio at $V_{OUT}=5.0V$



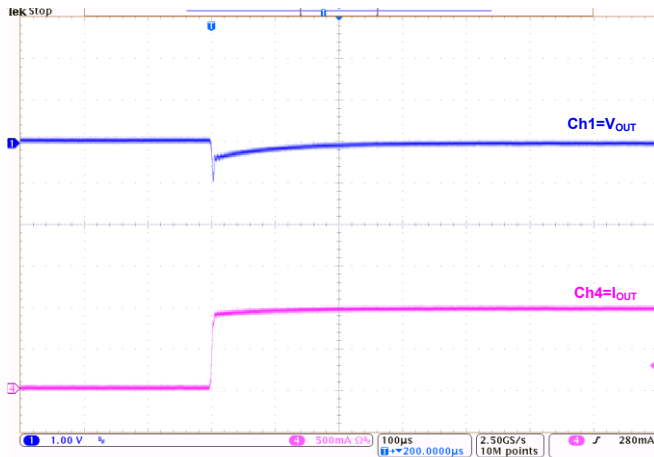
Load Transient at $V_{OUT}=5.0V$
($I_{OUT}=1mA \sim 1A \sim 1mA$)



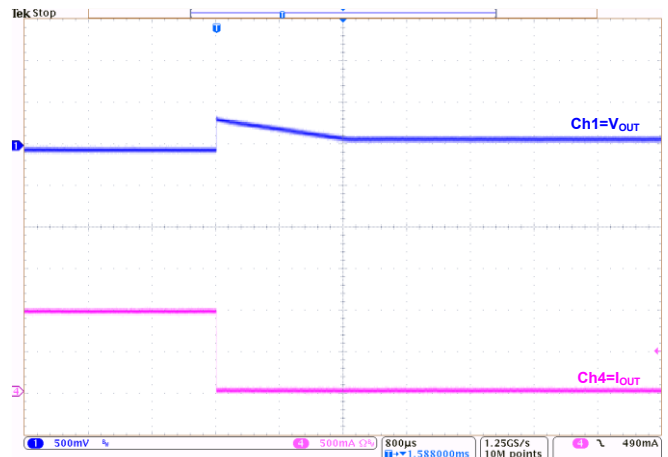
Load Transient at $V_{OUT}=5.0V$
($I_{OUT}=1mA \sim 300mA \sim 1mA$)

■ Typical Performance Characteristics (Continued):

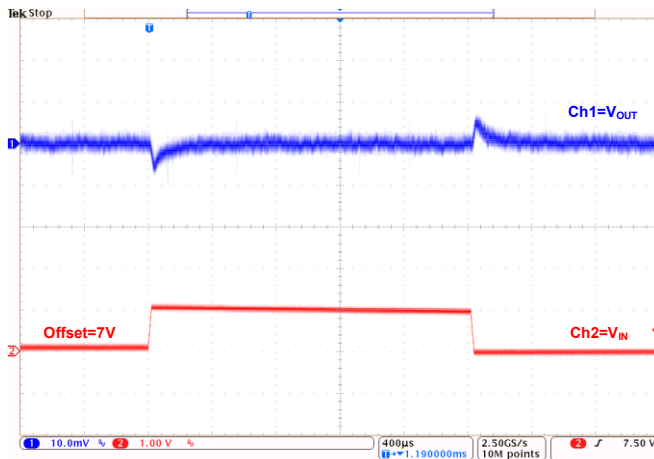
Test Conditions: $V_{IN}=V_{OUT}+2.0V$, $C_{IN}=2.2\mu F$, $C_{OUT}=2.2\mu F$, unless otherwise indicated.



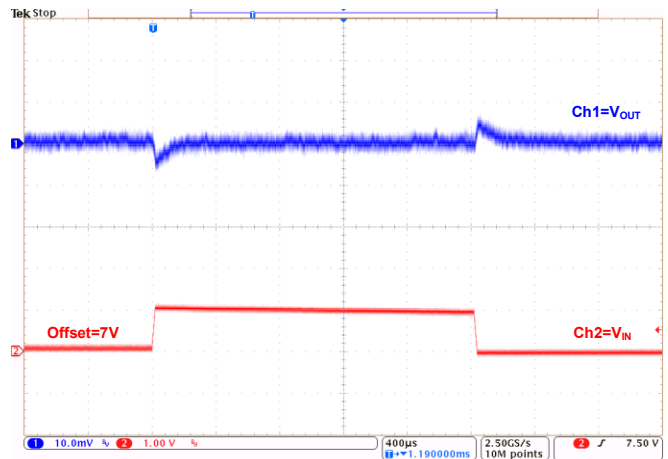
Load Transient at $V_{OUT}=5.0V$
($I_{OUT}=0mA\sim 1A$)



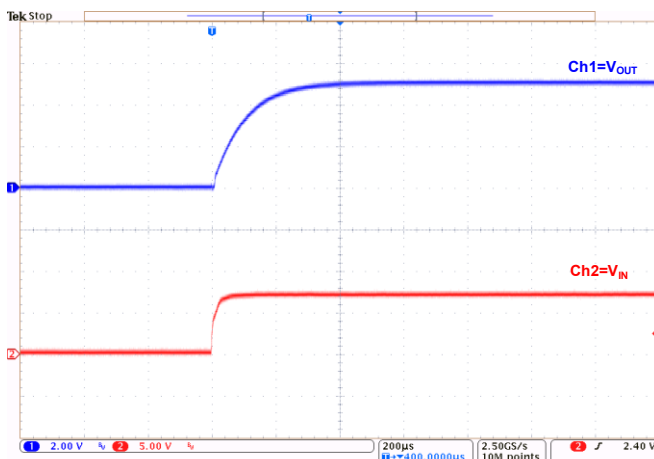
Load Transient at $V_{OUT}=5.0V$
($I_{OUT}=1A\sim 0mA$)



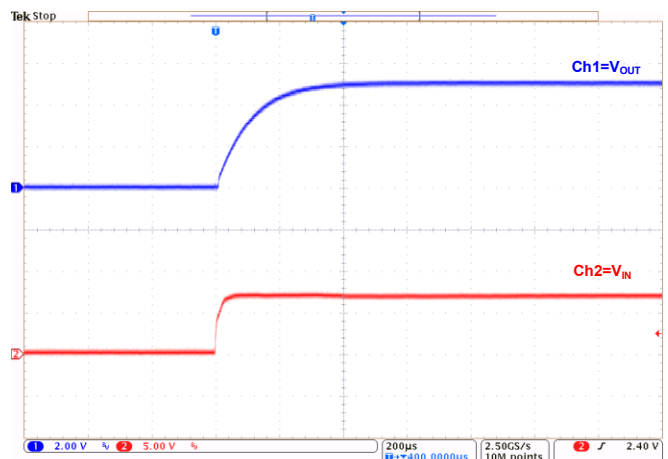
Line Transient at $V_{OUT}=5.0V$
($I_{OUT}=1mA$)



Line Transient at $V_{OUT}=5.0V$
($I_{OUT}=10mA$)



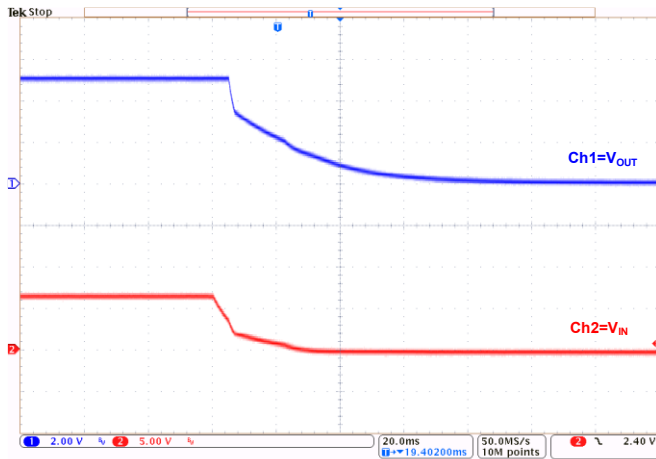
Power-Up at $V_{OUT}=5.0V$:
($I_{OUT}=1mA$)



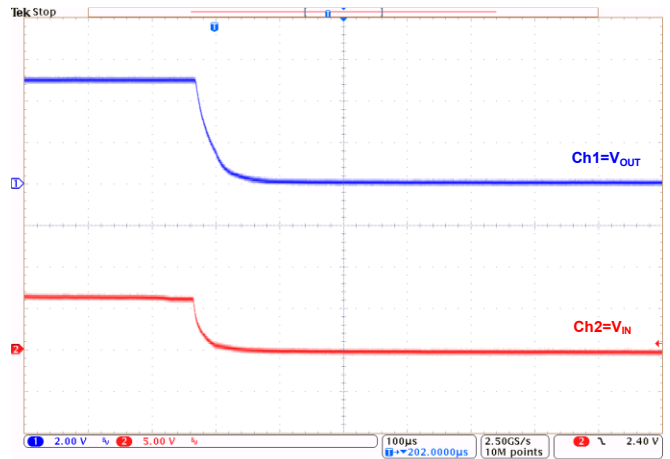
Power-Up at $V_{OUT}=5.0V$:
($I_{OUT}=1A$)

■ Typical Performance Characteristics (Continued):

Test Conditions: $V_{IN}=V_{OUT}+2.0V$, $C_{IN}=2.2\mu F$, $C_{OUT}=2.2\mu F$, unless otherwise indicated.



Power-Down at $V_{OUT}=5.0V$:
($I_{OUT}=1mA$)

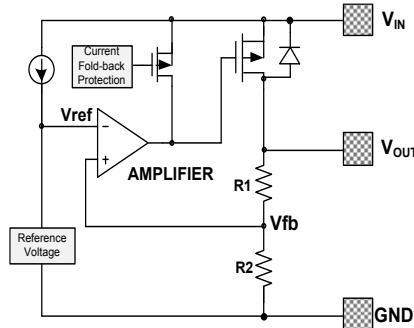


Power-Down at $V_{OUT}=5.0V$:
($I_{OUT}=1A$)

■ Operational Explanation

Output voltage control

The voltage divided by resistors R1 and R2 is compared with the internal reference voltage by the error amplifier. The amplifier output then drives the P-channel MOSFET connected to the V_{OUT} pin. The output voltage at the V_{OUT} pin is regulated by this negative feedback system. The current limit circuit and short protect circuit operate in relation to output current level.



■ Pass transistor

The pass transistor with low turn-on resistance used in HM7218 is a P-channel MOSFET. If the potential on V_{OUT} pin is higher than V_{IN} , it is possible that IC will be destroyed due to reverse current which is caused by parasitic diodes between V_{IN} and V_{OUT} . Therefore, the V_{OUT} pin potential exceeds $V_{IN}+0.3V$ is not allowed.

■ Current limit, over temperature protection

The HM7218 series includes a combination of a fixed current limiter circuit which aid the operations of the current limiter and circuit protection. When the load current reaches the current limit level, the fixed current limiter circuit operates and output voltage drops. As a result of this drop in output voltage, the foldback circuit operates, output voltage drops further and output current decreases.

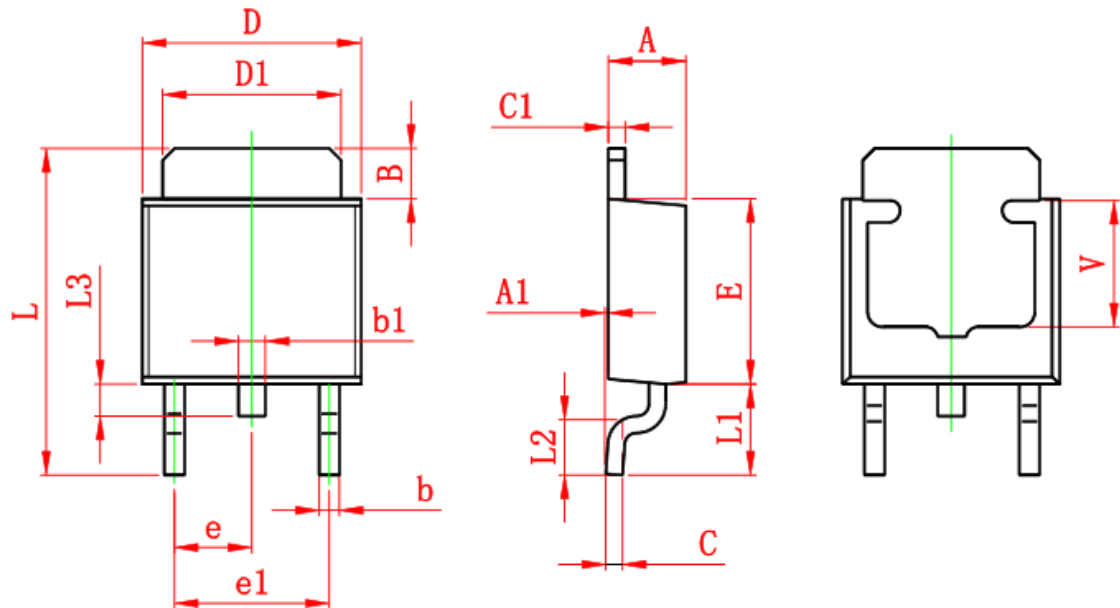
Special attention should be paid to that the product of the dropout voltage on the chip and the output current must be smaller than the heat dissipation. If power consumption on the chip is more than the heat dissipation, OTP will protect the chip from damaging due to over temperature.

■ Notes:

1. The input and output capacitors should be placed as close as possible to the IC.
2. If the impedance of the power supply is high, which is caused by forgetting installing input capacitor or installing too small value capacitor, the oscillation may occur.
3. Pay attention to the operation conditions of input and output voltage and load current, such that the power consumption in the IC should not exceed the allowable power consumption of the package even though the chip has short circuit protection.
4. IC has a built-in anti-static protection (ESD) circuit, but please do not add excessive stress to the IC.

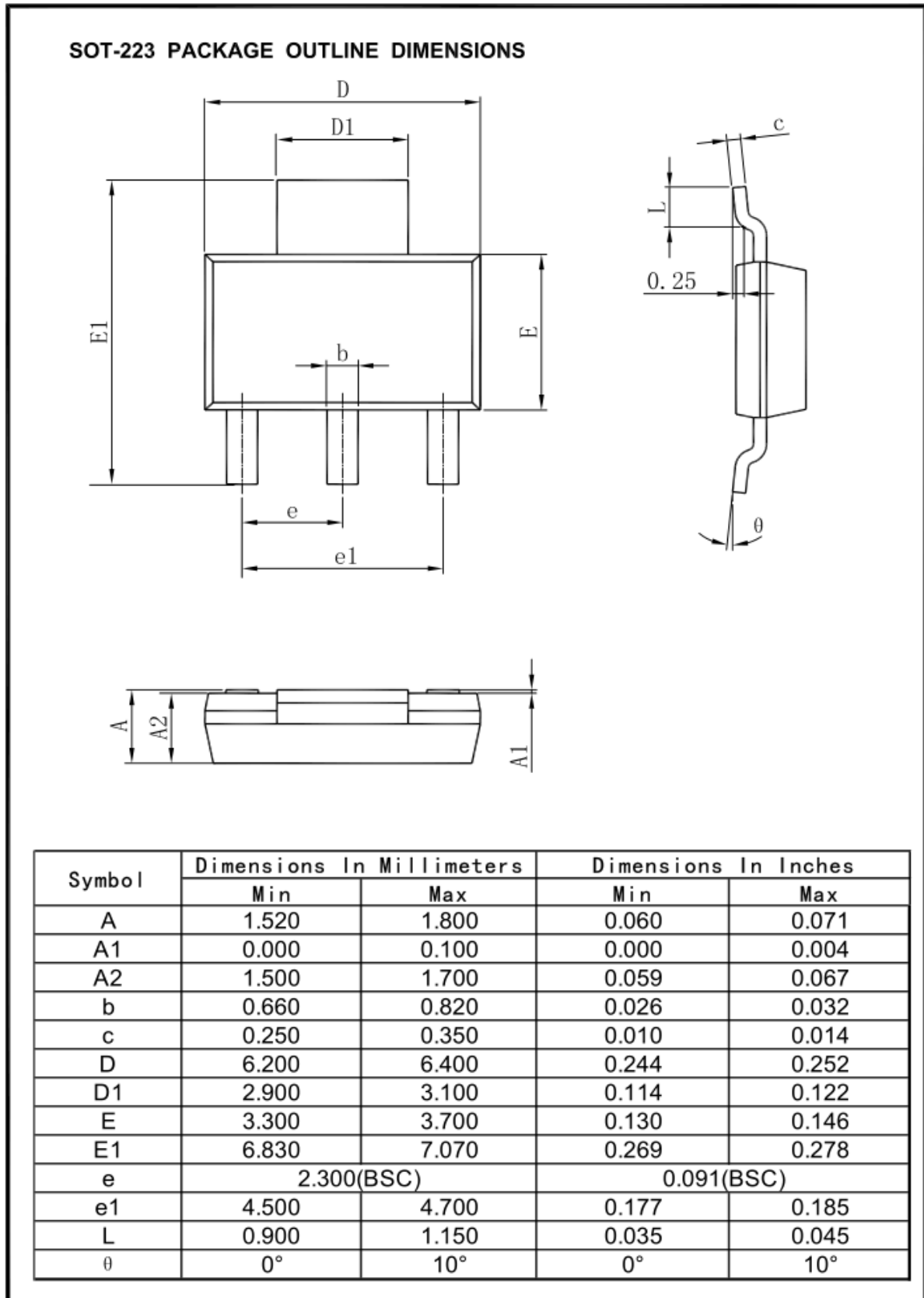
■ Packaging Information

TO-252-2L PACKAGE OUTLINE DIMENSIONS



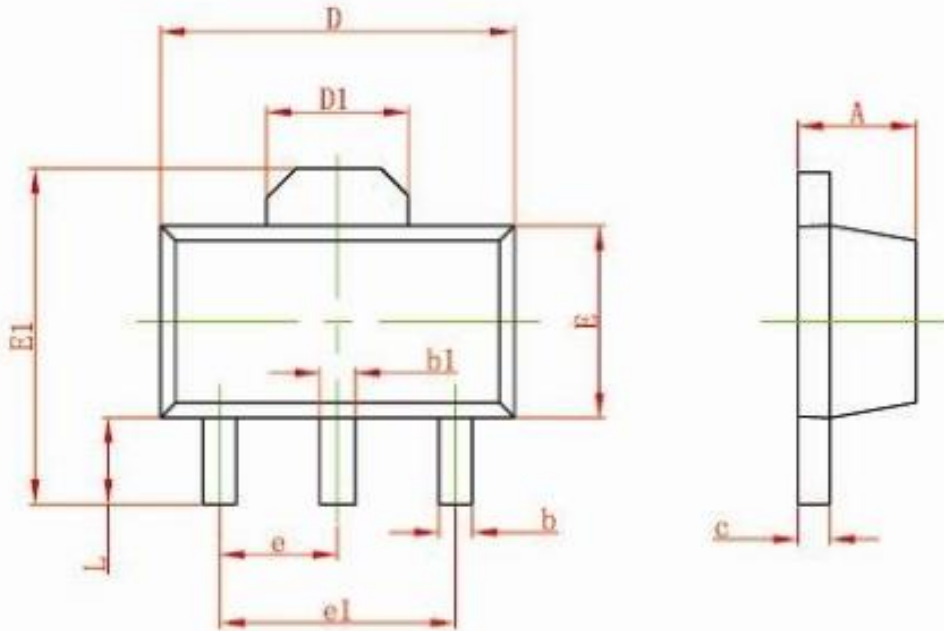
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
B	1.350	1.650	0.053	0.065
b	0.500	0.700	0.020	0.028
b1	0.700	0.900	0.028	0.035
c	0.430	0.580	0.017	0.023
c1	0.430	0.580	0.017	0.023
D	6.350	6.650	0.250	0.262
D1	5.200	5.400	0.205	0.213
E	5.400	5.700	0.213	0.224
e	2.300 TYP.		0.091 TYP.	
e1	4.500	4.700	0.177	0.185
L	9.500	9.900	0.374	0.390
L1	2.550	2.900	0.100	0.114
L2	1.400	1.780	0.055	0.070
L3	0.600	0.900	0.024	0.035
V	3.800 REF.		0.150 REF.	

■ Packaging Information (Continued)



■ Packaging Information (Continued)

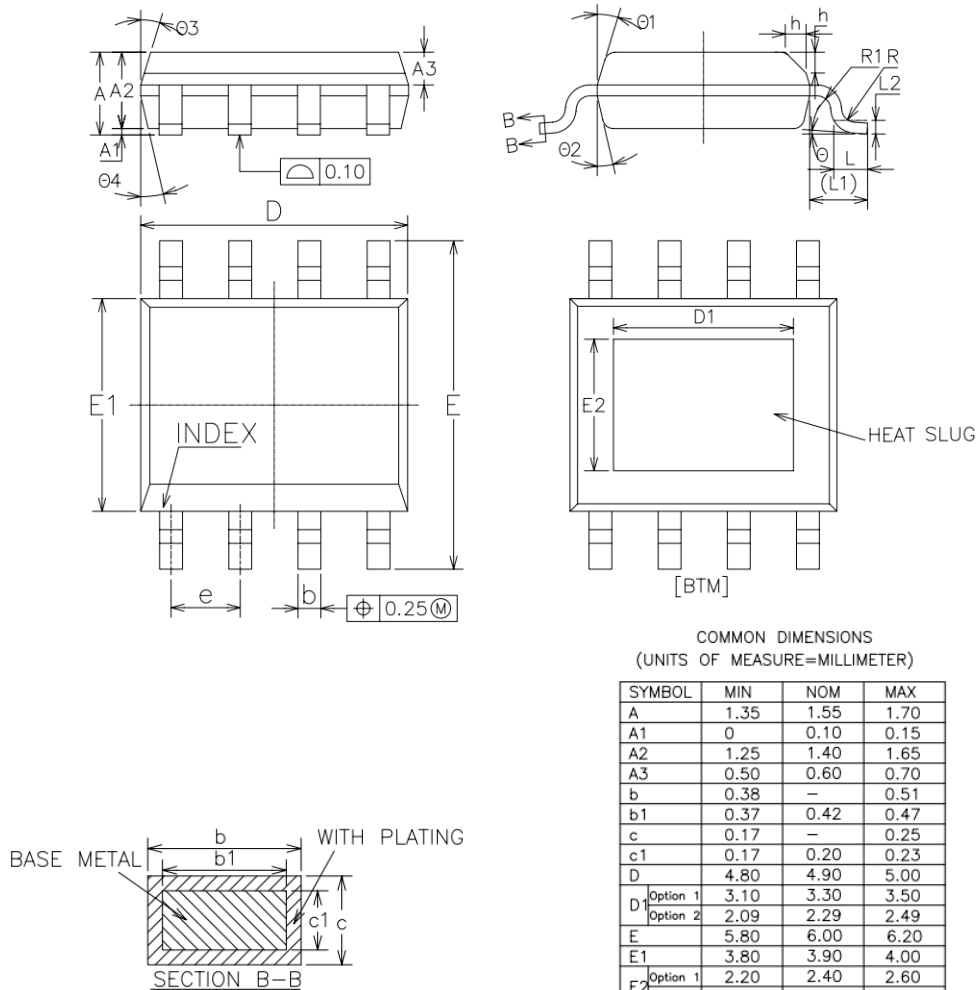
SOT-89-3L PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.400	1.600	0.055	0.063
b	0.320	0.520	0.013	0.197
b1	0.400	0.580	0.016	0.023
c	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550 REF		0.061 REF	
E	2.300	2.600	0.091	0.102
E1	3.940	4.250	0.155	0.167
e	1.500 TYP		0.060TYP	
e1	3.000 TYP		0.118TYP	
L	0.900	1.200	0.035	0.047

■ Packaging Information (Continued)

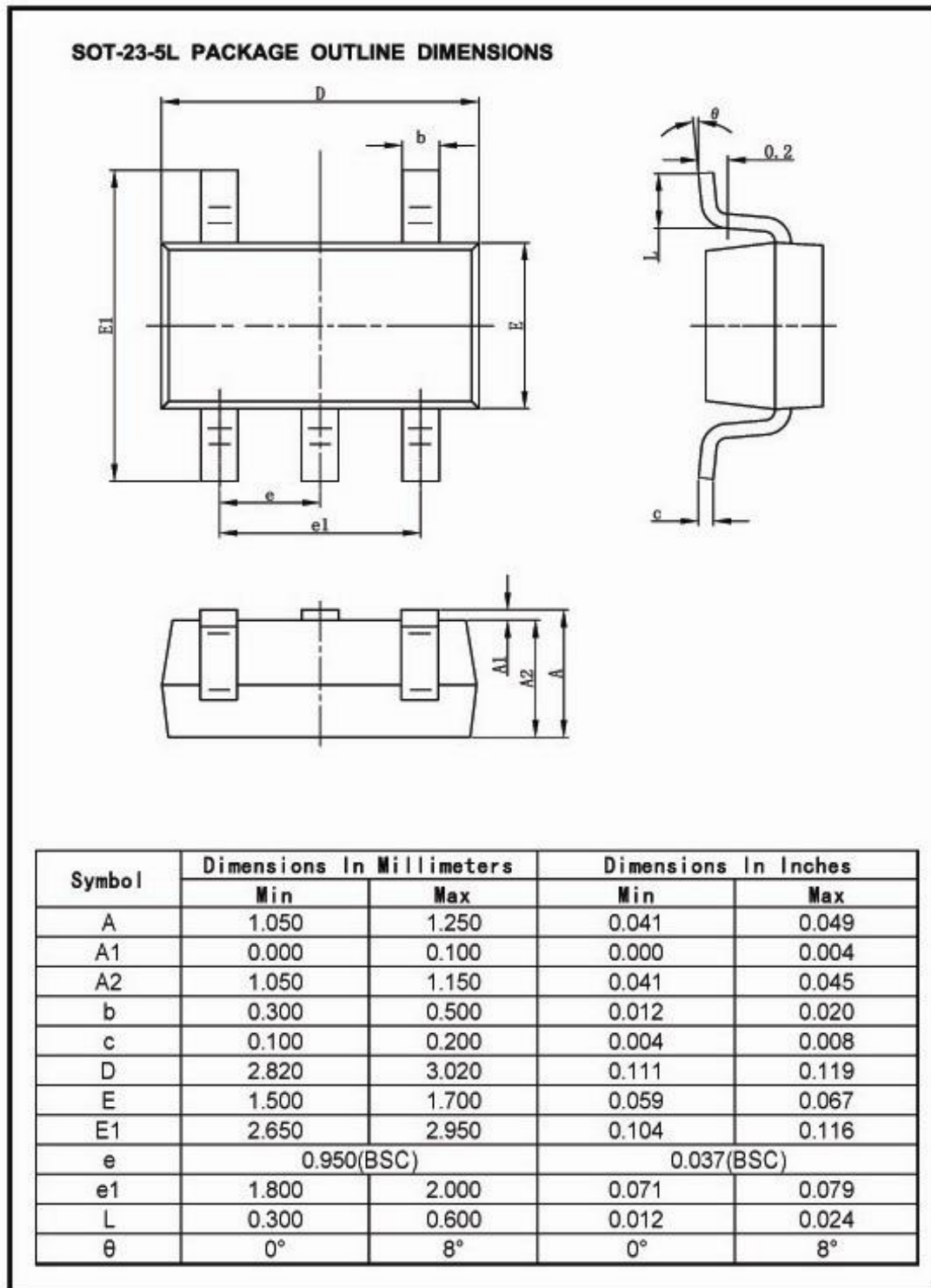
ESOP8 PACKAGE OUTLINE DIMENSIONS



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

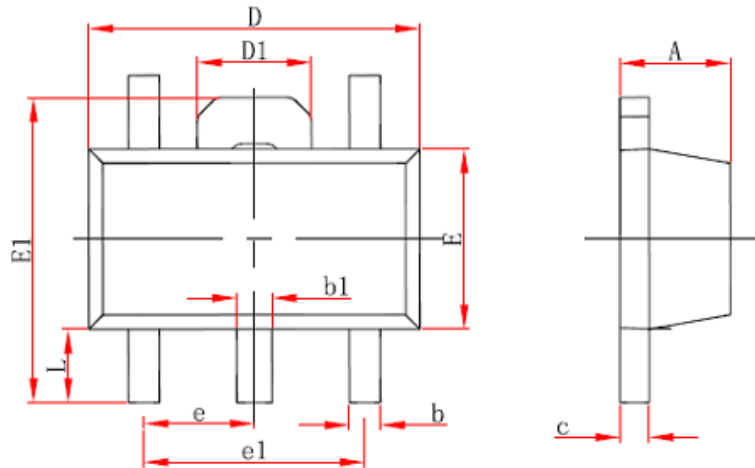
SYMBOL	MIN	NOM	MAX
A	1.35	1.55	1.70
A1	0	0.10	0.15
A2	1.25	1.40	1.65
A3	0.50	0.60	0.70
b	0.38	—	0.51
b1	0.37	0.42	0.47
c	0.17	—	0.25
c1	0.17	0.20	0.23
D	4.80	4.90	5.00
D1	Option 1 Option 2	3.10 2.09	3.30 2.29
E	5.80	6.00	6.20
E1	3.80	3.90	4.00
E2	Option 1 Option 2	2.20 2.09	2.40 2.29
e	1.17	1.27	1.37
L	0.45	0.60	0.80
L1	1.04REF		
L2	0.25BSC		
R	0.07	—	—
R1	0.07	—	—
h	0.30	0.40	0.50
Ø	0°	—	8°
Ø1	15°	17°	19°
Ø2	11°	13°	15°
Ø3	15°	17°	19°
Ø4	11°	13°	15°

■ Packaging Information (Continued)



■ Packaging Information (Continued)

SOT-89-5L PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.400	1.600	0.055	0.063
b	0.320	0.520	0.013	0.020
b1	0.380	0.580	0.015	0.023
c	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550 REF.		0.061 REF.	
E	2.300	2.600	0.091	0.102
E1	3.940	4.250	0.155	0.167
e	1.500 TYP.		0.060 TYP.	
e1	3.000 TYP.		0.118 TYP.	
L	0.900	1.200	0.035	0.047