

## N-Channel Super Trench Power MOSFET

### Description

The HM51 EP51 U uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{DS(on)}$  and  $Q_g$ . This device is ideal for high-frequency switching and synchronous rectification.

### Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

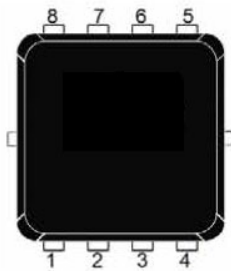
### General Features

- $V_{DS} = 60V, I_D = 1.0A$   
 $R_{DS(on)} = 4.4m\Omega$  (typical) @  $V_{GS} = 10V$   
 $R_{DS(on)} = 6.4m\Omega$  (typical) @  $V_{GS} = 4.5V$
- Excellent gate charge x  $R_{DS(on)}$  product(FOM)
- Very low on-resistance  $R_{DS(on)}$
- 150 °C operating temperature
- Pb-free lead plating

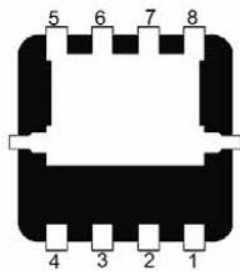
**100% UIS TESTED!**

**100%  $\Delta V_{ds}$  TESTED!**

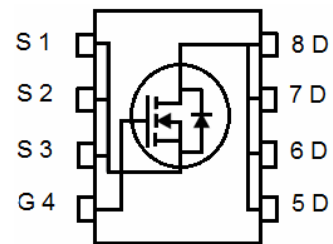
DFN 3.3X3.3



Top View



Bottom View



Schematic Diagram

### Package Marking and Ordering Information

| Device Marking | Device      | Device Package | Reel Size | Tape width | Quantity |
|----------------|-------------|----------------|-----------|------------|----------|
| HMS1 EP51 U    | HMS1 EP51 U | DFN3.3X3.3-8L  | -         | -          | -        |

### Absolute Maximum Ratings ( $T_c = 25^\circ C$ unless otherwise noted)

| Parameter  | Symbol              | Limit      | Unit          |
|--|---------------------|------------|---------------|
| Drain-Source Voltage                             | $V_{DS}$            | 60         | V             |
| Gate-Source Voltage                              | $V_{GS}$            | $\pm 20$   | V             |
| Drain Current-Continuous                         | $I_D$               | 1.0        | A             |
| Drain Current-Continuous ( $T_c = 100^\circ C$ ) | $I_D (100^\circ C)$ | 0.5        | A             |
| Pulsed Drain Current                             | $I_{DM}$            | 1.5        | A             |
| Maximum Power Dissipation                        | $P_D$               | 60         | W             |
| Derating factor                                  |                     | 0.48       | W/ $^\circ C$ |
| Single pulse avalanche energy (Note 5)           | $E_{AS}$            | 350        | mJ            |
| Operating Junction and Storage Temperature Range | $T_J, T_{STG}$      | -55 To 150 | $^\circ C$    |

### Thermal Characteristic

|   |                 |     |              |
|---|-----------------|-----|--------------|
| Thermal Resistance, Junction-to-Case (Note 2) | $R_{\theta JC}$ | 2.1 | $^\circ C/W$ |
|---|-----------------|-----|--------------|

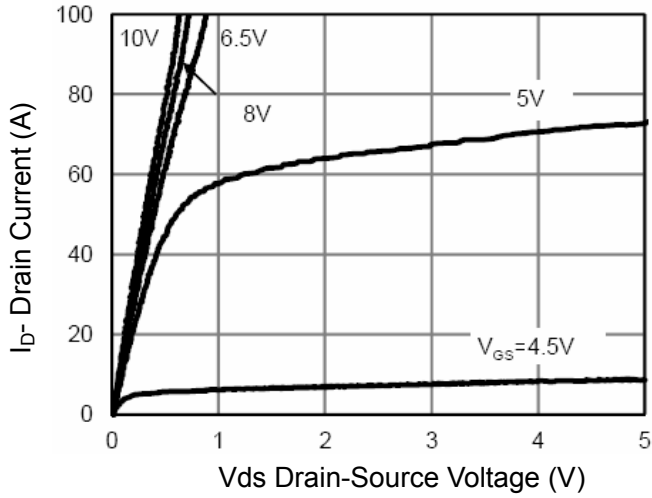
**Electrical Characteristics ( $T_C=25^{\circ}\text{C}$  unless otherwise noted)**

| Parameter                          | Symbol              | Condition   | Min | Typ          | Max  | Unit |
|------------------------------------|---------------------|---|-----|--------------|------|------|
| Off Characteristics                |                     |   |     |              |      |      |
| Drain-Source Breakdown Voltage     | BV <sub>DSS</sub>   | V <sub>GS</sub> =0V I <sub>D</sub> =250μA   | 60  |              | -    | V    |
| Zero Gate Voltage Drain Current    | I <sub>DSS</sub>    | V <sub>DS</sub> =60V,V <sub>GS</sub> =0V  | -   | -            | 1    | μA   |
| Gate-Body Leakage Current          | I <sub>GSS</sub>    | V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V   | -   | -            | ±100 | nA   |
| On Characteristics (Note 3)        |                     |   |     |              |      |      |
| Gate Threshold Voltage             | V <sub>GS(th)</sub> | V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250μA                               | 1.2 | 1.65         | 2.3  | V    |
| Drain-Source On-State Resistance   | R <sub>DS(ON)</sub> | V <sub>GS</sub> =10V, I <sub>D</sub> =25A   | -   | 4.4          | 5.2  | mΩ   |
| Drain-Source On-State Resistance   | R <sub>DS(ON)</sub> | V <sub>GS</sub> =4.5V, I <sub>D</sub> =25A  | -   | 6.4          | 7.8  | mΩ   |
| Forward Transconductance           | g <sub>FS</sub>     | V <sub>DS</sub> =5V,I <sub>D</sub> =25A   |     | 60           | -    | S    |
| Dynamic Characteristics (Note4)    |                     |   |     |              |      |      |
| Input Capacitance                  | C <sub>iss</sub>    | V <sub>DS</sub> =30V,V <sub>GS</sub> =0V,<br>F=1.0MHz                                 | -   | 1600         | -    | PF   |
| Output Capacitance                 | C <sub>oss</sub>    |   | -   | 320          | -    | PF   |
| Reverse Transfer Capacitance       | C <sub>rss</sub>    |   | -   | 9            | -    | PF   |
| Switching Characteristics (Note 4) |                     |   |     |              |      |      |
| Turn-on Delay Time                 | t <sub>d(on)</sub>  | V <sub>DD</sub> =30V,I <sub>D</sub> =25A<br>V <sub>GS</sub> =10V,R <sub>G</sub> =1.6Ω | -   | 7            | -    | nS   |
| Turn-on Rise Time                  | t <sub>r</sub>      |   | -   | 2            | -    | nS   |
| Turn-Off Delay Time                | t <sub>d(off)</sub> |   | -   | 27           | -    | nS   |
| Turn-Off Fall Time                 | t <sub>f</sub>      |   | -   | 4            | -    | nS   |
| Total Gate Charge                  | Q <sub>g</sub>      | V <sub>DS</sub> =30V,I <sub>D</sub> =25A,<br>V <sub>GS</sub> =10V                     | -   | 26           | -    | nC   |
| Gate-Source Charge                 | Q <sub>gs</sub>     |   | -   | 8.3          |      | nC   |
| Gate-Drain Charge                  | Q <sub>gd</sub>     |   | -   | 5.5          |      | nC   |
| Drain-Source Diode Characteristics |                     |   |     |              |      |      |
| Diode Forward Voltage (Note 3)     | V <sub>SD</sub>     | V <sub>GS</sub> =0V,I <sub>S</sub> =25A   | -   |              | 1.2  | V    |
| Diode Forward Current (Note 2)     | I <sub>S</sub>      |   | -   | <del>0</del> | 0    | A    |
| Reverse Recovery Time              | t <sub>rr</sub>     | T <sub>J</sub> = 25°C, I <sub>F</sub> =25A  | -   | 38           | -    | nS   |
| Reverse Recovery Charge            | Q <sub>rr</sub>     | di/dt = 100A/μs (Note3)   | -   | 48           | -    | nC   |

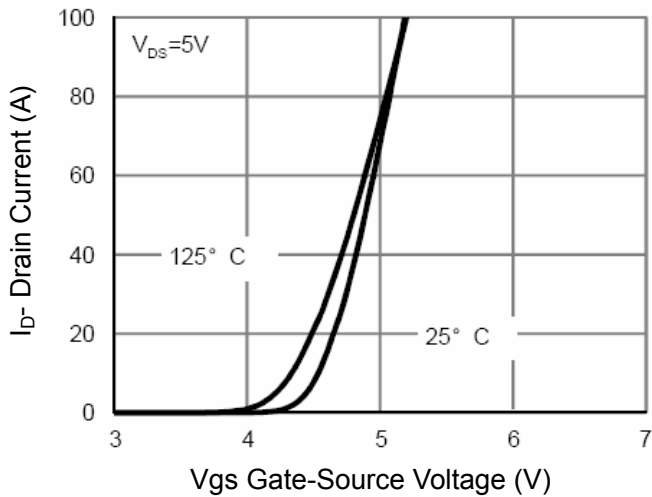
**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production
5. EAS condition :  $T_J=25^{\circ}\text{C}, V_{DD}=30V, V_G=10V, L=0.5\text{mH}, R_g=25\Omega$

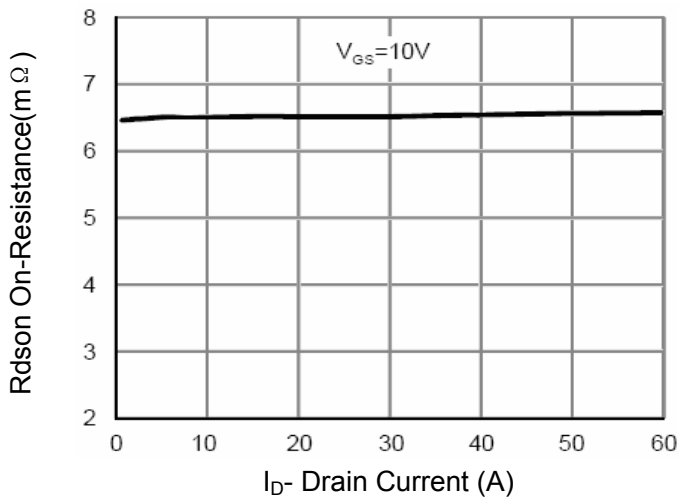
## Typical Electrical and Thermal Characteristics



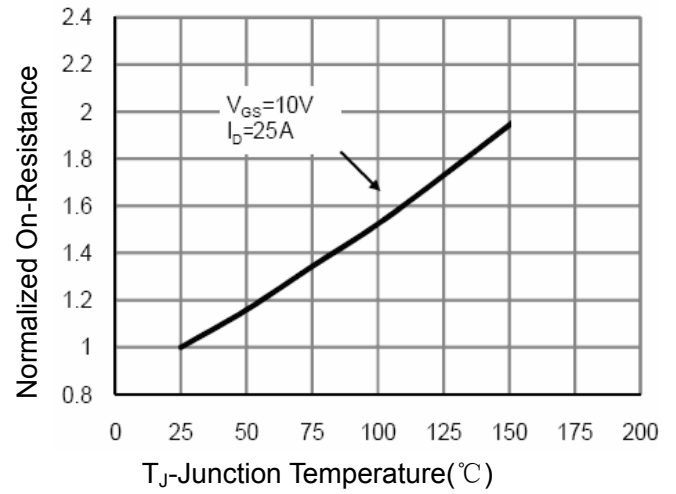
**Figure 1 Output Characteristics**



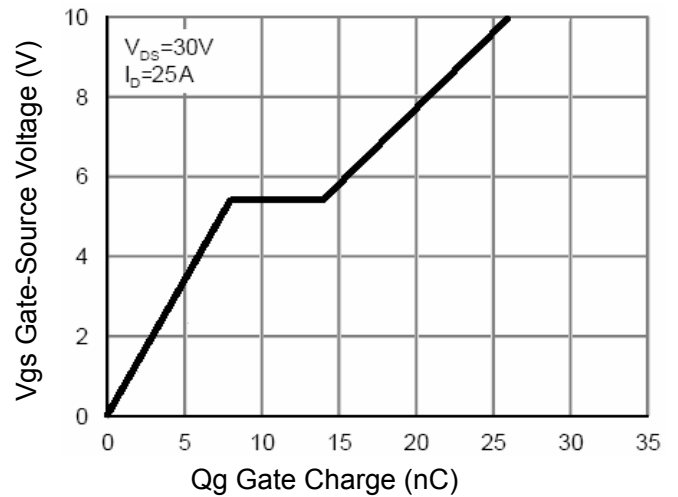
**Figure 2 Transfer Characteristics**



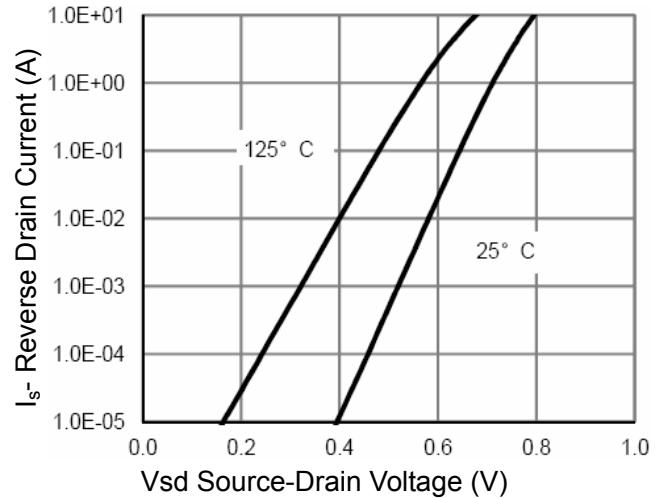
**Figure 3 Rdson- Drain Current**



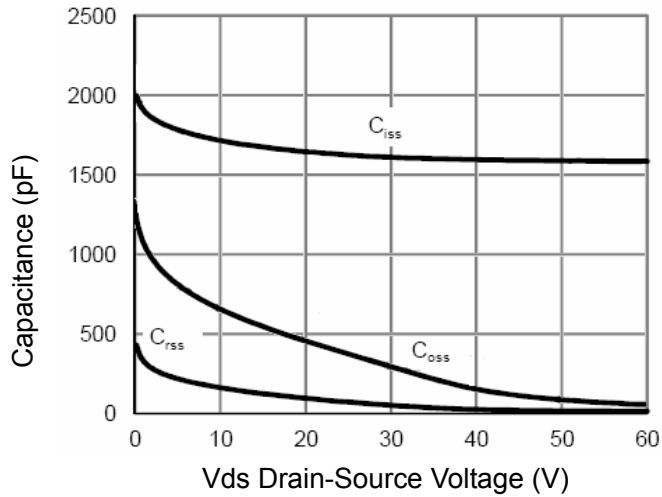
**Figure 4 Rdson-Junction Temperature**



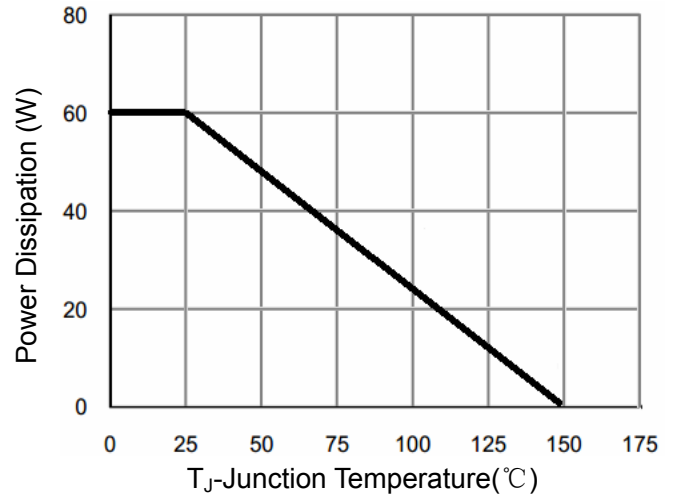
**Figure 5 Gate Charge**



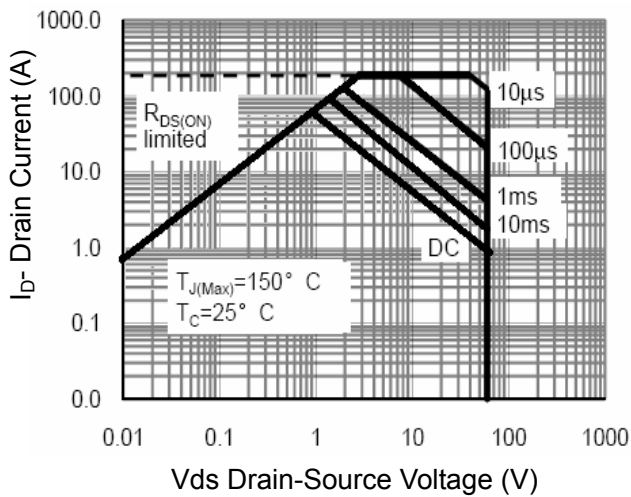
**Figure 6 Source- Drain Diode Forward**



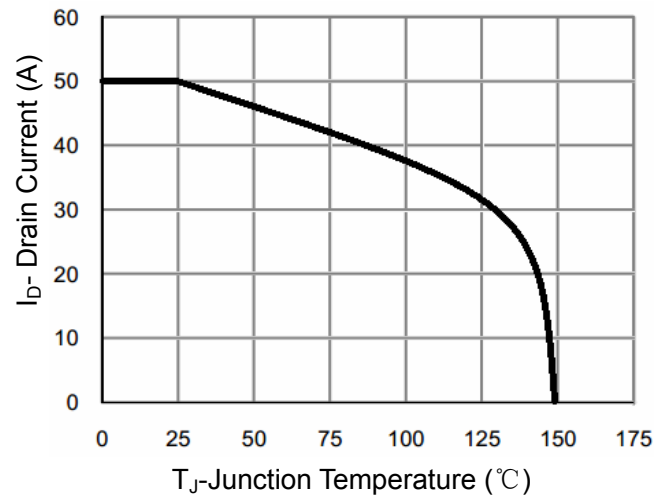
**Figure 7 Capacitance vs Vds**



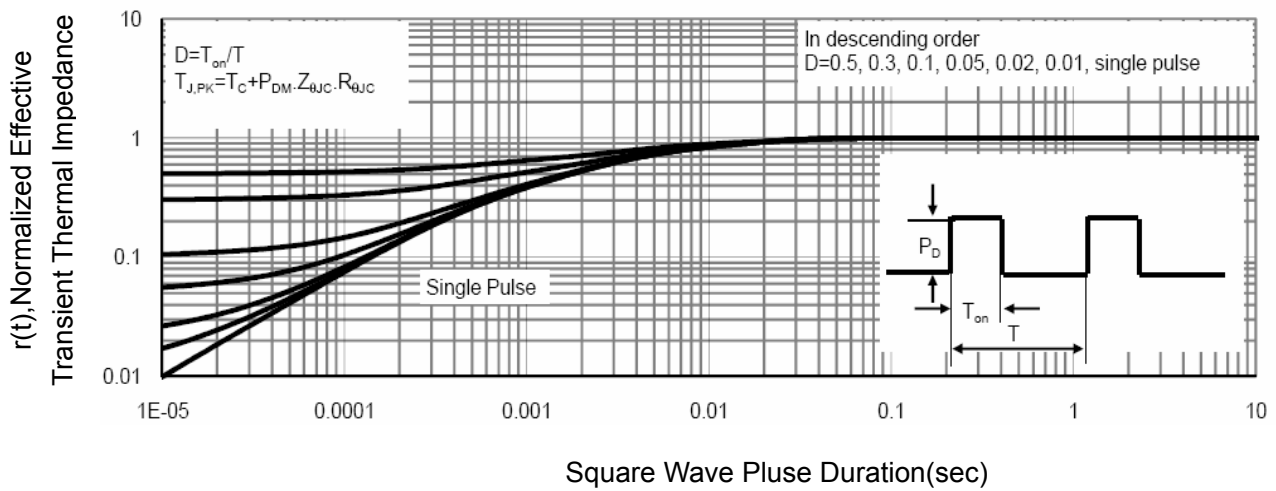
**Figure 9 Power De-rating**



**Figure 8 Safe Operation Area**

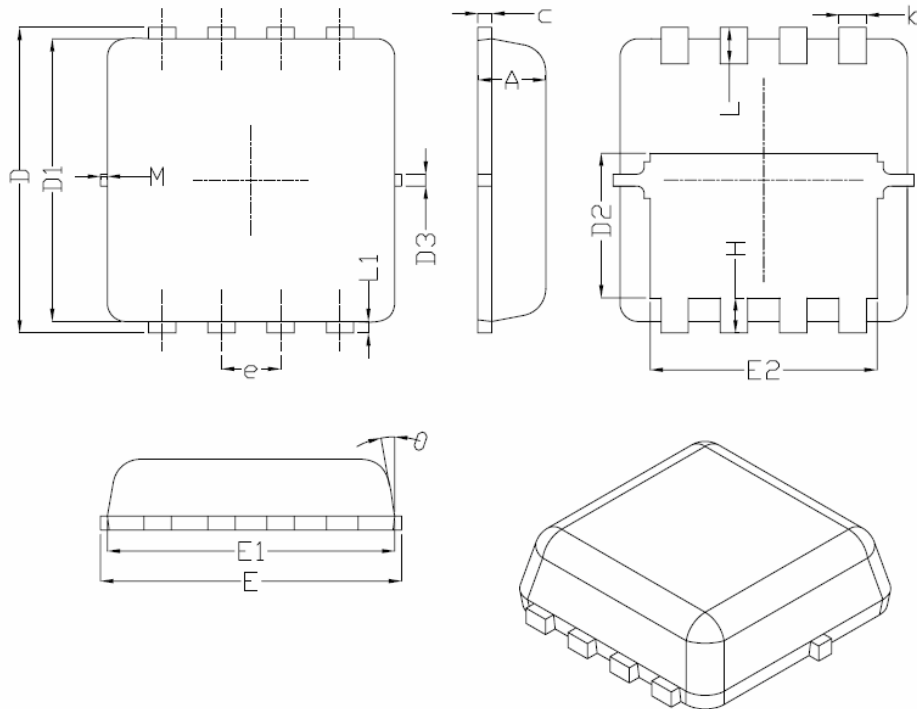


**Figure 10 Current De-rating**



**Figure 11 Normalized Maximum Transient Thermal Impedance**

## DFN3.3X3.3-8L Package Information



| Symbol | Dimensions In Millimeters |      |      |
|--------|---------------------------|------|------|
|        | Min.                      | Nom. | Max. |
| A      | 0.70                      | 0.75 | 0.80 |
| b      | 0.25                      | 0.30 | 0.35 |
| c      | 0.10                      | 0.15 | 0.25 |
| D      | 3.25                      | 3.35 | 3.45 |
| D1     | 3.00                      | 3.10 | 3.20 |
| D2     | 1.48                      | 1.58 | 1.68 |
| D3     | -                         | 0.13 | -    |
| E      | 3.20                      | 3.30 | 3.40 |
| E1     | 3.00                      | 3.15 | 3.20 |
| E2     | 2.39                      | 2.49 | 2.59 |
| e      | 0.65BSC                   |      |      |
| H      | 0.30                      | 0.39 | 0.50 |
| L      | 0.30                      | 0.40 | 0.50 |
| L1     | -                         | 0.13 | -    |
| M      | *                         | *    | 0.15 |
| θ      |                           | 10°  | 12°  |

Land Pattern  
(Only for Reference)

