

D

D

C

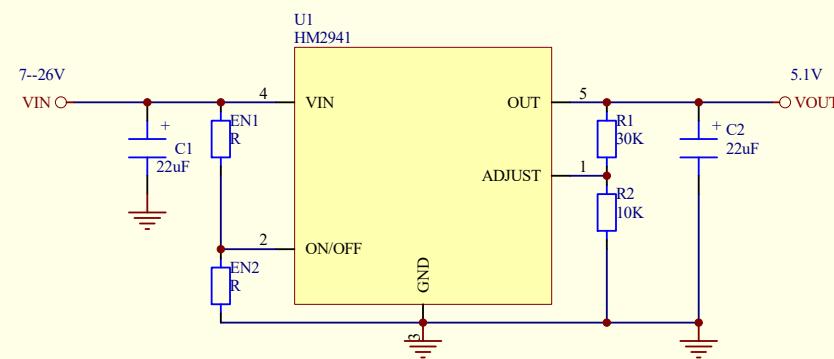
C

B

B

A

A



*输出电压设置: $V_{OUT} = 1.275V \times (R1/R2 + 1)$

*ON/OFF脚说明：拉高芯片关掉，拉低或悬空芯片打开；此引脚若直接拉高到6V以上需要串个100K以上电阻，或采用个电阻分压取得3-5V高电平。

Title		
Size A4	Number	Revision
Date:	21-Aug-2024	Sheet of
File:	F:\张春峰\做DEMO\DEMO.ddb	Drawn By: