

N-Channel Super Trench Power MOSFET

Description

The HM150N20LL uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(on)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

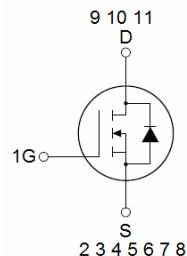
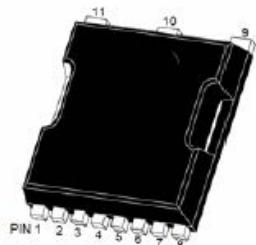
General Features

- $V_{DS} = 200V, I_D = 150A$
- $R_{DS(on)} = 6.6m\Omega$, typical @ $V_{GS} = 10V$
- Excellent gate charge $\times R_{DS(on)}$ product(FOM)
- Very low on-resistance $R_{DS(on)}$
- 175 °C operating temperature
- Pb-free lead plating

100% UIS TESTED!

100% ΔVds TESTED!

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Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
HMS150N20LL	HMS150N20LL	TOLL	-	-	-

Absolute Maximum Ratings ($T_C=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	200	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	150	A
Drain Current-Continuous($T_C=100^\circ C$)	$I_D(100^\circ C)$	105	A
Pulsed Drain Current	I_{DM}	450	A
Maximum Power Dissipation	P_D	450	W
Derating factor		3.0	W/°C
Single pulse avalanche energy (Note 5)	E_{AS}	2000	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 175	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Case	R_{eJC}	0.33	°C/W
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Electrical Characteristics ($T_C=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	100		-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=200\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	± 100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	2.0	3.0	4.0	V
Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=165\text{A}$	-	6.6	10	$\text{m}\Omega$
Forward Transconductance	g_{FS}	$V_{\text{DS}}=5\text{V}, I_{\text{D}}=165\text{A}$	-	200	-	S
Dynamic Characteristics (Note 4)						
Input Capacitance	C_{iss}	$V_{\text{DS}}=40\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$	-	8600	-	PF
Output Capacitance	C_{oss}		-	770	-	PF
Reverse Transfer Capacitance	C_{rss}		-	30	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{\text{d(on)}}$	$V_{\text{DD}}=40\text{V}, I_{\text{D}}=165\text{A}$ $V_{\text{GS}}=10\text{V}, R_{\text{G}}=1.6\Omega$	-	28	-	nS
Turn-on Rise Time	t_r		-	73	-	nS
Turn-Off Delay Time	$t_{\text{d(off)}}$		-	86	-	nS
Turn-Off Fall Time	t_f		-	33	-	nS
Total Gate Charge	Q_g	$V_{\text{DS}}=40\text{V}, I_{\text{D}}=165\text{A}, V_{\text{GS}}=10\text{V}$	-	102	-	nC
Gate-Source Charge	Q_{gs}		-	36	-	nC
Gate-Drain Charge	Q_{gd}		-	24	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=165\text{A}$	-		1.2	V
Diode Forward Current (Note 2)	I_{S}		-	-	150	A
Reverse Recovery Time	t_{rr}	$T_J = 25^\circ\text{C}, I_F = 165\text{A}$ $di/dt = 100\text{A}/\mu\text{s}$ (Note 3)	-	115	-	nS
Reverse Recovery Charge	Q_{rr}		-	292	-	nC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production
5. EAS condition : $T_J=25^\circ\text{C}, V_{\text{DD}}=40\text{V}, V_G=10\text{V}, L=0.5\text{mH}, R_g=25\Omega$

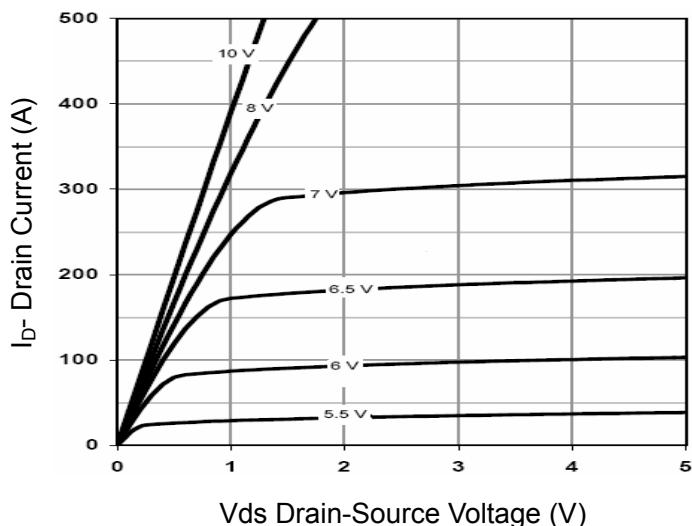


Figure 1 Output Characteristics

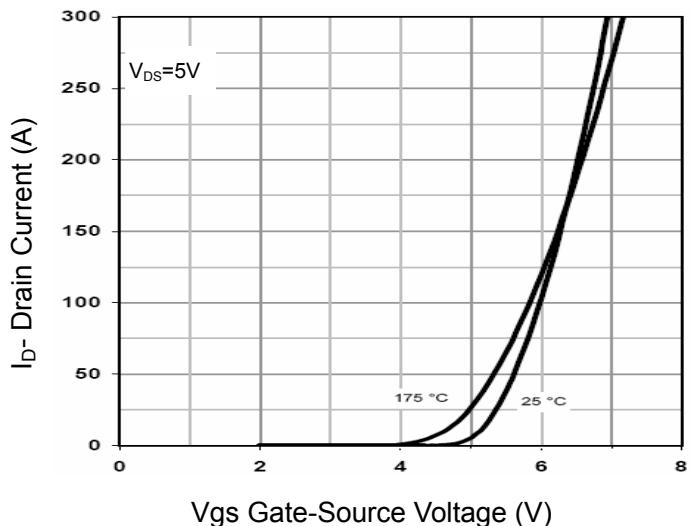


Figure 2 Transfer Characteristics

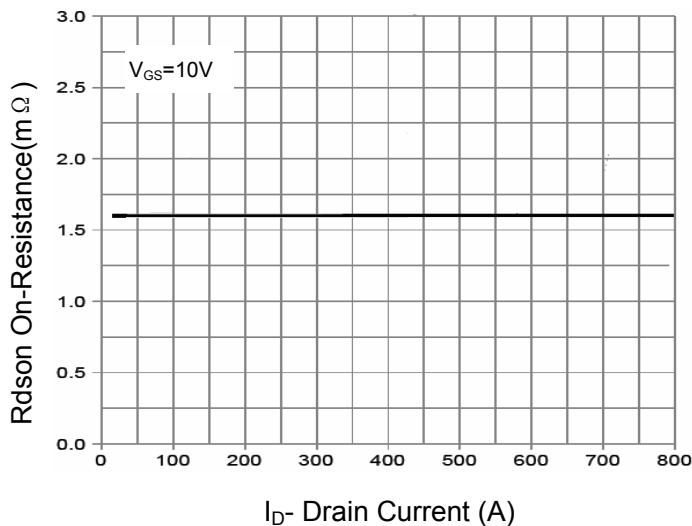


Figure 3 Rdson- Drain Current

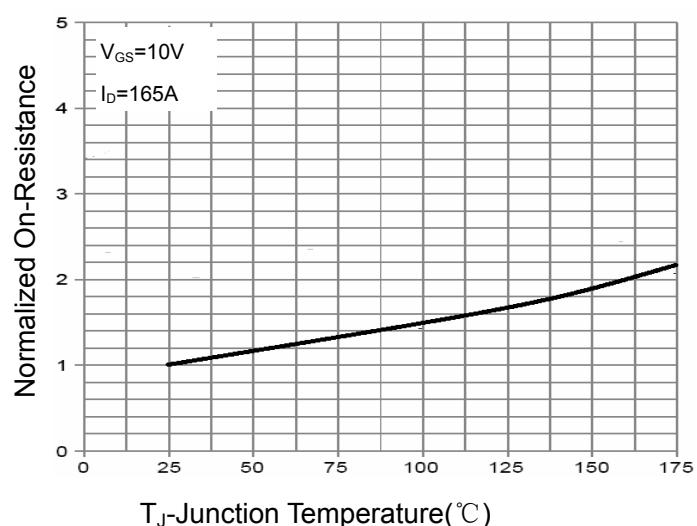


Figure 4 Rdson-Junction Temperature

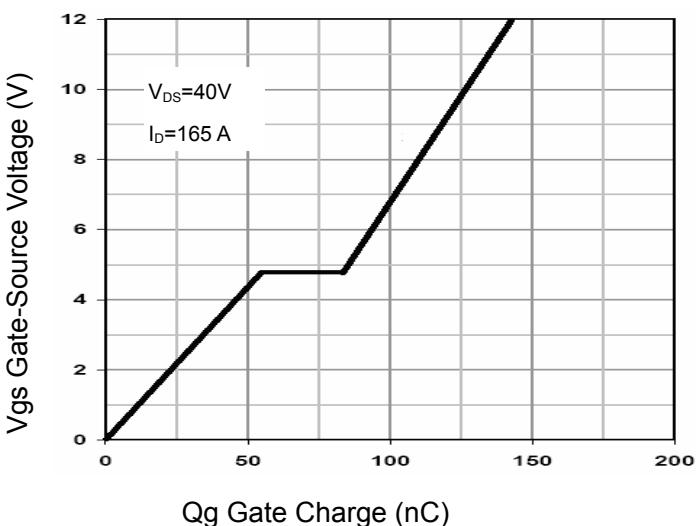


Figure 5 Gate Charge

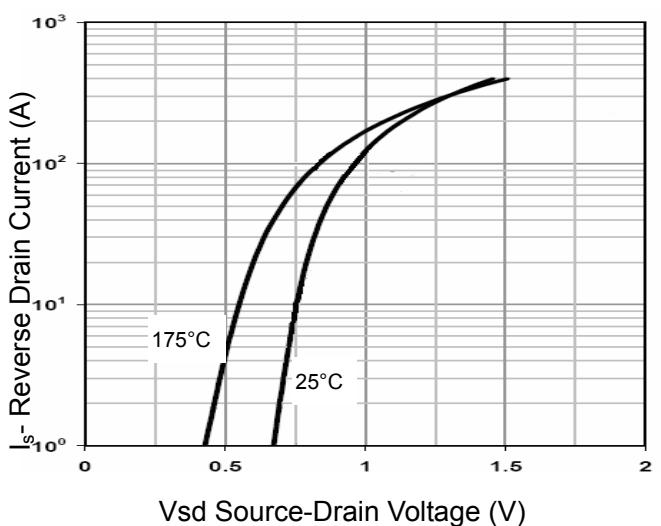


Figure 6 Source- Drain Diode Forward

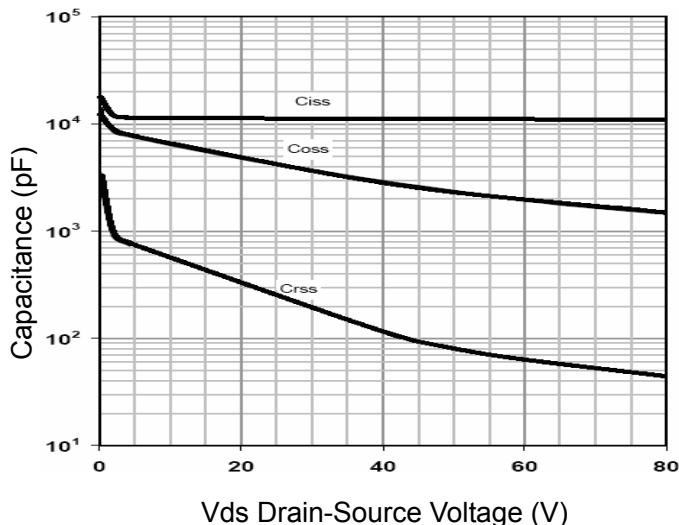


Figure 7 Capacitance vs Vds

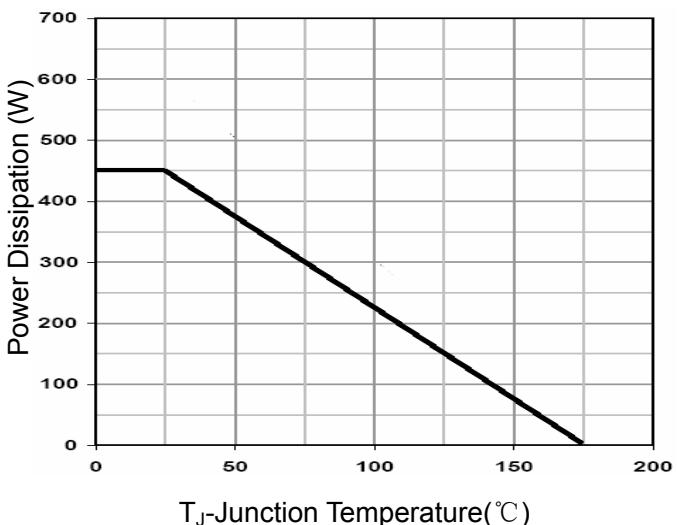


Figure 9 Power De-rating

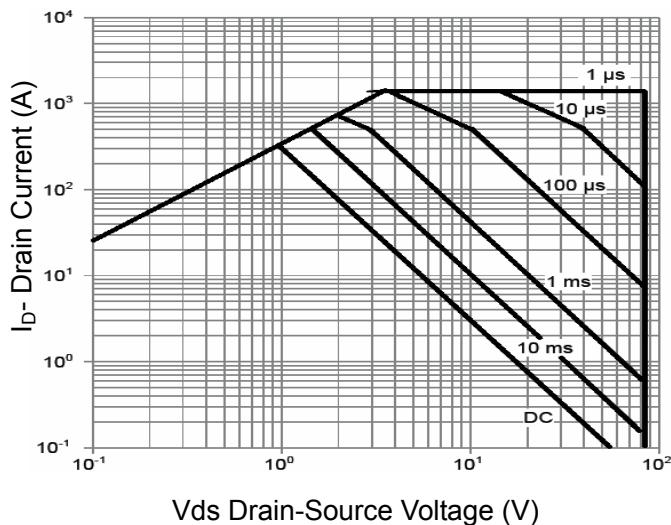


Figure 8 Safe Operation Area

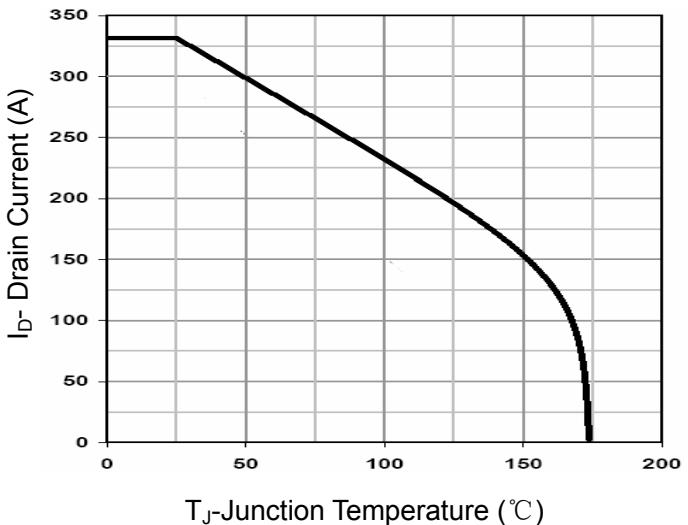


Figure 10 Current De-rating

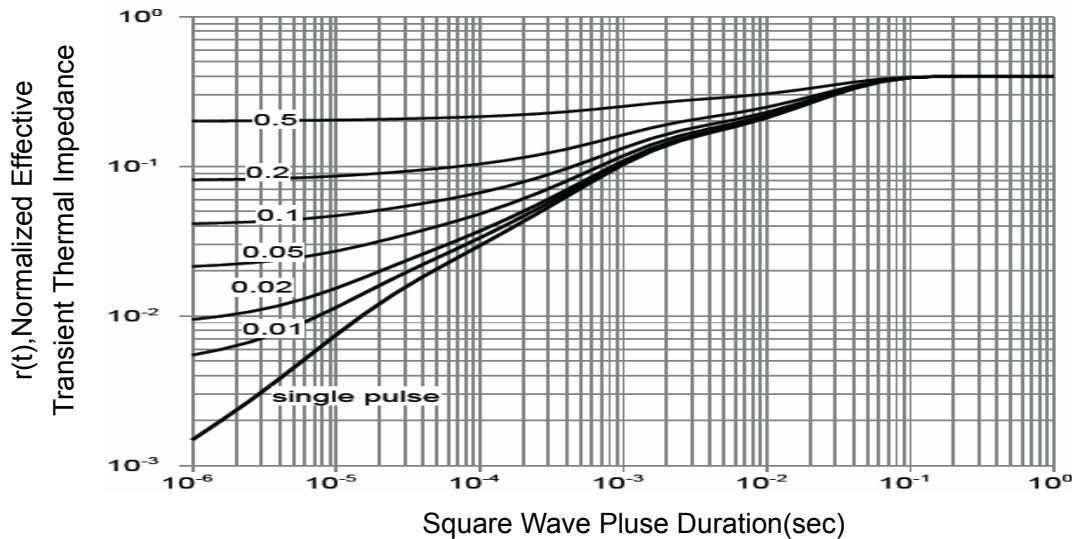
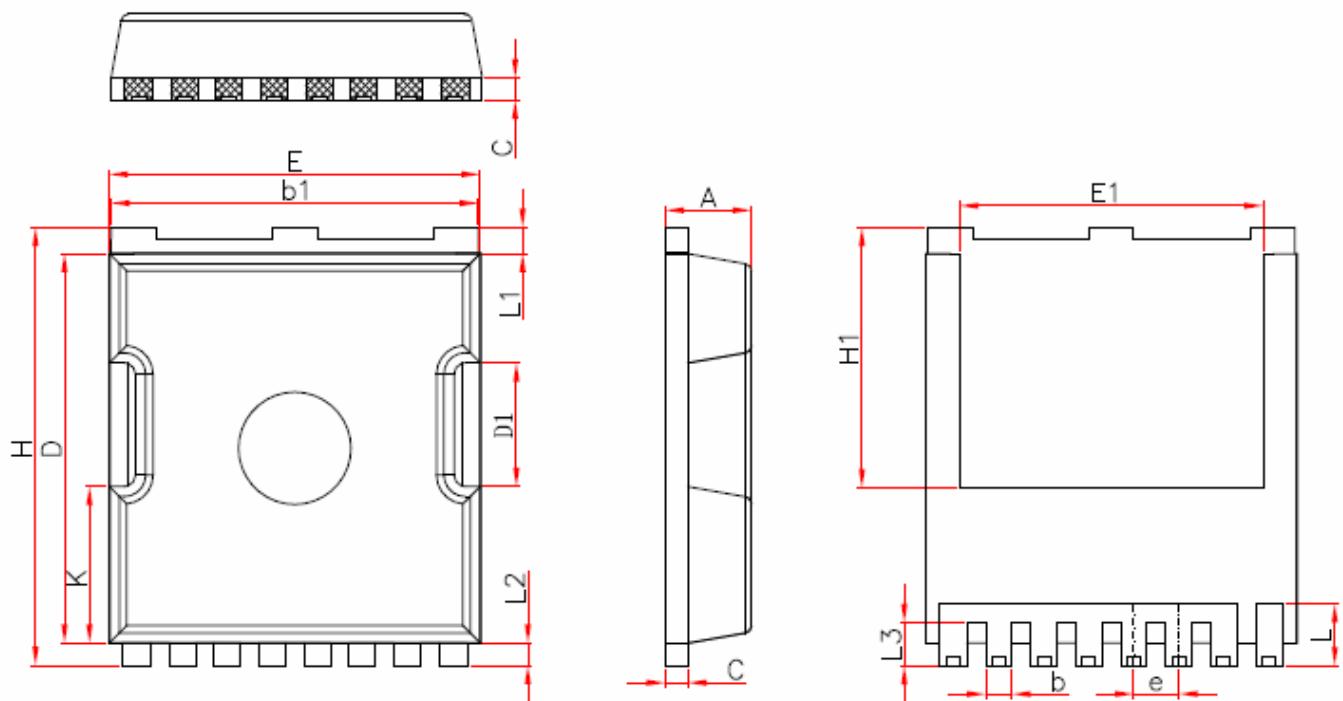


Figure 11 Normalized Maximum Transient Thermal Impedance

TOLL Package Information



Symbol	Millimeters		
	Min.	Nom.	Max.
A	2.20	2.30	2.40
b	0.65	0.75	0.85
b1	9.70	9.80	9.90
C	0.50	0.60	0.70
D	10.30	10.40	10.50
D1	3.15	3.3	3.45
E	9.70	9.90	10.10
E1	8.00	8.10	8.20
e	1.10	1.20	1.30
H	11.6	11.7	11.8
H1	6.85	6.95	7.05
K	4.08	4.18	4.28
L	1.60	1.65	2.10
L1	0.60	0.70	0.80
L2	0.50	0.60	0.70
L3	1.05	1.20	1.30