

HM5031DR Series- High input Voltage Linear Li-Ion Battery Charger

GENERAL DESCRIPTION

The HM5031DR Series is a complete constant current & constant voltage linear charger for single cell lithium-ion batteries. Its DFN package and low external component counts make the HM5031DR Series ideally suited for portable applications. Furthermore, the HM5031DR Series is specifically designed to work within USB power specifications.

No external sense resistor is needed, and no blocking diode is required due to the internal MOSFET architecture. The charge voltage is fixed at 4.2V/4.3V/4.35V/4.4V, and the charge current can be programmed externally with a single resistor. The HM5031DR Series automatically terminates the charge cycle when the charge current drops to 1/10 the programmed value after the final float voltage is reached.

When the input supply (wall adapter or USB supply) is removed, the HM5031DR Series automatically enters a low current state, dropping the battery drain current to less than 2uA. The HM5031DR Series can be put into shutdown mode, reducing the supply current to 50uA.

Under-voltage lockout, automatic recharge and two status pins to indicate charge and charge termination.

FEATURES

- 30V standoff input voltage at VCC pin
- Programmable Charge Current Up to 1000mA
- No MOSFET, Sense Resistor or Blocking Diode Required
- Complete Linear Charger in DFN Package for single Cell Lithium-Ion Batteries
- Charges Single Cell Li-Ion Batteries Directly from USB Port
- Preset 4.2V/4.3V/4.35V/4.4V Charge Voltage with $\pm 1\%$ Accuracy
- Charge Current Monitor Output for Gas Gauging
- Automatic Recharge
- Charge state pairs of output, no battery and fault status display
- CC/10 Charge Termination
- 50uA Supply Current in Shutdown
- 2.9V Trickle Charge Threshold
- Soft-Start Limits Inrush Current
- ESD HBM 2KV
- Available in DFN2x3-8 Package

APPLICATIONS

- Cellular Telephones, PDAs, MP3 /MP4 Players
- E-cigarettes
- Bluetooth
- GPS Applications

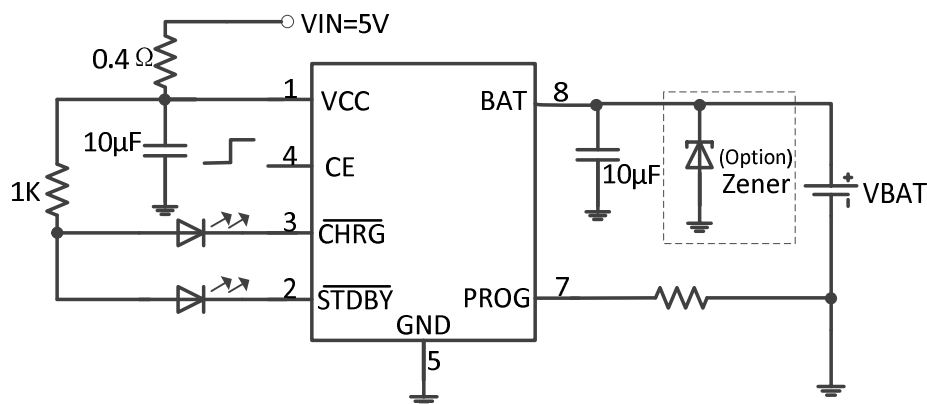


Figure1 .Typical Application Circuit

Note: For HM) \$' %L8F, CE Enable---connect 100K resistor with VCC

For HM) \$' %L8FB, Pin4 NC, Internal enable

ORDERING INFORMATION

PART NUMBER (note1)	VBAT	“CE” PIN Enable Logic	MARK (note2)
HM5031ADR	4.2V	High Enable	31XXAYW
HM5031BDR	4.3V	High Enable	31XXBYW
HM5031CDR	4.35V	High Enable	31XXCYW
HM5031DDR	4.4V	High Enable	31XXDYW
HM5031ADRN	4.2V	NC	31XXANYW
HM5031BDRN	4.3V	NC	31XXBNYW
HM5031CDRN	4.35V	NC	31XXCNYW
HM5031DDRN	4.4V	NC	31XXDNYW

Note1: In HM5031XDR、HM5031XDRN description, X may be A or B or C or D

Note2: “YW” is manufacture date code, “Y” means the year, “W” means the week

PIN CONFIGURATION

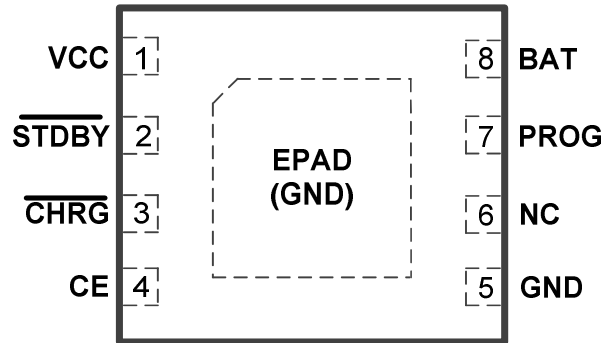


Figure 2. PIN Configuration (DFN2x3-8)

PIN DESCRIPTION

PIN NUMBER	PIN NAME	PIN DESCRIPTION
1,	VCC	Positive Input Supply Voltage.
2	$\overline{\text{STDBY}}$	The completion of battery charging instructions side.
3	$\overline{\text{CHRG}}$	Open-Drain Charge Status Output.
4	CE	Chip enable input. HM5031DRX's CE is High Enable, HM5031DRXN's Pin4 NC: No Connection
5	GND	Ground
6	NC	No Connection.
7	PROG	Charge Current Program, Charge Current Monitor and Shut down Pin.
8	BAT	Charge Current Output.
9	EPAD	Ground and EPAD

ABSOLUTE MAXIMUM RATINGS

(Note: Do not exceed these limits to prevent damage to the device. Exposure to absolute maximum rating conditions for long periods may affect device reliability.)

PARAMETER	VALUE	UNIT
Input Supply Voltage VCC	VSS-0.3 ~ VSS+30	V
PROG pin Voltage Vprog、CE pin Voltage Ven	VSS-0.3 ~ 10	V
BAT pin Voltage Vbat	VSS-0.3 ~ 11	V
CHRG、STDBY pin Voltage Vchrg	VSS-0.3 ~ 10	V
BAT pin Current bat	1.4	A
PROG pin Current Iprog	1.4	mA
Operating Ambient Temperature	-40 to 85	°C
Maximum Junction Temperature	150	°C
Storage Temperature	-55 to 150	°C
Lead Temperature (Soldering, 10 sec)	260	°C

ELECTRICAL CHARACTERISTICS

(VCC = 5.0V, Vbat=3.5V TA= 25°C unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Voltage Tolerance			4.25		26	V
Input over Voltage protection	V _{OVP}			6.8		V
The Hysteresis Voltage of V _{OVP}				0.4		V
Input Voltage Range			4.25		6	V
Input supply current	I _{CC}	Charge mode, R _{PROG} =10K		350	2000	uA
		Standby mode, Vbat=4.3V		150	500	uA
		Shutdown mode(V _{CC} <V _{bat} or V _{CC} <V _{uv})		50	200	uA
BAT pin Current	I _{BAT}	R _{PROG} =200k, Current mode	4	5	6	mA
		R _{PROG} =100k, Current mode	8	9.5	11	mA
		R _{PROG} =10k, Current mode	85	95	105	mA
		R _{PROG} =2k, Current mode	425	475	525	mA
		R _{PROG} =1k, Current mode	850	950	1050	mA
		Standby mode, Vbat=4.3V	0	-2.5	-6	uA
		Shutdown mode		1	2.5	uA
		Sleep mode, V _{CC} =0V		0.3	2.5	uA

Regulated Charge Voltage(HM5031DRA/AN)	Vfloat	0°C ≤ TA ≤ 85°C, Icharge = 40mA	4.158	4.2	4.242	V
Regulated Charge Voltage(HM5031DR6B/BN)			4.257	4.3	4.343	V
Regulated Charge Voltage(HM5031DRC/CN)			4.307	4.35	4.394	V
Regulated Charge Voltage(HM5031DRD/DN)			4.356	4.4	4.444	V
PROG pin Voltage	Vprog	R _{PROG} = 1k, Current mode	0.93	1.0	1.07	V
Trickle charge current	Itrikl	Vbat < Vtrikl, Rprog=1k	85	95	105	mA
Trickle charge Threshold Voltage	Vtrikl	R _{PROG} = 10K, Vbat Rising	2.7	2.9	3.1	V
Trickle voltage hysteresis voltage	Vtrhys	R _{PROG} = 10K	0.08	0.12	0.16	V
Recharge Battery threshold Voltage	Δ Vrecg	V _{FLOAT} - V _{RECHRG}		105	150	mV
Under voltage Lockout Threshold	VUVLO	BAT = 3.5V, IN Rising	3.05	3.4	3.75	V
VCC under voltage lockout hysteresis	Vuvhys			600		mV
CHRG pin Output low voltage	Vchrg	Ichrg=5mA		1.2	2	V
STDBY pin Output low voltage	Vstdby	Istdby=5mA		1.2	2	V
Enable Threshold		VCC=4.25V~6.5V	0.3	1	1.5	V
Enable Leakage Current			-0.1		+0.1	uA
Thermal Shutdown Temperature	T _{SHDN}			140		°C
Thermal Shutdown Hysteresis	ΔT _{SHDN}			20		°C

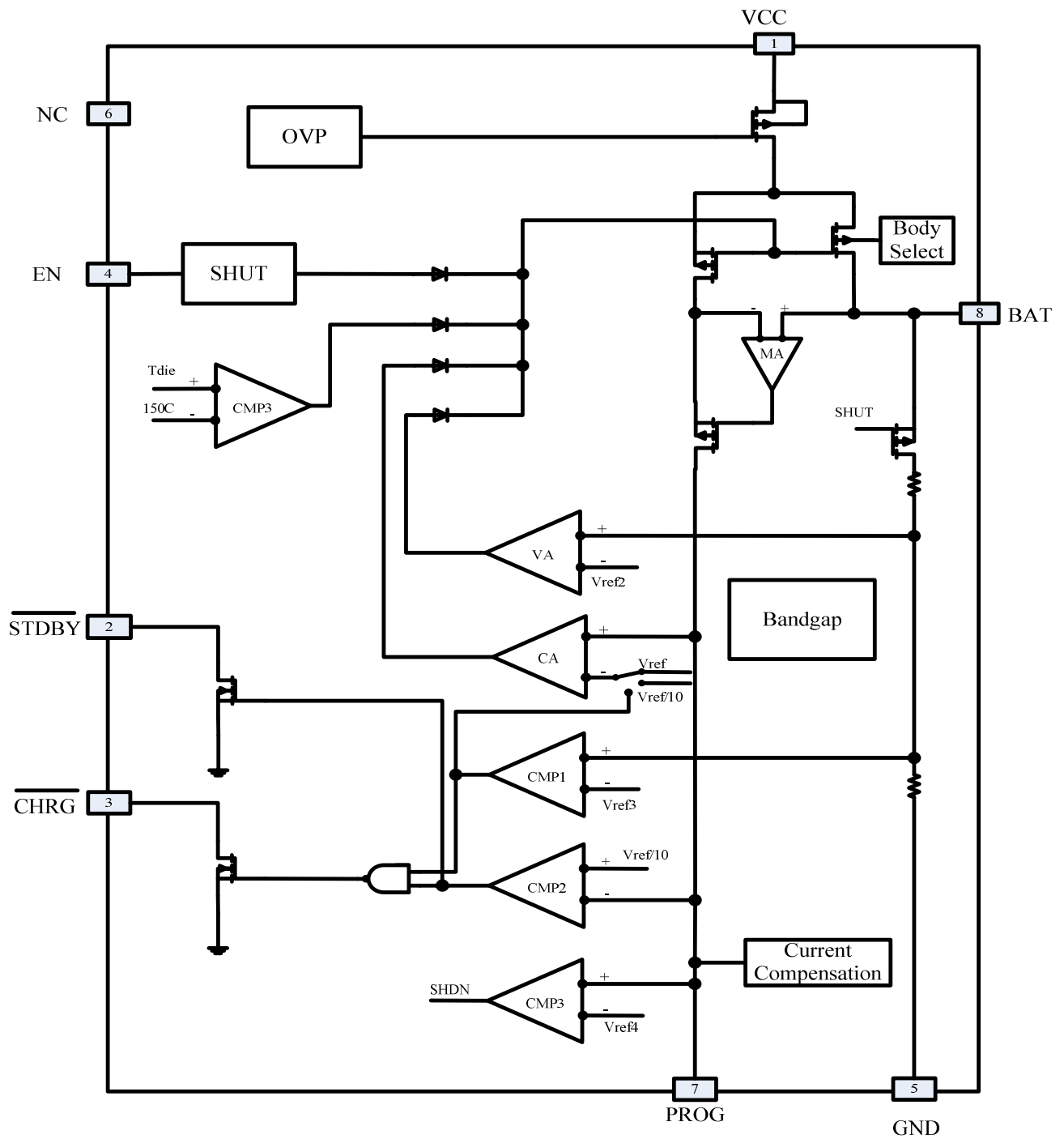


Figure3 Block Diagram

PIN FUNCTION

VCC (PIN 1): Positive Input Supply Voltage.

$\overline{\text{STDBY}}$ (PIN 2): The completion of battery charging instructions side.

When the battery charge is complete, $\overline{\text{STDBY}}$ pulled low by internal switches, indicating the completion of charging. In addition, $\overline{\text{STDBY}}$ pin will be in a high-impedance state.

$\overline{\text{CHRG}}$ (PIN 3): Open-Drain Charge Status Output.

When the battery is charging, the $\overline{\text{CHRG}}$ pin is pulled low by an internal N-channel MOSFET. When the charge cycle is completed, $\overline{\text{CHRG}}$ pin will be in a high-impedance state.

CE (PIN 4): Chip enable input.

HM5031DRX High Enable, HM5031DRXN connect "NC".

CE pin can be TTL or CMOS level-level driver.

CE pin is low voltage tolerance pin and can't connect with VCC directly.

GND (PIN 5): Ground.

NC (PIN 6): No Connection.

PROG (PIN 7): Charge Current Program, Charge Current Monitor and Shutdown Pin.

The charge current is programmed by connecting a 1% resistor, RPROG, to ground. When charging in constant-current mode, this pin serves to 1V. In all modes, the voltage on this pin can be used to measure the charge current using the following formula:

$$\text{IBAT} = (\text{VPROG}/\text{RPROG}) \times 950.$$

The PROG pin can also be used to shut down the charger. Disconnecting the program resistor from ground allows a compensated current to pull the PROG pin high. When it reaches the 1.21V shutdown threshold voltage, the charger enters shutdown mode, charging stops and the input supply current drops to 50uA.

Reconnecting RPROG to ground will return the charger to normal operation.

BAT (PIN 8): Charge Current Output.

Provides charge current to the battery and regulates the final float voltage to 4.2V/4.3V/4.35V/4.4V. An internal precision resistor divider from this pin sets the float voltage which is disconnected in shutdown mode.

OPERATION

CHARGE CYCLE OVERVIEW

When a battery charge cycle begins, the battery charger first determines if the battery is deeply discharged. If the battery voltage is below V_{trikl} , typically 2.9V, an automatic trickle charge feature sets the battery charge current to 10% of the full-scale value.

Once the battery voltage is above 2.9V, the battery charger begins charging in constant-current mode. When the battery voltage approaches the 4.2V/4.3V/4.35V/4.4V required to maintain a full charge, otherwise known as the float voltage, the charge current begins to decrease as the HM5031DR Series switches into constant-voltage mode.

TRICKLE CHARGE AND CHARGE TERMINATION

Any time the battery voltage is below V_{trikl} , the charger goes into trickle charge mode and reduces the charge current to 10% of the full-scale current. If for any reason the battery voltage rises above V_{trikl} , the charger will resume charging. When the battery is charging, the \overline{CHRG} pin is pulled low by an internal N-channel MOSFET. When the charge cycle is completed, \overline{CHRG} pin will be in a high-impedance state.

When the battery voltage reach the regulated charge voltage, typically 4.2V/4.3V/4.35V/4.4V and the charger current is below 10% of fast charge setting current, charging of the battery will discontinue and no more current will be delivered.

At this time, \overline{STDBY} pulled low by internal switches, indicating the completion of charging. In addition, \overline{STDBY} pin will be in a high-impedance state

CHARGE STATUS INDICATION

The \overline{CHRG} pin and the \overline{STDBY} pin indicates the status of the battery charger..Four possible states are represented by charging, complete, fault, floating

Table 1 illustrates the four possible states of them when the battery charger is active.

Charge Status	\overline{CHRG} LED	\overline{STDBY} LED
Charging	ON	OFF
Complete	OFF	ON
Fault UVLO	OFF	OFF
Floating BAT Pin C=10uF and Battery unavailable	\overline{STDBY} LED ON, \overline{CHRG} LED TWINKLE(About 1~4s)	

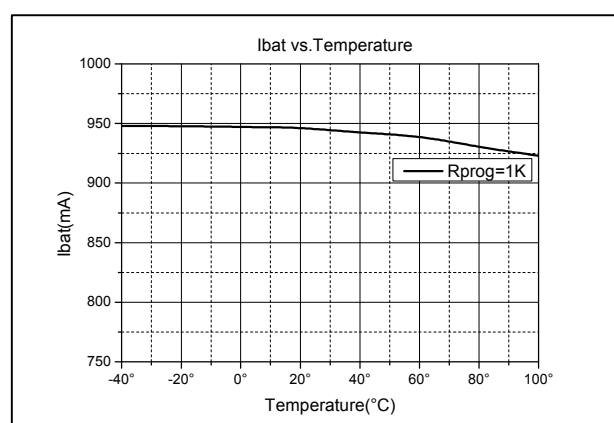
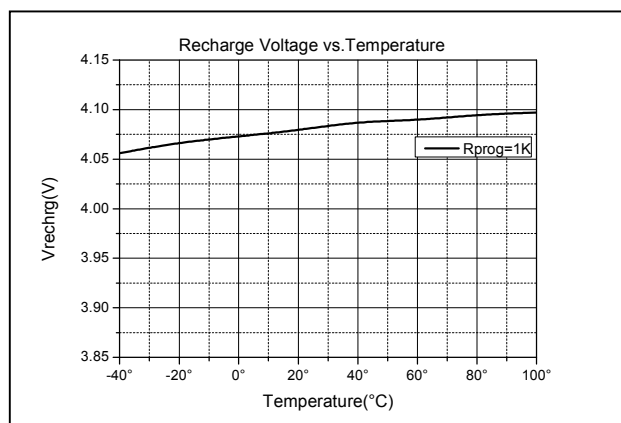
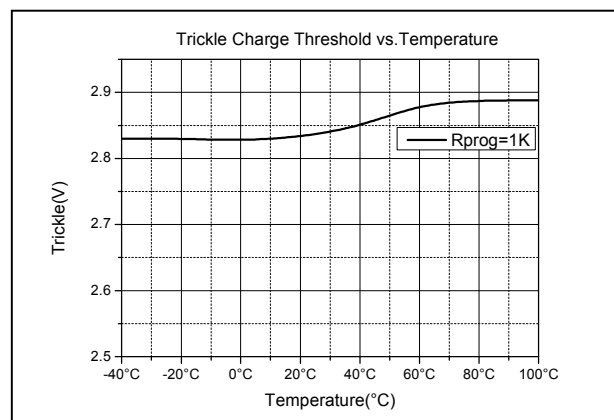
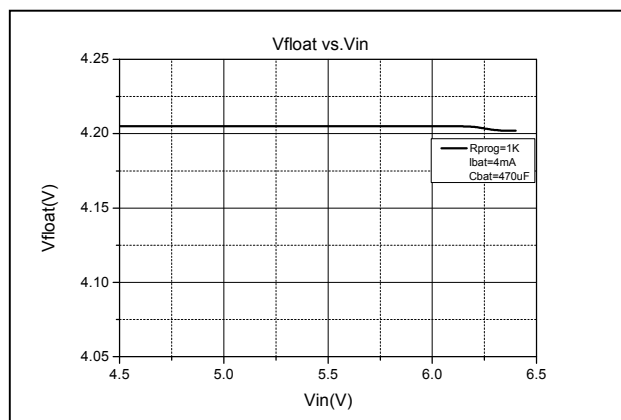
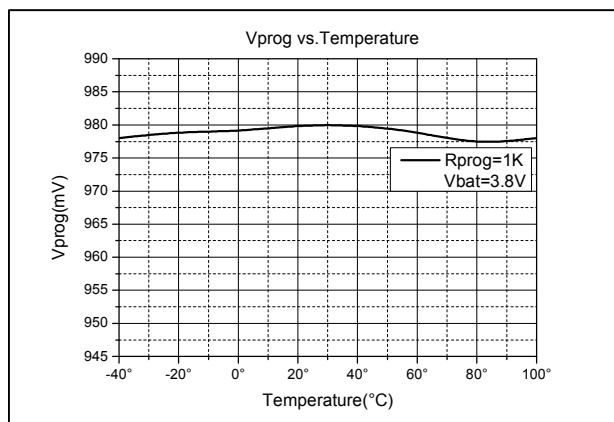
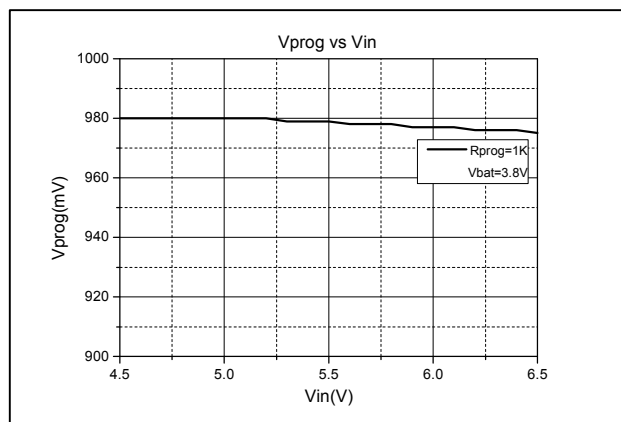
AUTOMATIC RECHARGE

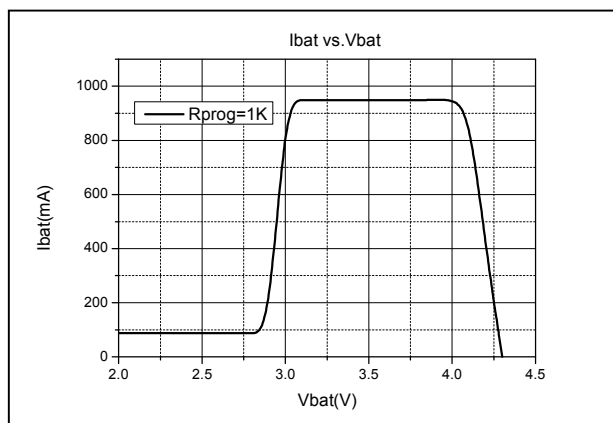
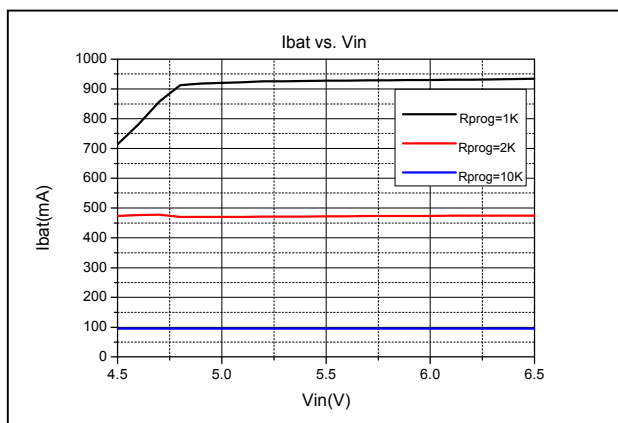
After the battery charger terminates, it will remain off, drawing only microamperes of current from the battery. If the portable product remains in this state long enough, the battery will eventually self discharge. To ensure that the battery is always topped off, a charge cycle will automatically begin when the battery voltage falls below V_{RECHRG} (typically 4.08V).

SHUT DOWN

When the Voltage of CE pin is disable,HM5031DR SERIES will shut down. the shut down current will below 2.5uA.

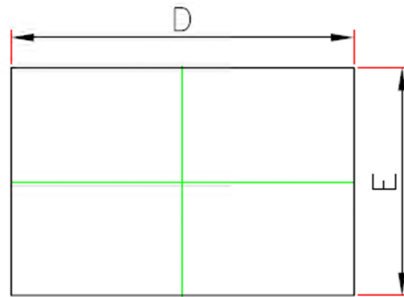
TYPICAL PERFORMANCE CHARACTERISTICS



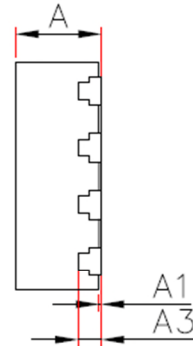


PACKAGE OUTLINE

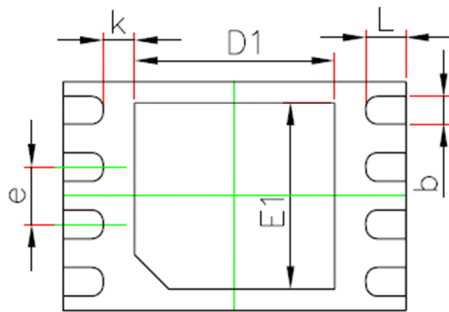
DFN2X3-8 PACKAGE OUTLINE AND DIMENSIONS



TOP VIEW



SIDE VIEW



BOTTOM VIEW

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN.	MAX.	MIN.	MAX.
A	0.700	0.800	0.028	0.031
A1	0.000	0.050	0.000	0.002
A3	0.203REF.		0.008REF.	
D	2.950	3.050	0.116	0.120
E	1.950	2.050	0.077	0.081
D1	1.650	1.850	0.065	0.073
E1	1.530	1.730	0.060	0.068
b	0.200	0.300	0.008	0.012
e	0.500BSC.		0.020BSC.	
k	0.275REF		0.011REF	
L	0.300	0.400	0.012	0.016